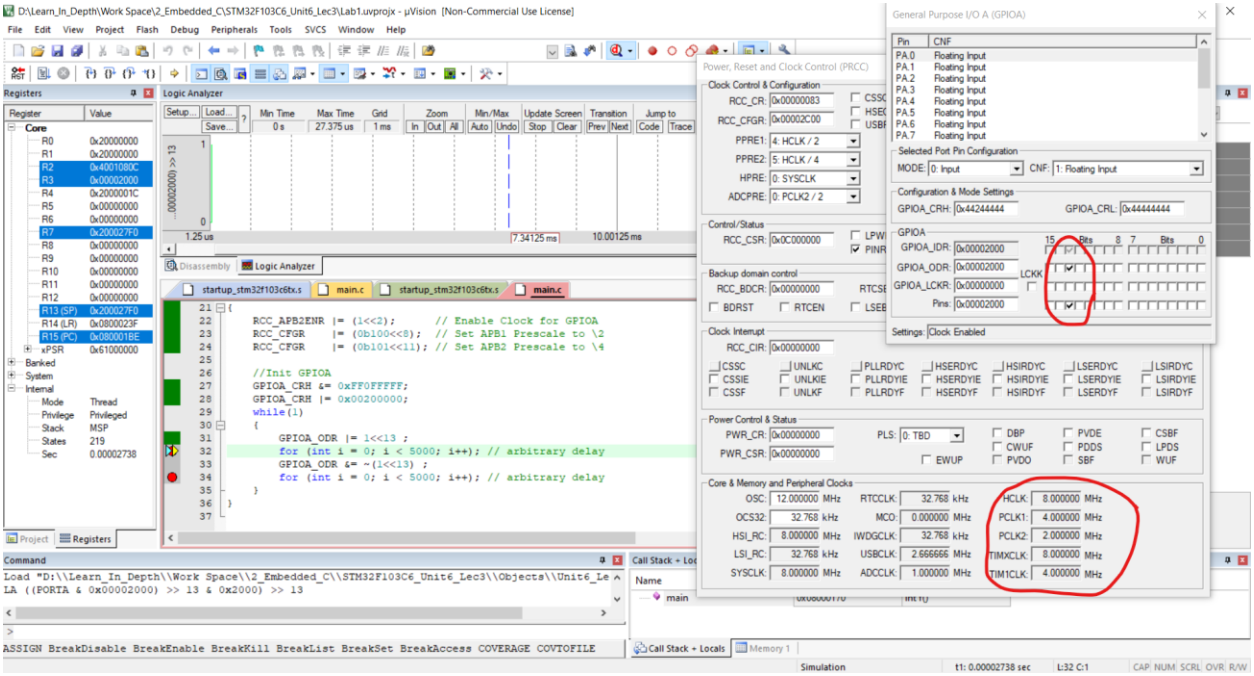
LAB2

In This Lab we changed the clocks of the Buses through RCC .

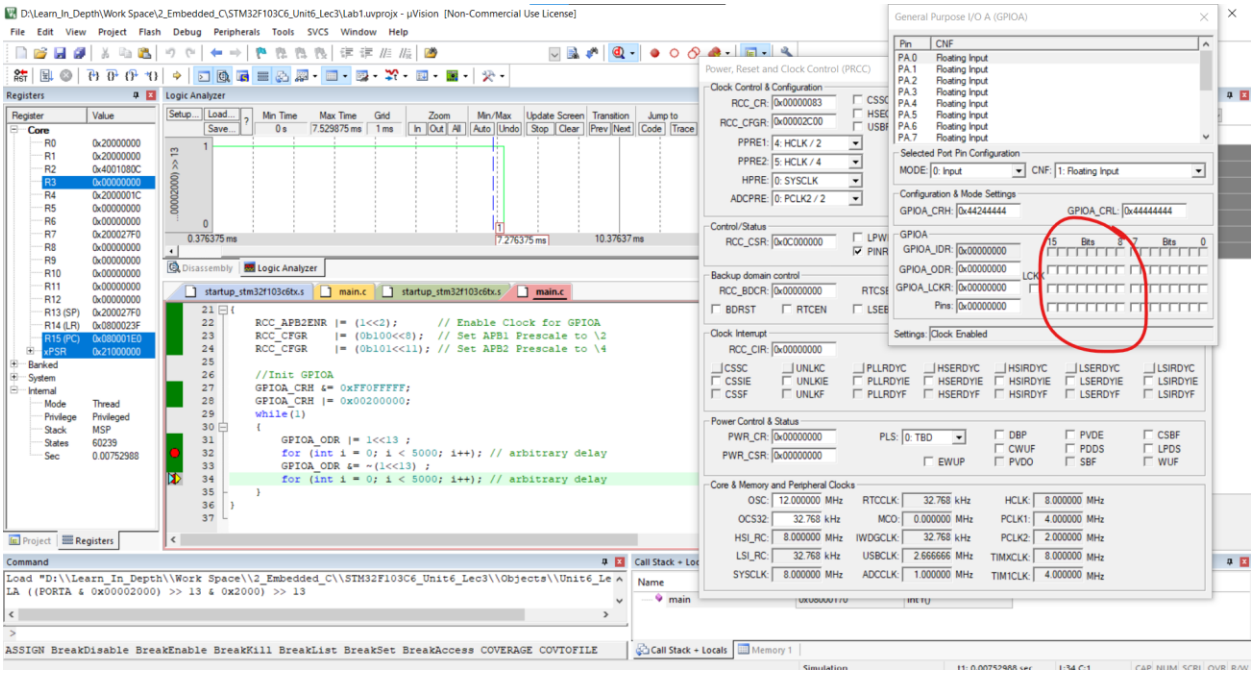
Led on



Note :-

All clocks in (core &memory and peripheral clocks) .

Led off


D:\Learn_In_Depth\Work Space\2_Embedded_C\STM32F103C6_Unit6_Lec3\Lab1.uvprojx - µVision [Non-Commercial Use License]
File Edit View Project Flash Debug Peripherals Tools SVCS Window Help
Registers
Logic Analyzer
General Purpose I/O A (GPIOA)
Power, Reset and Clock Control (PRCC)
startup_stm32f103c6tx.s
main.c
RCC_APB2ENR |= (1<<2); // Enable Clock for GPIOA
RCC_CFGR |= (0b100<<8); // Set APB1 Prescale to \2
RCC_CFGR |= (0b101<<11); // Set APB2 Prescale to \4
//Init GPIOA
GPIOA_CRH &= 0xFF0FFFFF;
GPIOA_CRH |= 0x00200000;
while(1)
GPIOA_ODR |= 1<<13 ;
for (int i = 0; i < 5000; i++); // arbitrary delay
GPIOA_ODR &= ~(1<<13) ;
for (int i = 0; i < 5000; i++); // arbitrary delay
Command
ASSIGN BreakDisable BreakEnable BreakKill BreakList BreakSet BreakAccess COVERAGE COVTOFILE