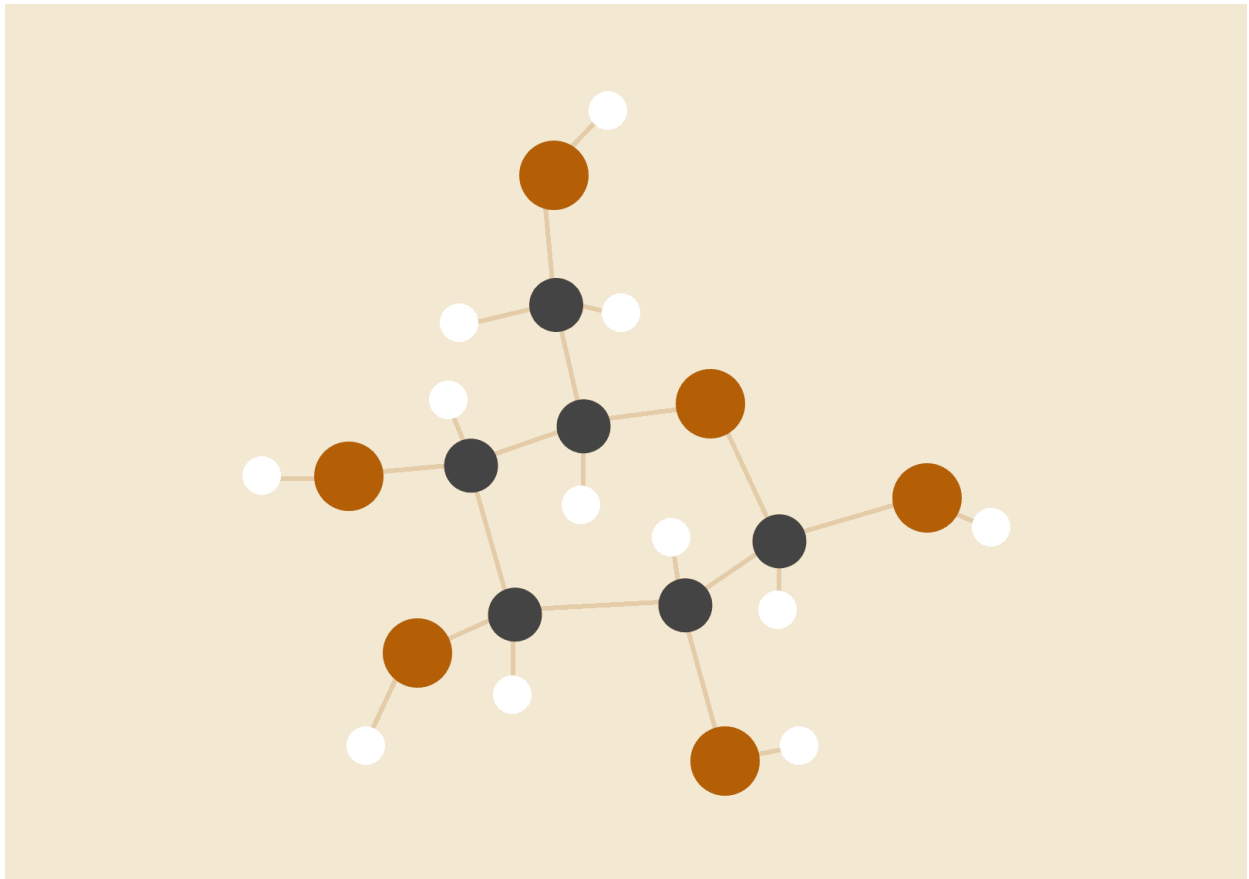


IMPLEMENTATION OF EDF SCHEDULER IN FREERTOS

RTOS MC GRADUATION PROJECT



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DESCRIPTION

In this project I'll be implementing the EDF scheduler from “ **Implementation and Test of EDF and LLREF Schedulers in FreeRTOS** ” written by “ **EnricoCarraro** ” while making the necessary changes. Then the implementation will be tested with a chosen set of tasks, the tests will be manual analytics, offline simulator, and run-time analysis.

IMPLEMENTING EDF FROM THE THESIS

To start things off, all the changes will be done in task.c file and will be guarded with configUSEEDFSCHEDULER that will be set to 1 whenever the scheduler needs to be used.

- The new Ready List is declared **xReadyTasksListEDF** is a simple list structure.

```
1  /* The new Ready List */
2  #if ( configUSE_EDF_SCHEDULER == 1 )
3
4      PRIVILEGED_DATA static List_t xReadyTasksListEDF;          /*< Ready tasks ordered by their deadline. */
5
6  #endif
```

- Adding the initialization of **xReadyTasksListEDF** in **prvInitialiseTaskLists()**.

```
1  static void prvInitialiseTaskLists( void )
2  {
3      ....
4
5      /* Initializing the new Ready List */
6      #if ( configUSE_EDF_SCHEDULER == 1 )
7          {
8              vListInitialise( &xReadyTasksListEDF );
9          }
10     #endif /* configUSE_EDF_SCHEDULER */
11
12     ....
13
14 }
```

- Adding tasks to the Ready List according to their deadlines.

```

1  /*
2  * Place the task represented by pxTCB into the appropriate ready list for
3  * the task. It is inserted at the end of the list.
4  */
5
6  #if (configUSE_EDF_SCHEDULER == 0)
7      #define prvAddTaskToReadyList( pxTCB )
8          ....
9          listINSERT_END( &( pxReadyTasksLists[ ( pxTCB )->uxPriority ] ), &( ( pxTCB )->xStateListItem ) );
10         ....
11
12 #else /* configUSE_EDF_SCHEDULER == 1 */
13     #define prvAddTaskToReadyList( pxTCB ) /*xStateListItem must contain the deadline value */ \
14         vListInsert( &(xReadyTasksListEDF), &( ( pxTCB )->xStateListItem ) )
15 #endif

```

- A new period variable is added in the **tskTaskControlBlock**.

```

1  typedef struct tskTaskControlBlock      /* Task Control Block Structure */
2  {
3      ....
4
5      /* The period of a task */
6      #if ( configUSE_EDF_SCHEDULER == 1 )
7
8          TickType_t xTaskPeriod;          /*< Stores the period of the task in tick. > */
9
10     #endif
11 } tskTCB;

```

- A new initialization task method is created **xTaskPeriodicCreate()**.

```

1  /* New task method with the period as a parameter */
2  BaseType_t xTaskPeriodicCreate(
3
4      TaskFunction_t pxTaskCode,
5      const char * const pcName,
6      const configSTACK_DEPTH_TYPE usStackDepth,
7      void * const pvParameters,
8      UBaseType_t uxPriority,
9      TaskHandle_t * const pxCreatedTask,
10     TickType_t period )

```

- Creating the idle task with the new task method in **vTaskStartScheduler()**.

```

1 void vTaskStartScheduler( void )
2 {
3     ....
4
5     #if (configUSE_EDF_SCHEDULER == 1)
6     {
7         /* Creating the idle task with the new task method */
8
9         TickType_t initIDLEPeriod = 100; /* The idle task period should be longer than all the other tasks */
10
11         xReturn = xTaskPeriodicCreate( prvIdleTask,
12                                       configIDLE_TASK_NAME,
13                                       configMINIMAL_STACK_SIZE,
14                                       (void * ) NULL,
15                                       ( tskIDLE_PRIORITY | portPRIVILEGE_BIT ),
16                                       &xIdleTaskHandle,
17                                       initIDLEPeriod );
18     }
19
20     #else /* configUSE_EDF_SCHEDULER == 0 */
21     {
22         /* The Idle task is being created with the default method */
23         xReturn = xTaskCreate(...);
24     }
25     #endif /* configUSE_EDF_SCHEDULER */

```

- **taskSELECTHIGHESTPRIORITYTASK()** method in **vTaskSwitchContext** is replaced in order to assign to **pxCurrentTCB** the task at the first place of the new Ready List.

```

1 void vTaskSwitchContext( void )
2 {
3     ....
4     /* Select a new task to run */
5
6     #if (configUSE_EDF_SCHEDULER == 0) /* Default method the selection is according to the task priority */
7     {
8         taskSELECT_HIGHEST_PRIORITY_TASK();
9     }
10
11     #else /* configUSE_EDF_SCHEDULER == 1 */ /* EDF method selecting the task with the earliest deadline */
12     {
13         pxCurrentTCB = (TCB_t * ) listGET_OWNER_OF_HEAD_ENTRY( &(xReadyTasksListEDF ) );
14     }
15
16     #endif
17
18     ....
19
20 }

```

UPDATING THE IMPLEMENTATION

There are three main changes that have to be made to the implementation in order to have a fully functioning EDF Scheduler.

- The deadline of the IDLE task should be always updating, to prevent the idle task from running while there are ready tasks .
- The new task deadline should be calculated before adding the task to the Ready List.
- Whenever a task is added to the ready list a context switching is required (The scheduler should be called)

All the previous updates were done in **xTaskIncrementTick()** because it looks like the optimal place to do all these changes.

With each tick it looks for a ready task, if there is any then it will calculate it's new deadline and will be added to the ready list while also updating the IDLE task deadline. Then a switch context will occur.

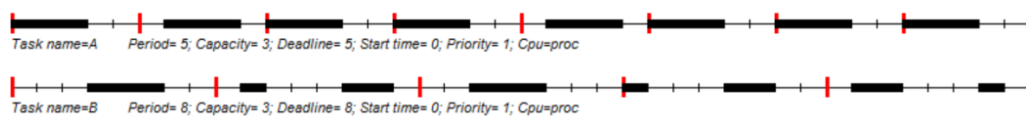
```
1 BaseType_t xTaskIncrementTick( void )
2 {
3     ....
4
5     #if ( configUSE_EDF_SCHEDULER == 1 )
6
7         /* Calculating the new task deadline */
8         listSET_LIST_ITEM_VALUE( &(amp; (pxTCB)->xStateListItem ), (pxTCB)->xTaskPeriod + xTickCount);
9
10        /* Updating the IDLE task deadline */
11        listSET_LIST_ITEM_VALUE( &(amp; (xIdleTaskHandle)->xStateListItem ), (xIdleTaskHandle)->xTaskPeriod + xTickCount);
12
13    #endif
14
15    /* Adding the task to the Ready List */
16    prvAddTaskToReadyList( pxTCB );
17
18    #if ( configUSE_EDF_SCHEDULER == 1 )
19
20        /* Whenever a task is added to the Ready List the Scheduler should run and context switching should occur */
21        xSwitchRequired = pdTRUE;
22
23    #endif
24
25    ....
26
27 }
28
29 return xSwitchRequired;
```

TASKS

TASKS	PERIODICITY	DEADLINE	EXECUTION TIME	PRIORITY
TASK 1	5 ms	5 ms	3 ms	1
TASK 2	8 ms	8 ms	3 ms	1

Testing the EDF Scheduler will be through these two tasks which were taken from the thesis.

EDF scheduling of task 1 (T=5, D=5, C=3) and task 2 (T=8, D=8, C=3)



The detection of a perfectly working EDF will be at tick = 10 when Task 2 is running but Task 1 becomes ready and the deadline of TASK 1 is 15 while TASK 2 is 16, so the scheduler should pick TASK 1 because it has the earliest deadline.

HYPERPERIOD

Hyperperiod = LCM(Pi) which is 40 ms

URM CALCULATIONS

$$U = \sum_{i=1}^n \frac{C_i}{P_i} \leq n(2^{\frac{1}{n}} - 1)$$

$$U = (3 / 5) + (3 / 8) = 0.6 + 0.375$$
$$U = 0.975$$

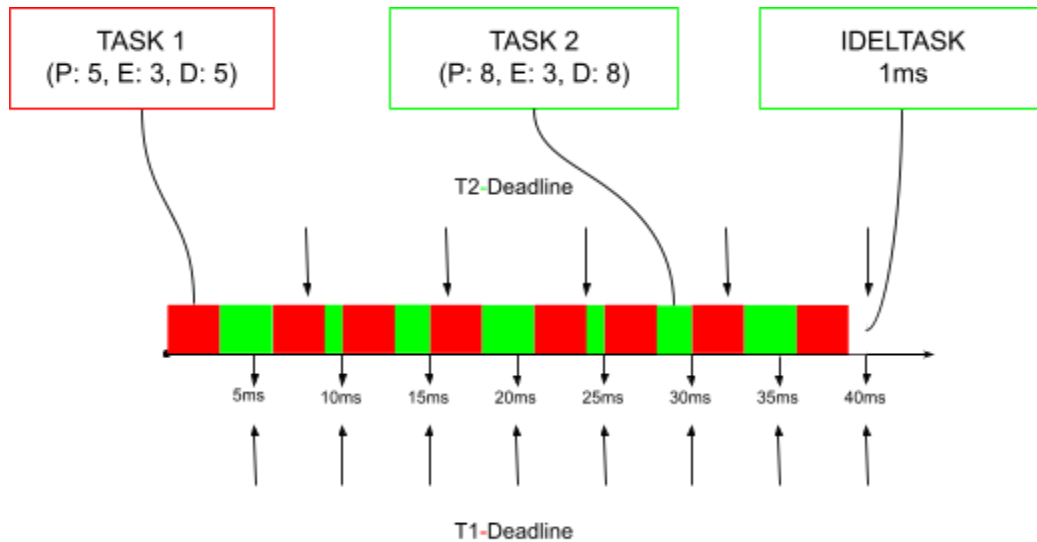
$$\text{Urm} = 2(2^{\frac{1}{2}} - 1) = 0.83$$

$$U > U_{rm}$$

System guaranteed **NOT** schedulable in a **Rate-Monotonic Scheduler**.

CPU LOAD / SYSTEM SCHEDULABILITY MANUALLY

Applying the previous calculations we can plot out the tasks on this 40 ms hyperperiod timeline (Taking in consideration it's a EDF Scheduler):



CPU manual calculations:

$$R \text{ (Busy Time)} = (8 \times 3) + (5 \times 3) \\ = 39 \text{ ms}$$

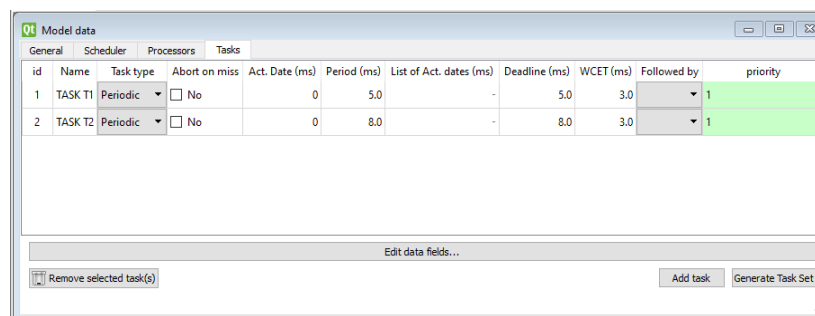
$$C \text{ (Total Time)} = 40 \text{ ms}$$

$$\text{CPU Load} = R/C = 39 / 40 = 97.5\%$$

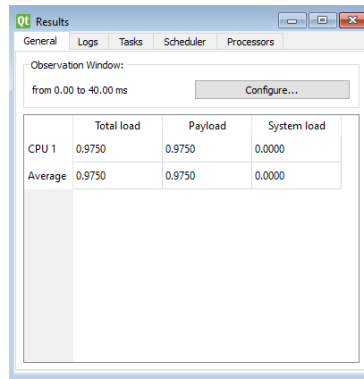
According to the timeline and cpu load calculations the system is loaded but barely schedulable. Every task comes at the right time and no task misses the deadline.

CPU LOAD / SYSTEM SCHEDULABILITY ON SIMULATION

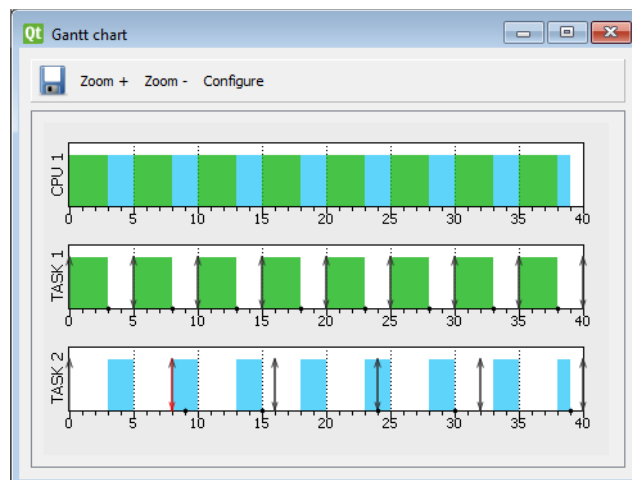
Set the Tasks with the periodicity, deadline, execution time, and priority.



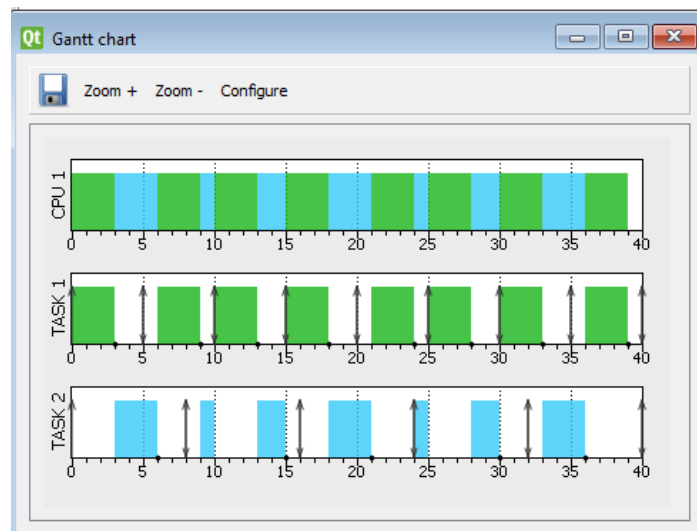
The CPU load is as it was manually calculated and that confirms the calculations.



Using the **Rate-Monotonic Scheduler** we can clearly see that it is **NOT** schedulable as we calculated with URM. TASK 2 missed the first deadline at tick = 8.



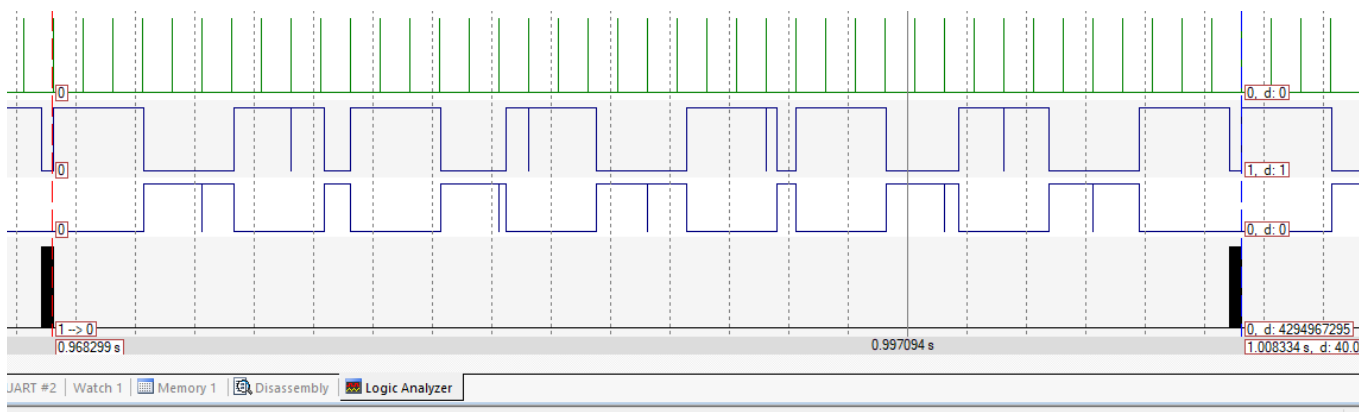
On the other hand using the **EDF Scheduler**, the simulation timeline is identical to the manually drawn timeline, and confirms that the system **IS** schedulable and no task missed the deadline.



RUN-TIME ANALYSIS

To confirm that the implemented EDF Scheduler is working as it is intended, the run time analysis should be identical to the previous EDF simulation output.

With the help of trace hooks, we can clearly see in this 40 ms hyperperiod that the implemented EDF scheduler is working exactly as it is supposed to. Also the cpu load is identical to the manually calculated and simulated results.



Watch 1		
Name	Value	Type
task_1_total_time	0x000541BC	int
task_2_total_time	0x00034B0C	int
system_time	0x0008A746	int
cpu_load	98	int
<Enter expression>		

Command | Call Stack + Locals | UART #2 | Watch 1 | Memory 1 | Disassembly | Logic Analyzer

BOUNDS:

To modify FreeRTOS **vTaskGetRunTimeStats** function to read the new **ReadyListEDF** and gather the required information about the tasks.

Following the **vTaskGetRunTimeStats** we can see that it calls **uxTaskGetSystemState** to get info about the tasks. The old Ready List was a bunch of lists in a list (each one for a certain priority)

So this is the part where it needs to be modified because the new Ready List is just one list with no more lists inside it.

```
1  UBaseType_t uxTaskGetSystemState( TaskStatus_t * const pxTaskStatusArray,  
2                                     const UBaseType_t uxArraySize,  
3                                     configRUN_TIME_COUNTER_TYPE * const pulTotalRunTime )  
4  {  
5      UBaseType_t uxTask = 0, uxQueue = configMAX_PRIORITIES;  
6  
7      vTaskSuspendAll();  
8      {  
9          /* Is there a space in the array for each task in the system? */  
10         if( uxArraySize >= uxCurrentNumberOfTasks )  
11         {  
12             /* Fill in an TaskStatus_t structure with information on each  
13              * task in the Ready state. */  
14             #if ( configUSE_EDF_SCHEDULER == 0 )  
15  
16                 do  
17                 {  
18                     uxQueue--;  
19                     uxTask += prvListTasksWithinSingleList( &(amp; pxTaskStatusArray[ uxTask ] ), &( pxReadyTasksLists[ uxQueue ] ), eReady );  
20  
21                 } while( uxQueue > ( UBaseType_t ) tsxIDLE_PRIORITY ); /*lint !e961 MISRA exception as the casts are only redundant for some ports. */  
22  
23             #else  
24  
25                 /* Getting the info from the new Ready List */  
26                 uxTask += prvListTasksWithinSingleList( &( pxTaskStatusArray[ uxTask ] ), &( xReadyTasksListEDF ), eReady );  
27  
28             #endif  
29  
30             ....  
31         }  
32     }
```

I had to change the execution time of both tasks because the system was loaded so adding **vTaskGetRunTimeStats** made the system not schedule.

New CPU load: 55%

Watch 1		
Name	Value	Type
task_1_total_time	0x000076F3	int
task_2_total_time	0x00004116	int
system_time	0x000148CE	int
cpu_load	55	int
<Enter expression>		

vTaskGetRunTimeStats OUTPUT:

The numbers do add up to confirm the previous cpu load calculated.

UART #2		
Task 2	3854	19%
IDLE	8476	43%
Task 1	6917	35%
IDLE	9020	44%
Task 1	7348	35%
Task 2	4078	19%
Task 2	4330	19%
IDLE	9534	43%
Task 1	7780	35%

CONCLUSION

The EDF Scheduler was successfully implemented and tested with a task set that utilizes the EDF scheduling.

The selected tasks system was verified for its schedulability with three different methods: manual analytical calculations, offline simulator, and in Run-Time analysis.

The implemented EDF Scheduler lived up to the expectations, and passed all the tests.

Further dive and updates to the source code were made to change the run time stats so that even the run time analysis could be made by the scheduler itself and printed out the cpu and tasks stats.