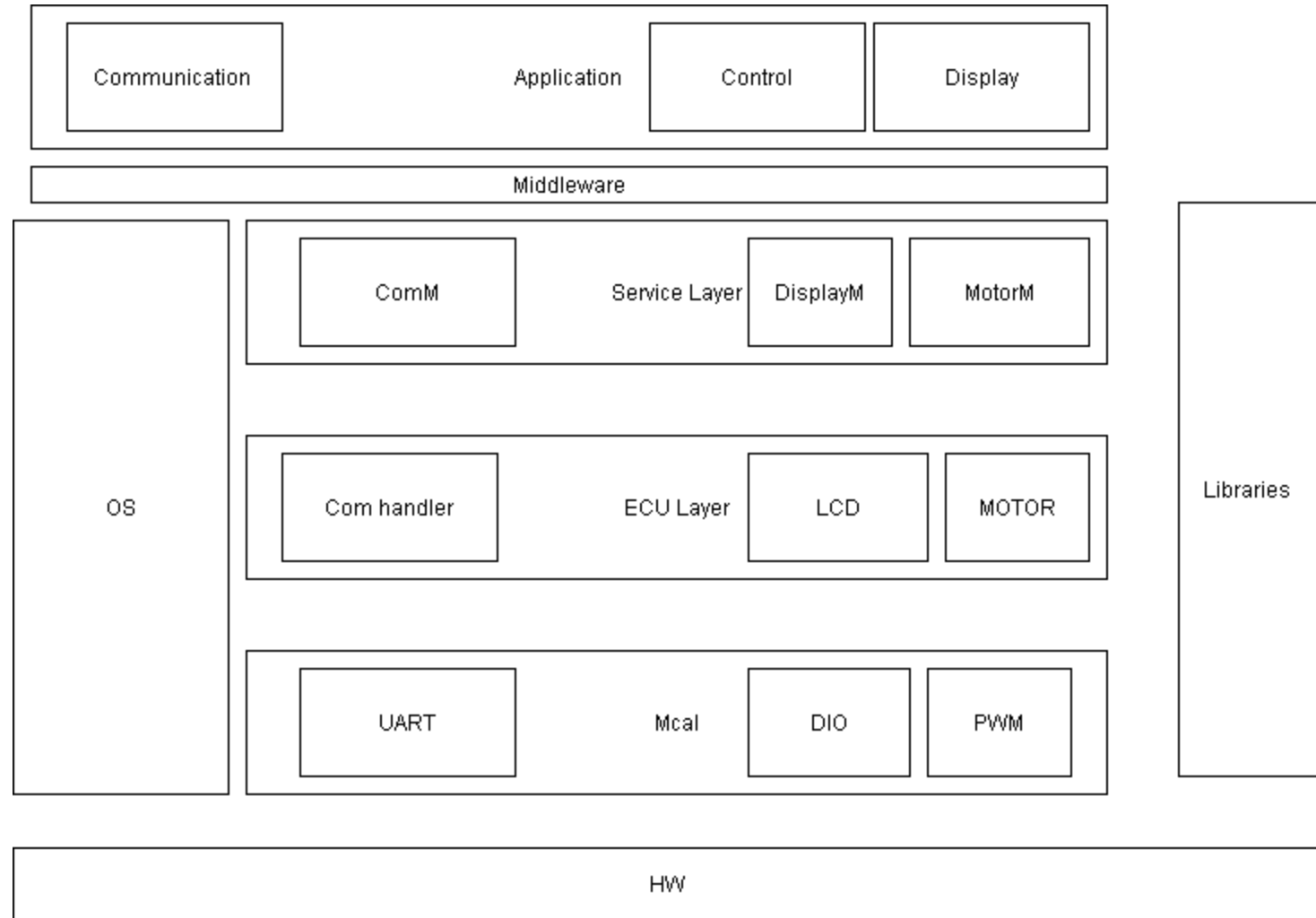


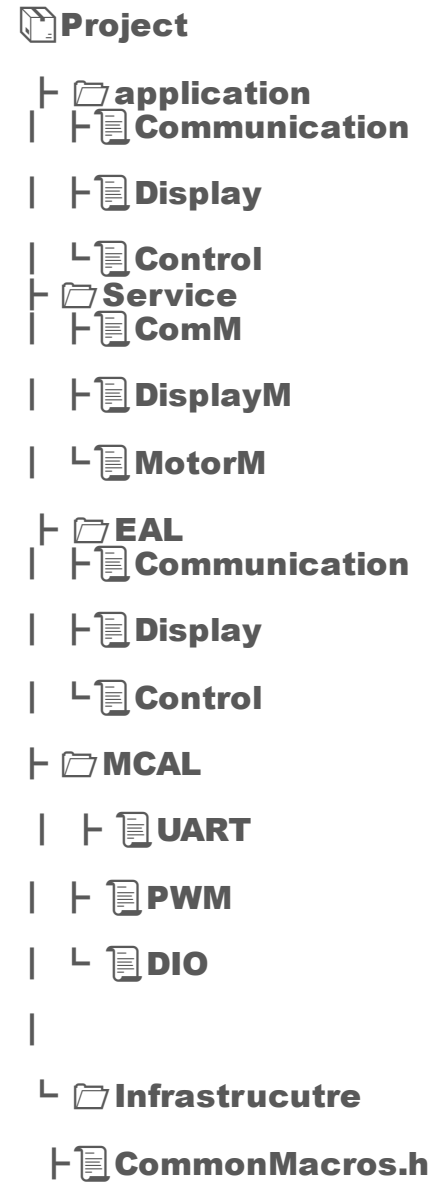
# Embedded SW Design

Dynamic Design

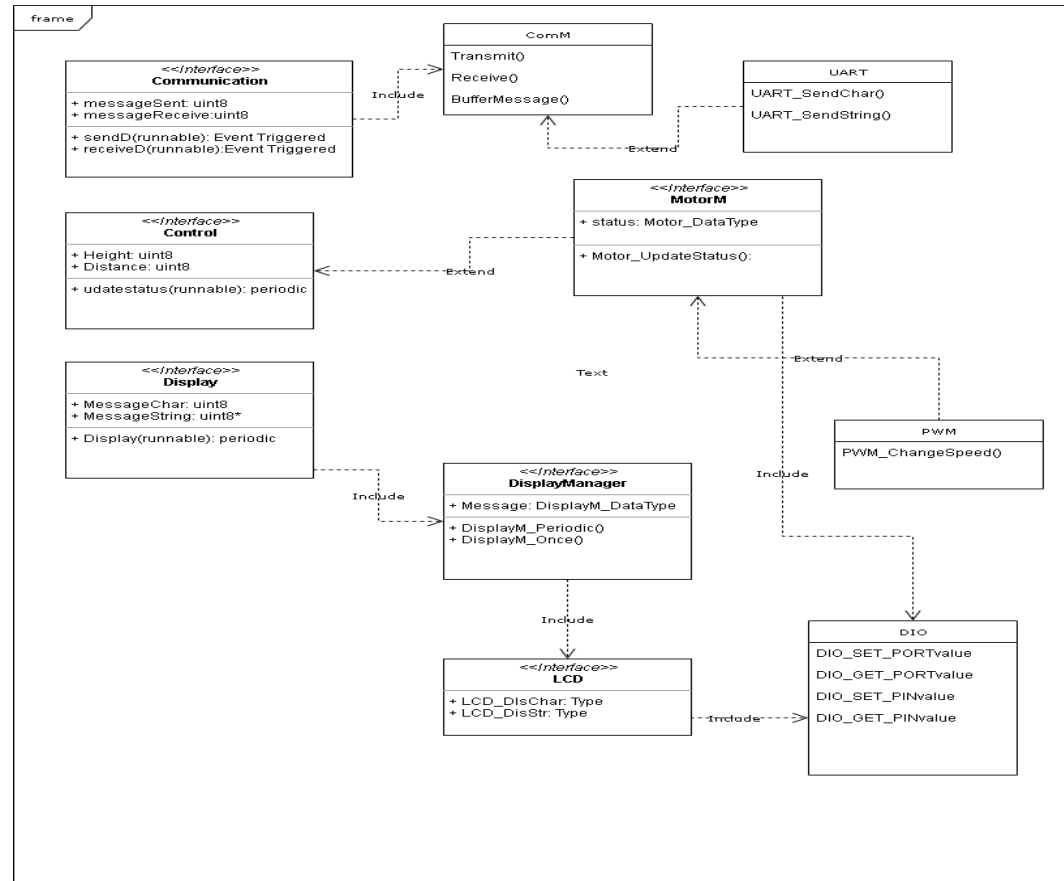
# 1. Layered Architecture (AUTOSAR based)



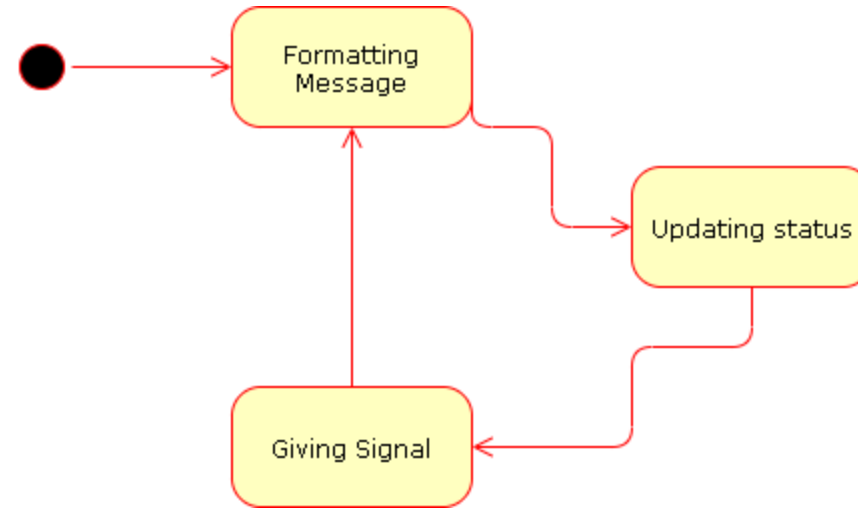
# Folder Structure



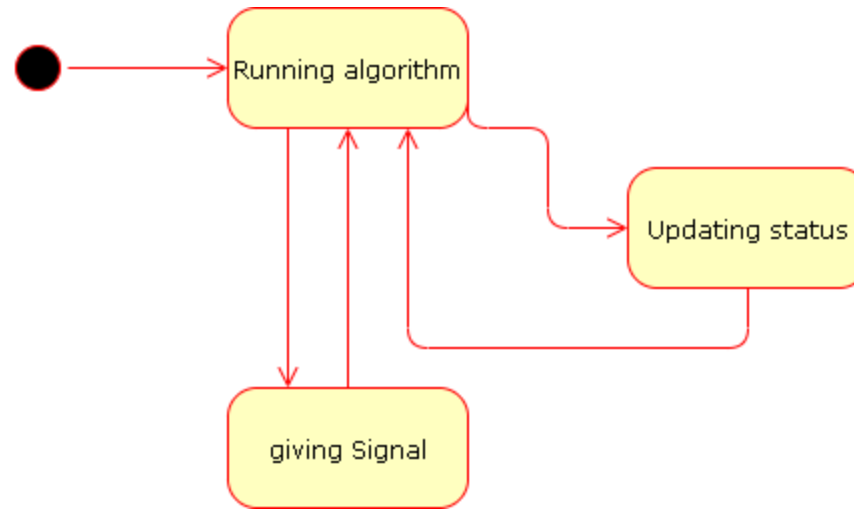
# Class Diagram



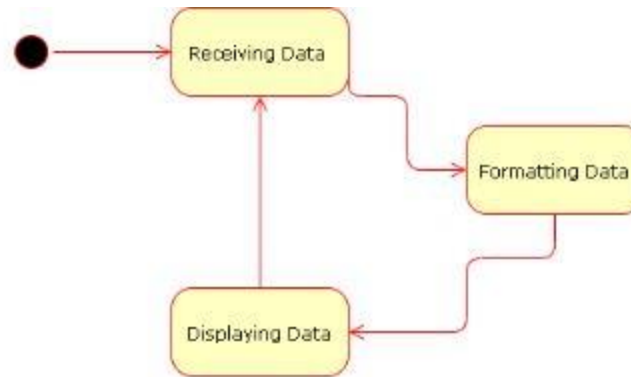
# State Machine Display SWC



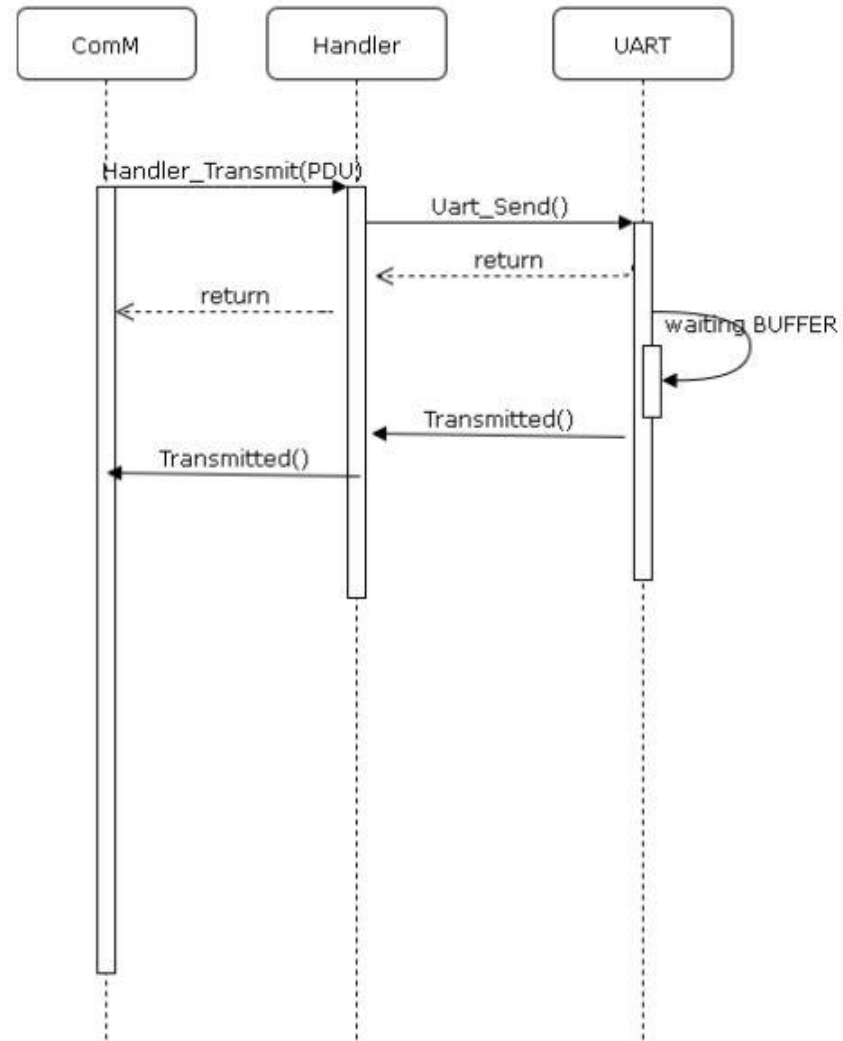
# State Machine Control SWC



# State Machine Communication SWC

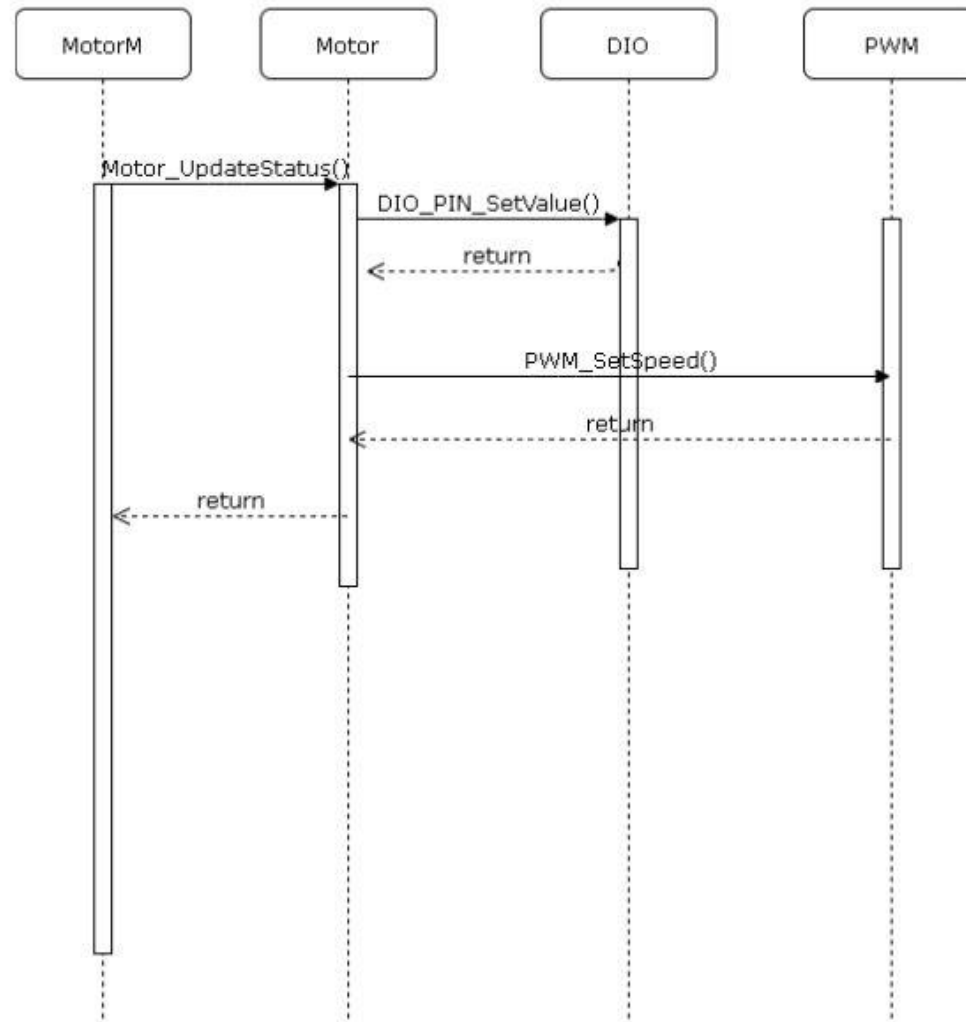


# Sequence Diagram Com stack:





# Sequence Diagram IO(Motor)stack:



# Sequence Diagram Display stack:

