

Faculty of Computing



Lab # 11

Computer Architecture

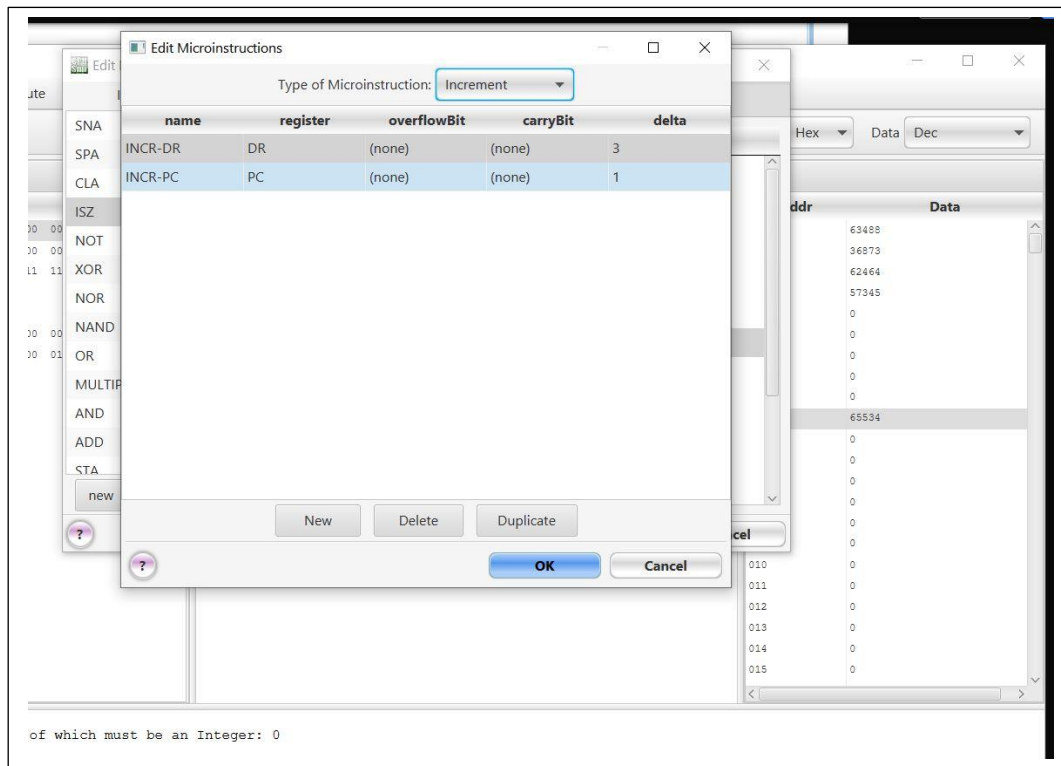
Lab Instructor: Ayesha

Name: Mahnoor Farrukh

SAP ID: 52926

Semester: BSCS-4

Task 1:



Registers

Name	Width	Data
PC	16	2
AR	12	1
DR	16	0
R	1	0
T	1	0
TR	16	8191
PC	12	6
S	1	1

```

1 IMP
2 ISZ 009
3 OUT
4 HLT

```

MAIN

Addr	Data
000	63488
001	32777
002	62464
003	57345
004	0
005	0
006	0
007	0
008	0
009	0
00A	0
00B	0
00C	0
00D	0
00E	0
00F	0
010	0

```

EXECUTING...
Enter inputs, the first of which must be an Integer: 2
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

```

Task 2:

*Lab11

File Edit Modify Execute Help

Data Bin

Registers

Name	Width	Data
PC	16	0000 0000 0000 1001
AR	12	0000 0000 0001
DR	16	0000 0000 0000 0011
E	1	0
I	1	0
IR	16	1110 0000 0000 0001
PC	12	0000 0000 0100
S	1	1

W1-0.a x CLA.a x SPA.a x 11.1Task.a x 11.2Task.a x

```
1 INP
2 ISZ 009
3 OUT
4 HLT
5
```

Addr Hex Data Dec

MAIN

Addr	Data
000	63488
001	36873
002	62464
003	57345
004	0
005	0
006	0
007	0
008	0
009	3
00A	0
00B	0
00C	0
00D	0
00E	0
00F	0
010	0
011	0
012	0
013	0
014	0
015	0

EXECUTING...

Enter Inputs, the first of which must be an Integer: 9

Output: 9

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]