Faculty of Computing



Lab # 8

Computer Architecture

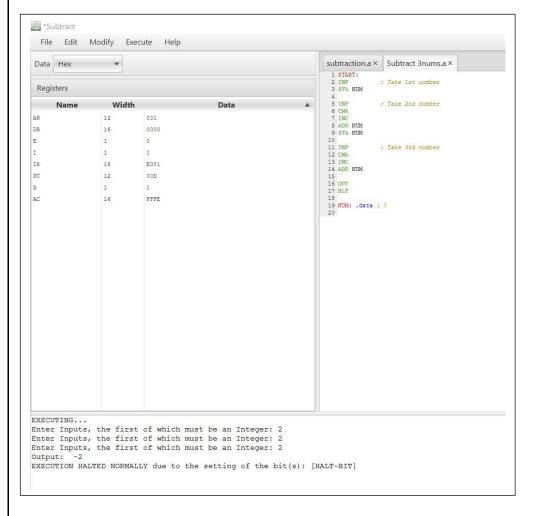
Lab Instructor: Ayesha Akram

Name: Mahnoor Farrukh

SAP ID: 52926

Semester: BSCS-4

Task 1:



Task 2:

```
1 START:
2 ADD NUM1
3 CMA
  Registers
   Name
                 Width
                                            Data
                                                                           4 INC
5 ADD NUM2
                 16
AR
                 12
                                                                           7 OUT
DR
                 16
                            6
                                                                          10 NUM1: .data 1 5
11 NUM2: .data 1 6
12 RESULT: .data 1 0
                 1
                            -1
                 1
                            0
IR
                 16
                            -8191
PC
                 12
                            6
S
                 1
                            -1
EXECUTING...
Output: 1
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

Task 3:

