

# State Space Models for Power SiC MOSFET

Nk M'Sirdi, K Frifita, E Baghaz, A Naamane, M Boussak

► **To cite this version:**

Nk M'Sirdi, K Frifita, E Baghaz, A Naamane, M Boussak. State Space Models for Power SiC MOSFET. International Conference on Electronic Engineering and Renewable Energy (ICEERE'2018), Apr 2018, Oujda, Morocco. hal-01779823

**HAL Id: hal-01779823**

**<https://hal.archives-ouvertes.fr/hal-01779823>**

Submitted on 27 Apr 2018

**HAL** is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

# State Space Models for Power SiC MOSFET

N. K. M'Sirdi, K. Frifita, E. Baghaz, A. Naamane, and M. Boussak

Aix Marseille Univ, Universite de Toulon, CNRS, LIS, Marseille, France  
SASV group of LIS UMR CNRS 7020, 13397, Marseille France, e-mail:  
nacer.msirdi@univ-amu.fr

**Abstract.** This paper proposes a State Space Model for a power a Silicon Carbide (SiC) MOSFET. The model uses the electrical EKV MOSFET structure. The model is developed for the SiC MOSFET C2M0025120D CREE (1200V, 90A) and uses the parameters extracted from datasheet

**Keywords:** SiC MOSFET State Space Model, State Space Simulation Model.

## 1 Introduction

In high power applications, Silicon (Si) circuits are more and more replaced by SiC MOSFET devices. The wide range of operation lead to parameter variations, inherent perturbations and constraints which affect behavior of the components. Then disposal of good simulation models are necessary to get realistic simulations and accurate results.

The increasing use of switching devices in converters in power systems give more abilities but results in complex control problems related to nonlinearities involved and distortion in the electric network. Non robust controls result in a degradation of the power factor and generate voltage and current disturbances which have a different frequency than the fundamental. The state space models are helpful for systems analysis advanced controllers design. Up to now and to the best of our knowledge, the only author talking about state space representation for a MOSFET is Karvonen [1,2]. The model used, is based on SPICE® equations of discrete elements whose values are extracted from datasheet. But only the state vector is presented in the paper and in the thesis. The author uses the Modified Nodal Analysis (MNA) [3,4]. The state space equation have never been developed for MOSFETs and proposed in literature.

In this paper, we propose state space representations, for a SiC MOSFET, deduced from a circuit model we have recently proposed [5,6], based on the well known EKV MOSFET model [7]. The model is developed for the SiC MOSFET transistor C2M0025120D CREE (1200V, 90A) [8] and is implemented in Matlab/Simulink simulation softwares to allow easy control design for SiC MOSFET based applications. The results show that the proposed model is the most interesting to develop advanced controllers, compared to all the literature existing models.

## 2 SiC MOSFET Model

### 2.1 The Modelling Context

**The MOSFET Models** Several simulators have been proposed to describe the device conception and its behaviour. In [9], we find for an electro thermal simulator integrated into the environment. This is based on the use of the Verilog-A description language and the SKILL script language. The VERILOG-A language is used for modeling electrical components and for thermal modeling. Circuit design does not require all details which make such models very slow to run. The EKV model is an accurate mathematical MOSFET model developed by C. C. Enz, F. Krummenacher and E. A. Vittoz based in the 1980s, which takes into account physical properties [7].

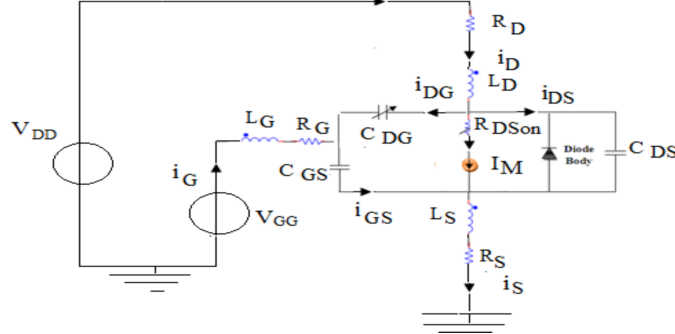
The manufacturers propose as much as possible models for their new circuits. Recently CREE proposed an LTspice simplified models for SiC MOSFETs like the one we are interested in [8]. The models proposed in Psim, Pspice, LMS AMESIM, Saber simulation softwares are often redundant, need solvers and complex. Linearized models are valid only in a restricted area. In order to get credible results based on simulations, we need to have accurate models, with acceptable precision.

In [1] the proposed MOSFET model is based on combined nodal and voltage loops analysis to get the equivalent circuit equations (by applying Kirchhoff's laws) to evaluate the states and their derivatives [10]. The main contribution of [2] is the Gate-Drain capacitance characterization and its dependence on the current flow.

### 2.2 The nominal Electric Model

The electrical model of a SiC MOSFET proposed in [5] is based on the EKV's one. The electric equivalent circuit of figure 1 summarizes the model description like it has been used in physical modeling based simulation softwares like Saber, Pspice and Psim. The Source, Gate and Drain voltages are respectively  $V_S$ ,  $V_G$  et  $V_D$ . The resistance  $R_G$  estimates the gate losses as well as  $R_D$  and  $R_S$  are used to account for losses at the Source and Drain electrodes. The circuit includes also 3 inductors in series with the electrodes ( $L_G$ ,  $L_D$ ,  $L_S$ ) to describe the behavior at high frequencies.

In this model, we used parameters extracted from the data-sheet [5, 6]. The model accounts for a MOSFET operation in 3 regions. It is composed by two current sources  $I_{G1}$  and  $I_{G2}$ . For interpolation between these 3 regions of operation, a linear combination of logarithmic functions is used by EKV. The specific current is  $I_{s0} = 2 \cdot V_{th}^2 \cdot K_s \cdot g_m$ . The Drain-Source current ( $I_{DS}$ ), in the EKV MOSFET model, is defined by equation (??), as the difference between the direct  $I_{G1}$  (2) and the reverse one  $I_{G2}$  (3) [6].



**Fig. 1.** Electric model of the SiC MOSFET (C2M0080120D)

$$I_M = I_{G1}(V_p - V_S) - I_{G2}(V_p - V_D) \quad (1)$$

$$I_{G1} = I_s \cdot g_m \cdot \left[ \ln \left( 1 + \exp \frac{V_p - V_s}{2V_{DS}} \right) \right]^2 \quad (2)$$

$$I_{G2} = I_s \cdot ft \cdot \left[ \ln \left( 1 - \exp \frac{V_p - V_D}{2} \right) \right]^2 \quad (3)$$

$g_m$  is the transconductance,  $K_s$  is the slope factor,  $V_p = \frac{(V_G - V_{th})}{K_s}$  the pinch voltage with  $V_{th}$  the thermodynamic reference voltage and  $ft$  the internal thermal voltage.

The following electric equations corresponding to the figure (1) can then be obtained applying the Kirchhoff laws for each current node and voltage loop of the circuit. The equation system is redundant and non linear. Let us note  $\xi \in \{0, 1\}$  a state control variable introduced to switch betokening the ON/OFF states. We have  $\xi = 1$  when the MOSFET is in ON state and respectively  $\xi = 0$  when the MOSFET is in the OFF state.

$$\left\{ \begin{array}{l} V_{GG} = L_G \frac{di_G}{dt} + R_G i_G + V_{GS} + V_S \\ V_{DD} = L_D \frac{di_D}{dt} + R_D i_D + V_{DS} + V_S \\ V_{DD} - V_{GG} = L_D \frac{di_D}{dt} + R_D i_D + V_{DG} - L_G \frac{di_G}{dt} - R_G i_G \\ V_S = L_S \frac{di_S}{dt} + R_S i_S \\ V_{DS} = \xi \cdot R_{DS(on)} I_M \\ i_{DS} = C_{DS} \frac{dV_{DS}}{dt} \\ i_{GS} = C_{GS} \frac{dV_{GS}}{dt} \\ i_{DG} = C_{DG} \frac{dV_{DG}}{dt} \\ i_G = C_{GS} \frac{dV_{GS}}{dt} - C_{DG} \frac{dV_{DG}}{dt} \\ i_D = \xi \cdot I_M + C_{DG} \frac{dV_{DG}}{dt} + C_{DS} \frac{dV_{DS}}{dt} \\ i_S = \xi \cdot I_M + C_{GS} \frac{dV_{GS}}{dt} + C_{DS} \frac{dV_{DS}}{dt} \end{array} \right. \quad (4)$$

The Modified Nodal Analysis (MNA) is in general used to solve this set of simultaneous, redundant and non linear equations [3, 4] by selecting appropriate variables. The best formulation, to avoid redundancy, computation burden and

singularities, for simulation and the control design, is the state space form, after simplifying the model.

The inputs are the voltage sources ( $u(t) = [V_{GG}, V_{DD}]^T$ ) and the outputs may be all the state variables ( $i_G, i_D, i_S, V_{GS}, V_{DG}, V_{DS}$ ) or only some of them as needed by the application considered.

### 2.3 State Space SiC MOSFET Model

The SiC MOSFET inputs are the two voltage sources ( $V_{DD}, V_{GG}$ ). The MOSFET model is in general implemented as an electric circuit (see Fig. 1) completed with an appropriate elements to switch between ON and OFF states and introducing the non linear current of equation (??) [1]. The system of equation (4) deduced by application of Kirchoff's laws is redundant and has different time and values scales. The input, the outputs and the state equation have to be carefully selected.

**First State Space formulation trial** The SiC MOSFET circuit is made of six energy storage elements (3 capacitances and 3 inductances). Storage elements are obviated in the system equations (4). The corresponding energy storage variables are, in nature, the internal system states (to be used for the state space representation).

We can keep as state variables the 3 currents (charging the capacitances) and the 3 voltages (inductors), which seem, a priori, independent. The chosen state vector  $X$  is then composed by  $v_1$  and  $v_2$ .

$$v_1 = [i_G \ i_D \ i_S]^T \text{ and } v_2 = [V_{GS} \ V_{DG} \ V_{DS}]^T \quad (5)$$

$$X = [i_G \ i_D \ i_S \ V_{GS} \ V_{DG} \ V_{DS}]^T \quad (6)$$

Let us keep only a minimum of independent equations (3 for voltages Differential Equations (DAE) and 3 for currents ones):

$$\begin{cases} L_G \frac{di_G}{dt} + L_S \frac{di_S}{dt} + V_{GS} + R_G i_G + R_S i_S = V_{GG} \\ L_D \frac{di_D}{dt} - L_G \frac{di_G}{dt} - R_G i_G + R_D i_D + \xi \cdot R_{DSon} I_M - V_{GS} = V_{DD} - V_{GG} \\ \frac{di_S}{dt} = \frac{di_D}{dt} + \frac{di_G}{dt} \end{cases} \quad (7)$$

$$\begin{cases} -C_{DG} \frac{dV_{DG}}{dt} + C_{GS} \frac{dV_{GS}}{dt} = i_G \\ C_{DS} \frac{dV_{DS}}{dt} + C_{GS} \frac{dV_{GS}}{dt} = i_S - \xi \cdot I_M \\ \frac{dV_{DS}}{dt} - \frac{dV_{DG}}{dt} - \frac{dV_{GS}}{dt} = 0 \end{cases} \quad (8)$$

We get then

$$\begin{cases} \dot{v}_1 = -M_1^{-1} K_1 v_1 - M_1^{-1} K_2 v_2 + M_1^{-1} u_1 + \xi \cdot M_1^{-1} R_{DSon} u_2 \\ \dot{v}_2 = -M_2^{-1} K_3 v_1 + M_2^{-1} u_2 \end{cases} \quad (9)$$

which gives us the state space representations of the electric part of the model

$$\dot{X} = AX + Bu \quad (10)$$

The matrices are as follows

$$M_1 = \begin{bmatrix} L_G & 0 & L_S \\ -L_G & L_D & 0 \\ -1 & -1 & 1 \end{bmatrix}, K_1 = \begin{bmatrix} R_G & 0 & R_S \\ -R_G & R_D & 0 \\ 0 & 0 & 0 \end{bmatrix} \quad (11)$$

$$K_2 = \begin{bmatrix} 1 & 0 & 0 \\ -1 & 0 & 0 \\ 0 & 0 & 0 \end{bmatrix} \text{ and } u_1 = \begin{bmatrix} V_{GG} \\ V_{DD} - V_{GG} \\ 0 \end{bmatrix} \quad (12)$$

and

$$M_2 = \begin{bmatrix} C_{GS} & -C_{DG} & 0 \\ C_{GS} & 0 & C_{DS} \\ -1 & -1 & 1 \end{bmatrix} \text{ and } u_2 = \begin{bmatrix} 0 \\ -I_M \\ 0 \end{bmatrix} \quad (13)$$

$$K_3 = \begin{bmatrix} -1 & 0 & 0 \\ 0 & 0 & -1 \\ 0 & 0 & 0 \end{bmatrix} \quad (14)$$

Computing the inverse matrices  $M_1^{-1}$  et  $M_2^{-1}$ , we get the A matrix

$$A = \begin{bmatrix} \frac{-R_G(L_D+L_S)}{d_1} & \frac{R_D L_S}{d_1} & \frac{-R_S L_D}{d_1} & \frac{-L_D-L_S}{d_1} & 0 & 0 \\ \frac{R_G L_S}{d_1} & \frac{-R_D(L_G+L_S)}{d_1} & \frac{-L_G R_S}{d_1} & \frac{L_S}{d_1} & 0 & 0 \\ \frac{-R_G L_D}{d_1} & \frac{-R_D L_G}{d_1} & \frac{-R_S(L_D+L_G)}{d_1} & \frac{-L_D}{d_1} & 0 & 0 \\ \frac{C_{DS}}{d_2} & 0 & \frac{C_{DG}}{d_2} & 0 & 0 & 0 \\ \frac{-(C_{DS}+C_{GS})}{d_2} & 0 & \frac{C_{GS}}{d_2} & 0 & 0 & 0 \\ -\frac{C_{GS}}{d_2} & 0 & \frac{C_{DG}+C_{GS}}{d_2} & 0 & 0 & 0 \end{bmatrix} \quad (15)$$

This state space representation offers a very bad numerical conditionning. We can remark that we have two null eigen values  $\lambda_1 = \lambda_2 = 0$ . This means that there are two integrators in the process. This will integrate twice all the numerical computation errors. There are two big complex conjugate eigen values, in the A matrix which are for our case:  $\lambda_3 = \lambda_{34}^* = -1.1250e+08 + 1.0468e+08i$ . The last two eigen values are the biggest and the smallest ones  $\lambda_5 = -87.903e+06$  and  $\lambda_6 = 8.4371e-09$ . They have as ratio  $\frac{\lambda_1}{\lambda_2} = -1.0419e+16$  which show us that this matrix is very badly conditionned for simulation omputings.

This proves that this state space form cannot be used in simulations because of the numerical problems; this justifies the need of the NDA approach [1].

**Second State Space formulation** The fact we got two zero eigen values means that special relations exist between the state variables considered and inputs. Effectively we can remark that in the system equations we have Algebraic equations mixed with differential equations. The previous system 4 can then be rewretten to emphasize the Differential and Algebraic Equations (DAE). We can observe, in the oridnary differential equations (ODEs) of 16 and 17, that

dependence of the three currents and the three voltages is obvious. Each one of those systems has then only 2 independent ODEs, as their third one expresses only the difference between the previous ones.

$$\begin{cases} L_G \frac{di_G}{dt} + L_S \frac{di_S}{dt} + R_G i_G + R_S i_S + V_{GS} = V_{GG} \\ L_D \frac{di_D}{dt} + R_D i_D + V_{DS} + L_S \frac{di_S}{dt} + R_S i_S = V_{DD} \\ i_D = i_S - i_G = i_{DG} + i_{DS} + \xi \cdot I_M \end{cases} \quad (16)$$

$$\begin{cases} -C_{DG} \frac{dV_{DG}}{dt} + C_{GS} \frac{dV_{GS}}{dt} = i_G \\ C_{DS} \frac{dV_{DS}}{dt} + C_{GS} \frac{dV_{GS}}{dt} = i_S - \xi \cdot I_M \\ V_{DS} = V_{DG} + V_{GS} \end{cases} \quad (17)$$

So we can remark that the variables  $i_D$  and  $V_{DS}$  (respectively) can be deduced by algebraic equations from the state variables  $v_1 = [i_G, i_S]^T$  and  $v_2 = [V_{GS}, V_{DG}]^T$  (respectively). In addition these equations include the system non linearities and thermal dependances and variations. We can replace  $i_D = i_S - i_G = i_{DG} + i_{DS} + I_M$ . The same can be done for the voltage  $V_{DS} = V_{DG} + V_{GS}$  which imposes the inherent EKV current sources. Then we get the following ODE systems become

$$\begin{cases} L_G \frac{di_G}{dt} + L_S \frac{di_S}{dt} + R_G i_G + R_S i_S + V_{GS} = V_{GG} \\ -L_D \frac{di_G}{dt} + (L_D + L_S) \frac{di_S}{dt} - R_D i_G + (R_D + R_S) i_S + \xi \cdot R_{DSon} I_M = V_{DD} \end{cases} \quad (18)$$

$$\begin{cases} C_{GS} \frac{dV_{GS}}{dt} - C_{DG} \frac{dV_{DG}}{dt} - i_G = 0 \\ (C_{GS} + C_{DS}) \frac{dV_{GS}}{dt} + C_{DS} \frac{dV_{DG}}{dt} - i_S = -\xi \cdot I_M \end{cases} \quad (19)$$

Finally

$$\begin{cases} \dot{v}_1 = -M_1^{-1} K_1 v_1 - M_1^{-1} K_2 v_2 + M_1^{-1} u_1 \\ \dot{v}_2 = -M_2^{-1} K_3 v_1 + \xi \cdot M_2^{-1} u_2 \end{cases} \quad (20)$$

The matrices are as follows

$$M_1 = \begin{bmatrix} L_G & L_S \\ -L_D & L_D + L_S \end{bmatrix}, \quad K_1 = \begin{bmatrix} R_G & R_S \\ -R_D & R_D + R_S \end{bmatrix} \quad (21)$$

$$K_2 = \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix} \text{ and } u_1 = \begin{bmatrix} V_{GG} \\ V_{DD} \end{bmatrix} \quad (22)$$

and

$$M_2 = \begin{bmatrix} C_{GS} & -C_{DG} \\ C_{GS} + C_{DS} & C_{DS} \end{bmatrix} \text{ and } u_2 = \begin{bmatrix} 0 \\ -I_M \end{bmatrix} \quad (23)$$

$$K_3 = \begin{bmatrix} -1 & 0 \\ 0 & -1 \end{bmatrix} \quad (24)$$

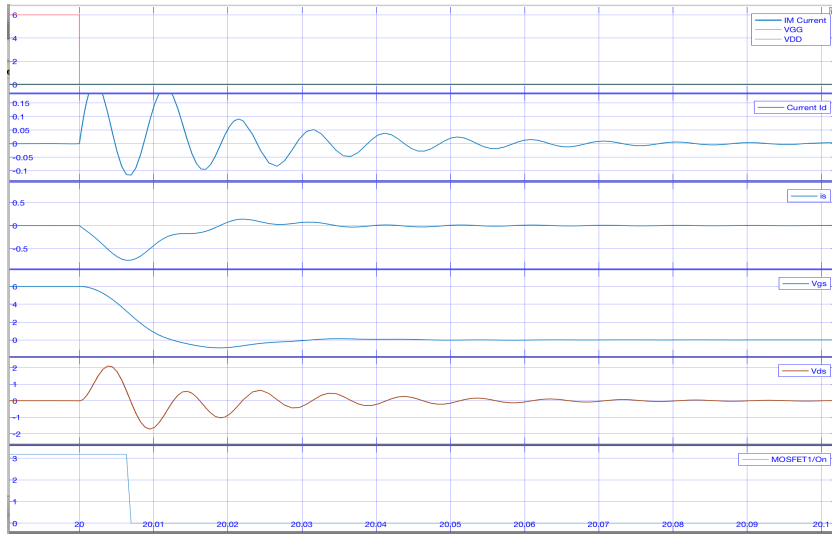
The matrices determinants are  $d_1 = 2.7900 \cdot 10^{-16}$  and  $d_2 = 2.1135 \cdot 10^{-19}$  (respectively for  $M_1$  and  $M_2$ ) which means than we have interest to simulate the

two equations separately (one for  $v_1$  and the second for  $v_2$ ). The system has as eigen values  $\lambda_p$ :

$$\lambda_p = 10^8 [-0.4679 + 6.3800i; -0.4679 - 6.3800i; -1.0967 + 1.7151i; -1.0967 - 1.7151i] \quad (25)$$

### 3 Simulations Tests and Model Validation

In order to verify the afore-mentioned characteristics, a series of simulation circuits were developed. The developed model has been implemented in Saber, Pspice and Psim softwares and compared to the experimental curves presented by the SiC MOSFET data sheet for its validation and parameters extraction.



**Fig. 2.** Transient responses

### 4 Conclusion

A new state space model has been proposed in this paper for a SiC Power MOSFET. It is deduced from an electric equivalent circuit, by selecting appropriate nonlinear differential equations written in a state space form.

This model can be implemented under Matlab/Simulink software and tested in simulation. The Characteristics of the MOSFET simulation models (on-state resistance, threshold voltage, and transconductance, etc...) are compared to Pspice simulation model

The simulation model of SiC power MOSFET is easy to use in numerical design and prototyping of electric circuits and simulations. The future work of our staff will be on nonlinear and robust control of a power circuit including the SiC MOSFETs.



**Acknowledgments** This research activity was held by the SASV group of the LIS and co funded by an FUI project. The authors would like to thanks also Europractice (<http://www.europractice.stfc.ac.uk/> ).

## References

1. A. Karvonen and T. Thiringer, "Mosfet modeling adapted for switched applications using a state-space approach and internal capacitance characterization," in *International Conference on Power Electronics and Drive Systems (PEDS) pp1107 - 1112 Taipei, Taiwan 2-5 Nov. 2009*, 2009. 1, 2.1, 2.3, 2.3
2. A. Karvonen, "Emi from switched converters : Simulation methods and reduction techniques," Chalmers University, Licentiate Thesis at Department of Energy and Environment, Division of Electric Power Engineering; Chalmers University of Technology Goteborg, Sweden 2011, Goteborg, Sweden, 2009. 1, 2.1
3. H. Chung-Wen, A. Ruehli, and P. Brennan, "The modified nodal approach to network analysis," *IEEE Transactions on Circuits and Systems*, vol. 22, no. 6, pp. 504–509, 1975. 1, 2.2
4. L. M. Wedepohl and L. Jackson, "Modified nodal analysis: an essential addition to electrical circuit theory and analysis," *Journal of Engineering Science and Education*, vol. 11, no. 3, pp. 84–92, 2002. 1, 2.2
5. N. K. M'Sirdi, E. Baghaz, K. Frifita, A. Naamane, and M. BOUSSAK, "Simulation model of a sic power mosfet variables estimations and control of a power source," in *14th International Conference on Informatics in Control, Automation and Robotics - Volume 1: ICINCO*, O. G. 1 and K. Madani, Eds., vol. 1, july 2017, pp. 637–643. 1, 2.2, 2.2
6. K. Frifita, N. K. M'Sirdi, E. Baghaz, A. Naamane, and M. Boussak, "Electro-thermal model of a silicon carbide power mosfet," submitted to ICEERE2018, Saidia Oujda Morroco, April 2018. 1, 2.2
7. E. Vittoz, C. C. Enz, and F. Krummenacher, "A thermal characterization of power transistors semiconductor," vol. 11. 1, 2.1
8. CREE, "Sic power mosfet cmf20120d datas-sheet and application note," <http://www.cree.com/products/pdf/CMF20120D.pdf>, 2015. 1, 2.1
9. A. Maxim and G. Maxim, "A high accuracy power MOSFET SPICE behavioral macromodel including the device selfheating and safe operating area simulation." 2.1
10. G. Verneau, L. Aubard, J. Cr  bier, and J. Schanen, "Empirical power mosfet modeling: Practical characterization and simulation implantation," in *37th IAS Annual Meeting. Conference Record of the Volume4*, . t. I. A. M. Conference Industry Applications Conference, Ed., vol. 4, no. DOI10.1109/IAS.2002.1042785. IAS Annual Meeting (IEEE Industry Applications Society) 4:2425 - 2432, February 2002, pp. 2425–2432. 2.1