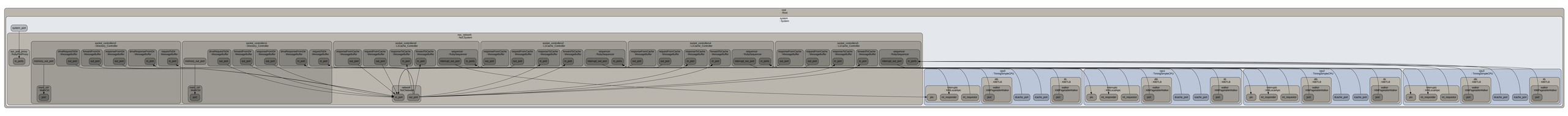
root
Root
system
System
system_port
sys_port_proxy
SimpleMemory
mem_ctrl_port
in_port
socket_controller0
Directory_Controller
memory_out_port
dmaRequestToDir
MessageBuffer
out_port
forwardFromDir
MessageBuffer
responseFromDir
MessageBuffer
in_port
requestToDir
MessageBuffer
in_port
reset_ctrl
port
socket_controller1
Directory_Controller
socket_controller2
L1Cache_Controller
responseFromCache
MessageBuffer
requestFromCache
MessageBuffer
responseToCache
MessageBuffer
forwardToCache
MessageBuffer
sequencer
RubySequencer
interrupt_out_port
in_ports
socket_controller3
L1Cache_Controller
network
SimpleNetwork
cpu0
TimingSimpleCPU
dtb
X86TLB
walker
X86PagetableWalker
port
interrupts
X86Interrupts
pio
int_responder
int_requester
dcache_port
icache_port
cpu1
TimingSimpleCPU
cpu2
TimingSimpleCPU
cpu3
TimingSimpleCPU