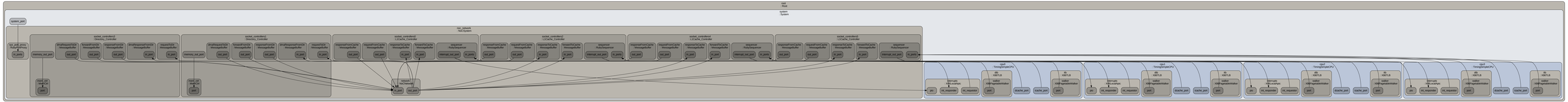

root
: Root
system
: System
system_port
noc_network
: NoCSystem
sys_port_proxy
: RubyPortProxy
in_ports
socket_controllers3
: Directory_Controller
memory_out_port
dmaRequestToDir
: MessageBuffer
out_port
forwardFromDir
: MessageBuffer
out_port
responseFromDir
: MessageBuffer
out_port
dmaResponseFromDir
: MessageBuffer
in_port
requestToDir
: MessageBuffer
in_port
mem_ctrl
: MemCtrl
port
socket_controllers1
: Directory_Controller
memory_out_port
dmaRequestToDir
: MessageBuffer
out_port
forwardFromDir
: MessageBuffer
out_port
responseFromDir
: MessageBuffer
out_port
dmaResponseFromDir
: MessageBuffer
in_port
requestToDir
: MessageBuffer
in_port
mem_ctrl
: MemCtrl
port
network
: NoCNetwork
in_port
out_port
socket_controllers0
: L1Cache_Controller
responseFromCache
: MessageBuffer
out_port
requestFromCache
: MessageBuffer
out_port
responseToCache
: MessageBuffer
in_port
forwardToCache
: MessageBuffer
in_port
sequencer
: RubySequencer
interrupt_out_port
in_ports
socket_controllers2
: L1Cache_Controller
responseFromCache
: MessageBuffer
out_port
requestFromCache
: MessageBuffer
out_port
responseToCache
: MessageBuffer
in_port
forwardToCache
: MessageBuffer
in_port
sequencer
: RubySequencer
interrupt_out_port
in_ports
socket_controllers4
: L1Cache_Controller
responseFromCache
: MessageBuffer
out_port
requestFromCache
: MessageBuffer
out_port
responseToCache
: MessageBuffer
in_port
forwardToCache
: MessageBuffer
in_port
sequencer
: RubySequencer
interrupt_out_port
in_ports
socket_controllers5
: L1Cache_Controller
responseFromCache
: MessageBuffer
out_port
requestFromCache
: MessageBuffer
out_port
responseToCache
: MessageBuffer
in_port
forwardToCache
: MessageBuffer
in_port
sequencer
: RubySequencer
interrupt_out_port
in_ports
cpu0
: TimingSimpleCPU
interrupts
: X86LocalApic
pio
int_responder
int_requestor
dtb
: X86TLB
walker
: X86PagetableWalker
port
dcache_port
icache_port
itb
: X86TLB
walker
: X86PagetableWalker
port
cpu1
: TimingSimpleCPU
interrupts
: X86LocalApic
pio
int_responder
int_requestor
dtb
: X86TLB
walker
: X86PagetableWalker
port
dcache_port
icache_port
itb
: X86TLB
walker
: X86PagetableWalker
port
cpu2
: TimingSimpleCPU
interrupts
: X86LocalApic
pio
int_responder
int_requestor
dtb
: X86TLB
walker
: X86PagetableWalker
port
dcache_port
icache_port
itb
: X86TLB
walker
: X86PagetableWalker
port
cpu3
: TimingSimpleCPU
interrupts
: X86LocalApic
pio
int_responder
int_requestor
dtb
: X86TLB
walker
: X86PagetableWalker
port
dcache_port
icache_port
itb
: X86TLB
walker
: X86PagetableWalker
port