



表 4: Strapping 管理

管脚	默认	SPI 启动模式	个机启动模式
GPIO2	无	1	1
GPIO8	无	无	1
GPIO9	内部上拉	1	0

SPI 启动模式下，控制 ROM Code 打印

管脚	默认	功能
GPIO8	无	0: fuse 的 EFUSE_UART_PRINT_CONTROL 字段为 0 时（默认值），上电正常打印，不受 GPIO8 控制。 1 时，若 GPIO8 为 0，上电正常打印；若 GPIO8 为 1，上电不打。 2 时，若 GPIO8 为 0，上电不打；若 GPIO8 为 1，上电正常打印。 3 时，上电不打，不受 GPIO8 控制。

<sup>1</sup> GPIO8 = 0 且 GPIO9 = 0 不可使用。

Schematic	Schematic1_2	Update Date	2023-08-18
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		PixelTime 电子手表	
	VER	SIZE	PAGE 1 OF 1
立创EDA	V1.3a	A2	嘉立创EDA

Name	No.	Type	Power domain	Function
XTAL_32K_P	4	I/O/T	VDD3P3_RTC	GPIO0, ADC1_CH0, XTAL_32K_P
XTAL_32K_N	5	I/O/T	VDD3P3_RTC	GPIO1, ADC1_CH1, XTAL_32K_N
GPIO2	6	I/O/T	VDD3P3_RTC	GPIO2, ADC1_CH2, FSPID
CHIP_EN	7	I	VDD3P3_RTC	High: on, enables the chip. Low: off, the chip powers off. Note: Do not leave the CHIP_PU pin floating.
GPIO3	8	I/O/T	VDD3P3_RTC	GPIO3, ADC1_CH3
MTMS	9	I/O/T	VDD3P3_RTC	GPIO4, ADC1_CH4, FSPID, MTMS
MTDI	10	I/O/T	VDD3P3_RTC	GPIO5, ADC2_CH0, FSPWP, MTDI
VDD3P3_RTC	11	PD	—	Input power supply for RTC
MTCK	12	I/O/T	VDD3P3_CPU	GPIO6, FSPID, MTCK
MTDO	13	I/O/T	VDD3P3_CPU	GPIO7, FSPID, MTDO
GPIO8	14	I/O/T	VDD3P3_CPU	GPIO8
GPIO9	15	I/O/T	VDD3P3_CPU	GPIO9
GPIO10	16	I/O/T	VDD3P3_CPU	GPIO10, FSPICS0
VDD3P3_CPU	17	PD	—	Input power supply for CPU IO
VDD_SPI	18	I/O/T/PD	VDD3P3_CPU	GPIO11, output power supply for flash
SPIHD	19	I/O/T	VDD3P3_CPU	GPIO12, SPIHD
SPIWP	20	I/O/T	VDD3P3_CPU	GPIO13, SPIWP
SPICS0	21	I/O/T	VDD3P3_CPU	GPIO14, SPICS0
SPICLK	22	I/O/T	VDD3P3_CPU	GPIO15, SPICLK
SPID	23	I/O/T	VDD3P3_CPU	GPIO16, SPID
SPIQ	24	I/O/T	VDD3P3_CPU	GPIO17, SPIQ
GPIO18	25	I/O/T	VDD3P3_CPU	GPIO18
GPIO19	26	I/O/T	VDD3P3_CPU	GPIO19
U0RXD	27	I/O/T	VDD3P3_CPU	GPIO20, U0RXD
U0TXD	28	I/O/T	VDD3P3_CPU	GPIO21, U0TXD
XTAL_N	29	—	—	External crystal output
XTAL_P	30	—	—	External crystal input
VDDA	31	PA	—	Analog power supply
VDDA	32	PA	—	Analog power supply
GND	33	G	—	Ground

Note:

- P: power pin; I: input; O: output; T: high impedance.
- VDD\_SPI, SPIHD, SPIWP, SPICS0, SPICLK, SPIQ, and SPID pins of ESP32-C3FN4 and ESP32-C3FH4 are used for connecting the embedded flash, and are not recommended for other uses.
- For the data port connection between ESP32-C3 family and external flash please refer to section 3.4.2.
- The pin function in this table refers only to some fixed settings and do not cover all cases for signals that can be input and output through the GPIO matrix. For more information on the GPIO matrix, please refer to ESP32-C3 Technical Reference Manual.

v1.1  
修改天线为rainson  
更换充电电阻R7为10k

v1.2  
led引脚更换为 IO5  
在开关电源处增加USB输入开机显示功能  
加入充电检测功能

v1.3  
修改中键key2引脚为io0，不使用strapping引脚  
修改下键key3为io9  
1750更换为1715  
修改充电电阻的位置  
增加温度设计

v1.3a  
PCB增加usb焊盘扩大  
修复电池引脚反向错误  
按照原理图增加10k  
C28和D4不引入ABOM