Time									
clk									
pc[31:0]	000+ 00000001	00000002	00000003	00000004	00000003	00000004	00000005		
reg1[31:0]	00000000			00000001		00000000			
reg2[31:0]	xxxxxxxx		00000000	00000002	00000001	00000000			
result[31:0]	000+ 00000000	00000001		00000002	00000000	FFFFFFFF			