MaldOS:
a Moderately Abstracted Layer for Developing Operating Systems

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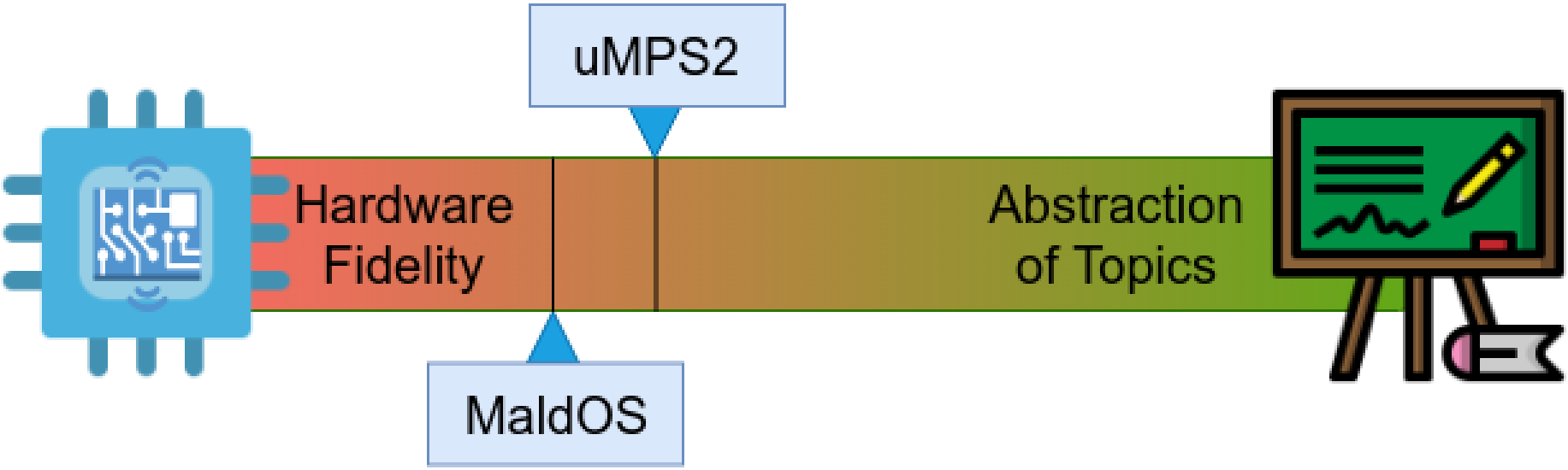
Sessione di laurea 14 marzo 2019

Structuring an Operating Systems course can be a thorny task.

Topics like scheduling, parallel programming, virtual memory and context switching are hard to understand via a purely abstract approach, and developing a concrete example is inherently non trivial.

Examples might be implemented as user space programs, but they would lose some details that deeply characterize kernel development. On the other end one could work, bare metal, on a real processor, but this entails many nuances unnecessary for students.

A previous solution to this issue was the μMPS family of emulators; this work proposes a slight shift in the form of an abstraction layer allowing for easier development on real hardware.



Tradeoffs

μMPS2 allows students to work in a simplified environment while still being faithful to the real MIPS architecture, providing an interesting experience. However

- it is still abstract work, as the resulting OS will never run on a real device
- μMPS is an ad-hoc emulator; general purpose software would be preferable
- Its debugging facilities lack step-by-step execution of C code
- MIPS is a dated architecture

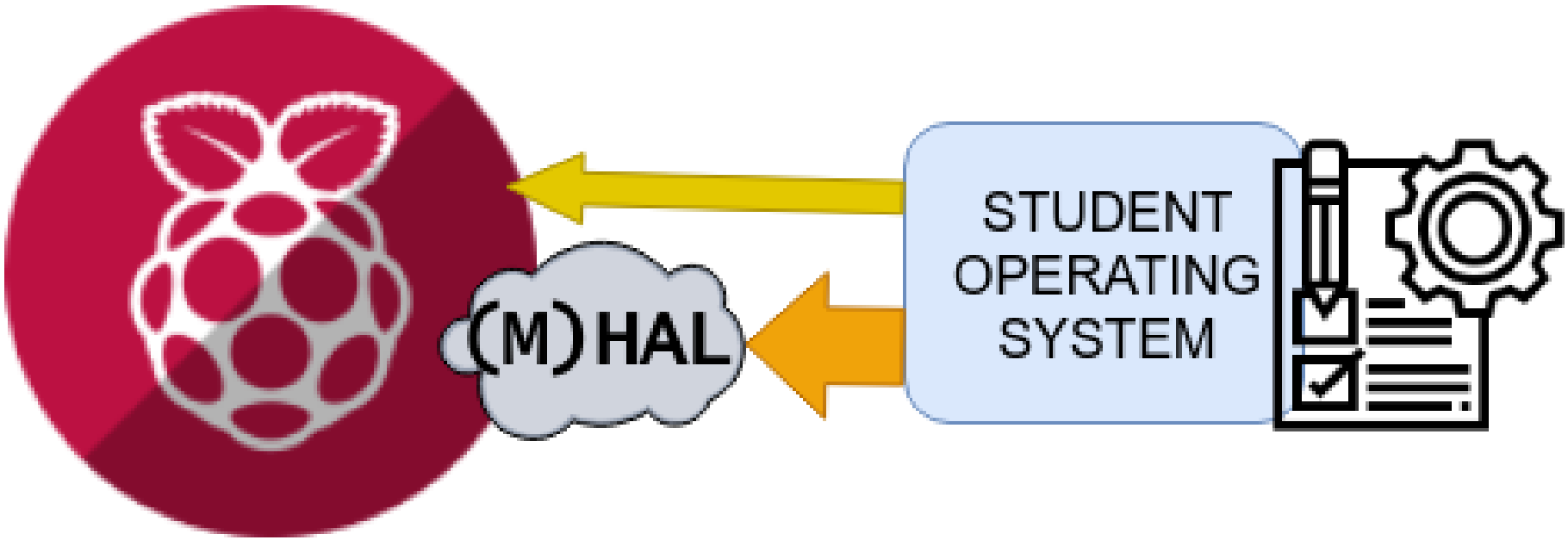
Tradeoffs

MaldOS is an abstraction layer devised to reproduce an μMPS-like simplified environment on a real device and architecture, the Raspberry Pi 3 and ARMv8.

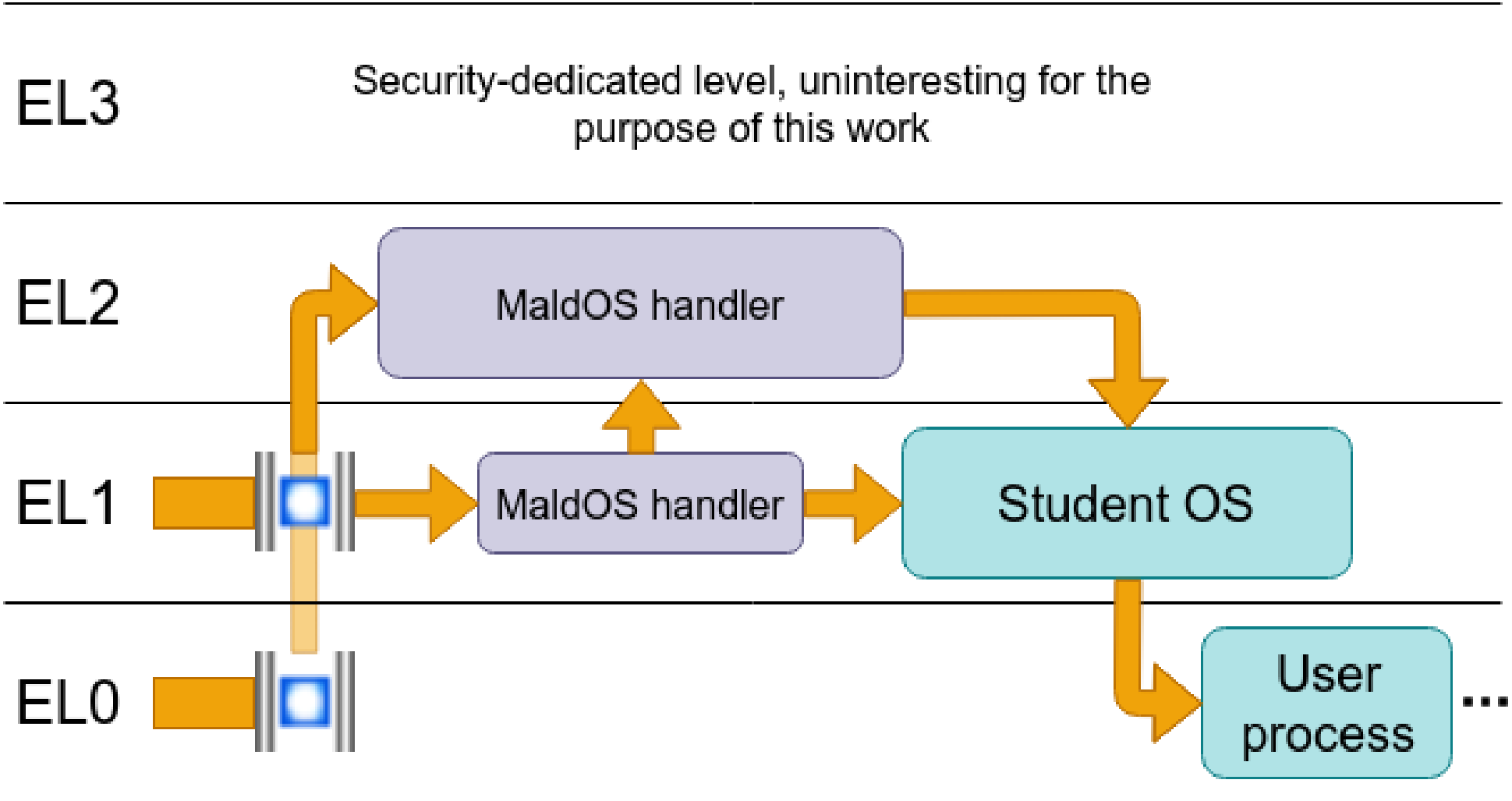
- it is an extremely practical example, and the result can be run on a real machine
- it relies on general purpose software (namely, Qemu and GDB)
- its debugging facilities are less specific but more powerfull (full GDB support)
- ARM is a more modern and widespread environment

Bare Metal

The problem with a real architecture is the intrinsic complexity of hardware management. The abstraction layer softens the approach by partially taking care of initialization and configuration procedures.



Interrupt Management



Emulated Devices

MaldOS creates μMPS-style emulated devices with an ideal interface. Thanks to the mailbox mechanism the integration is seamless.

Virtual Disk device registers
status register
command register
data 0 register
data 1 register
mailbox register

EMMC device registers
second argument register
block size count register
first argument register
command register
response registers (4)
status register
control register (2 of 3)
interrupt register
interrupt mask register

... 11 more