

System Bus of 8086(Data + Control)



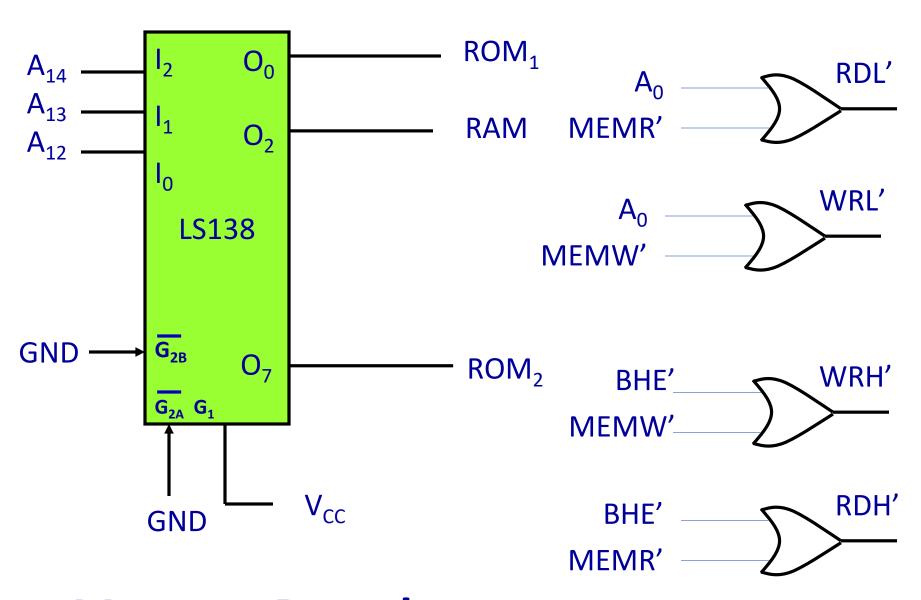


M/IO'	RD'	WR'	Bus cycle
1	0	1	MEMR'
1	1	0	MEMW'
0	0	1	IOR'
0	1	0	IOW'

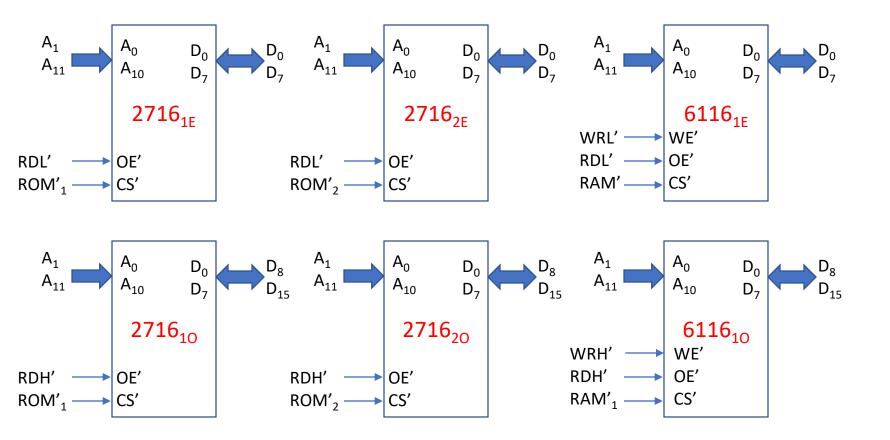


# **Strobe Signals**

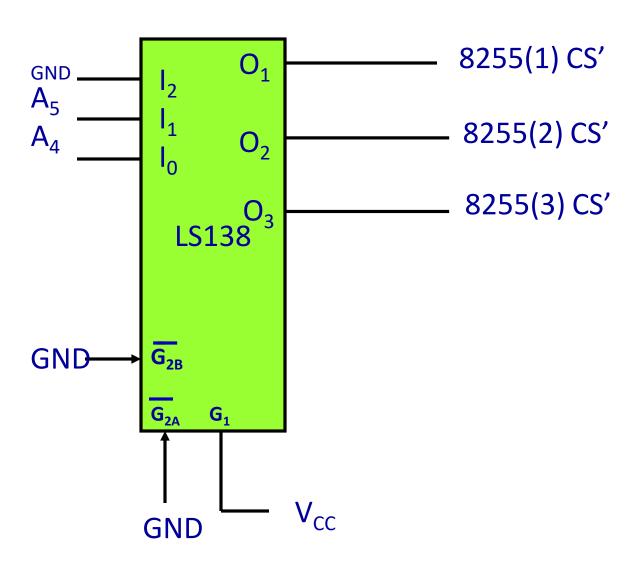
<u>back</u>



#### **Memory Decoder**

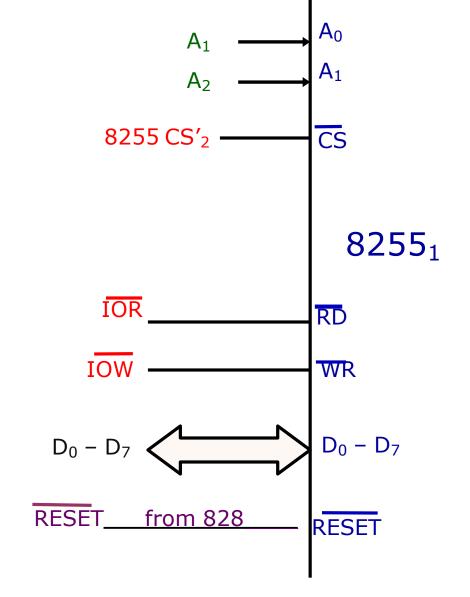


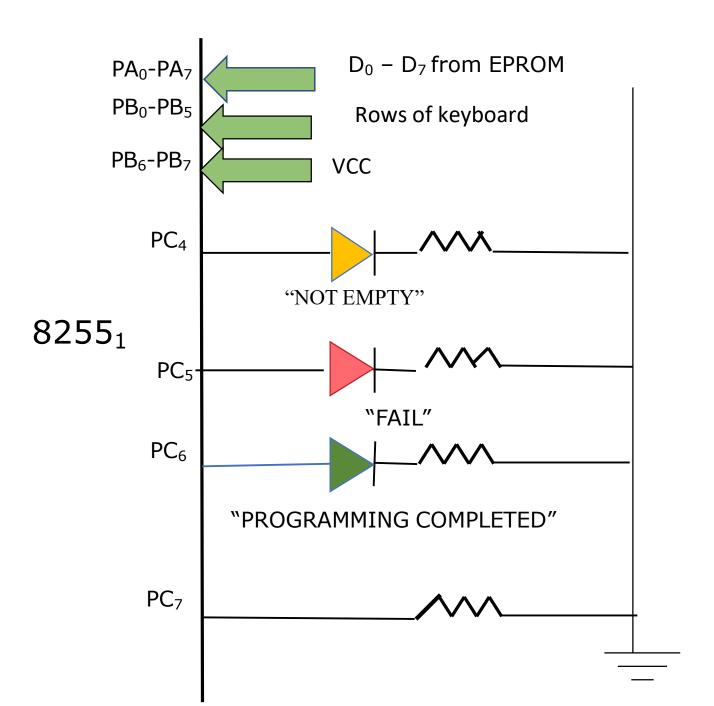
#### **Memory Interfacing**

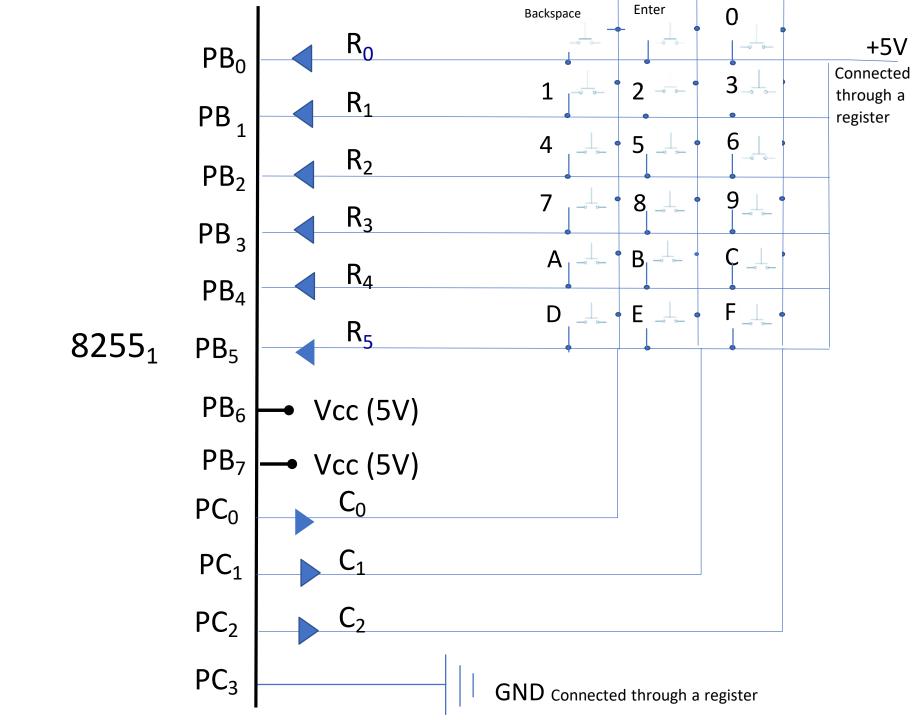


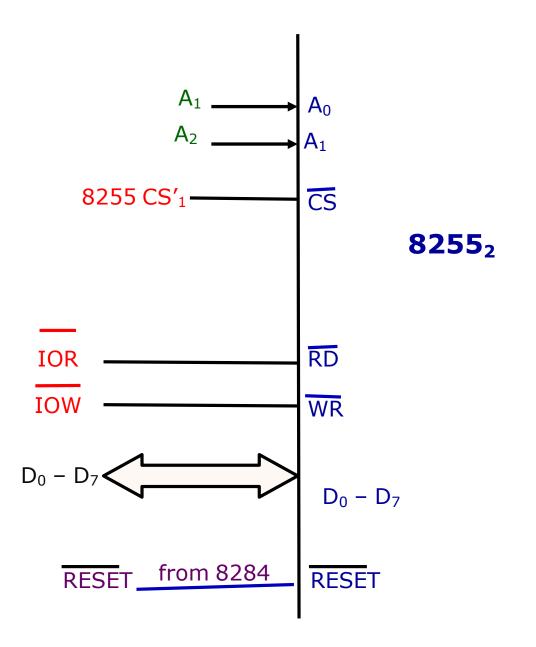
## I/O Decoder

### $8255_1$ Interface to the processor



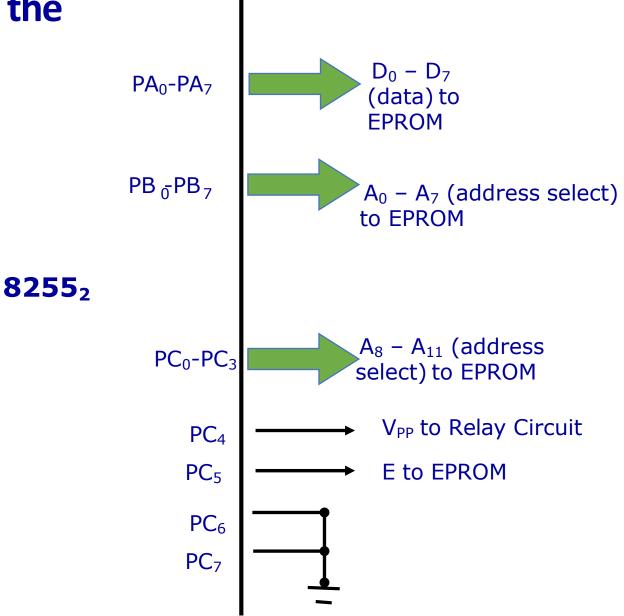




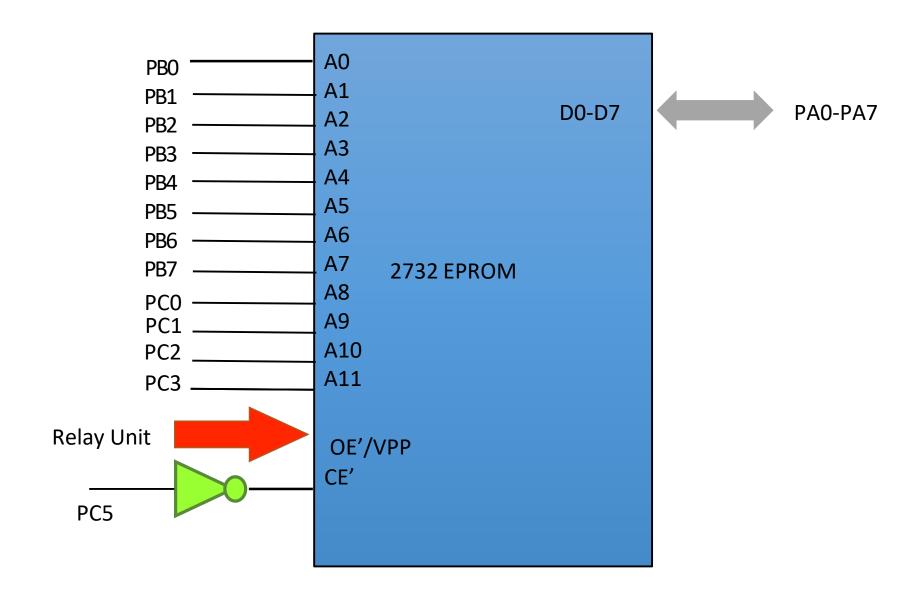


8255(2) Interface to the processor

# 8255<sub>2</sub> Interface to the EPROM

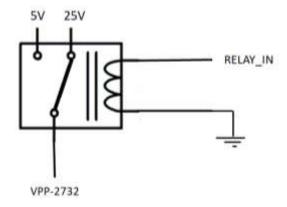


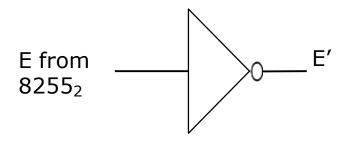
#### **2732 EPROM** (Ports from 8255(**2**))



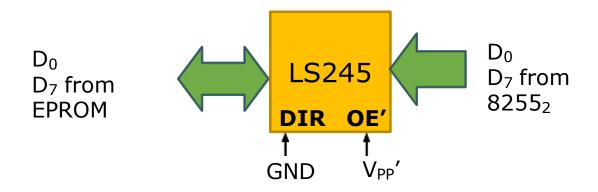
# Relay circuit and EPROM

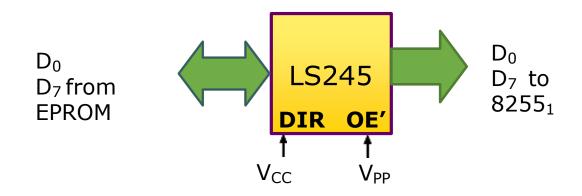
#### Relay



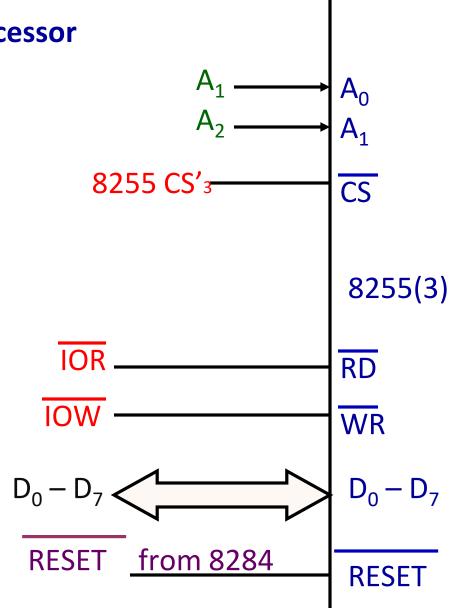


## **EPROM Data Lines Interfacing**

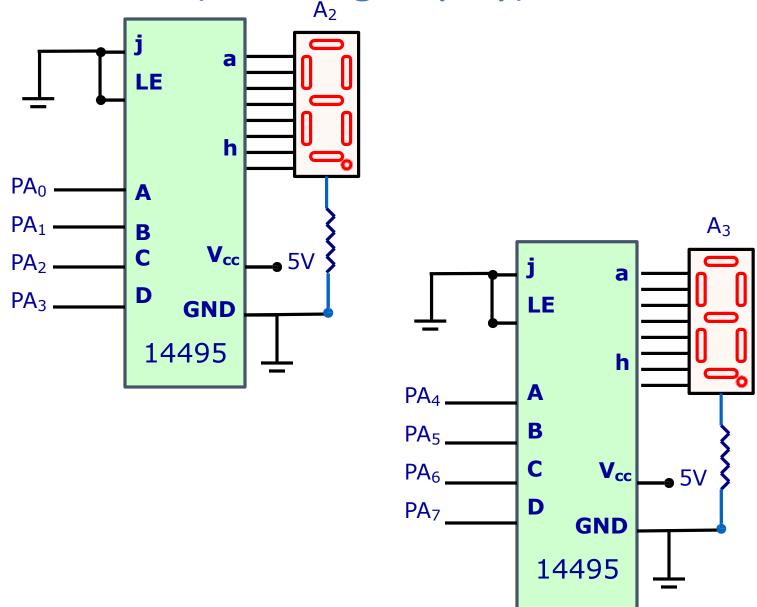




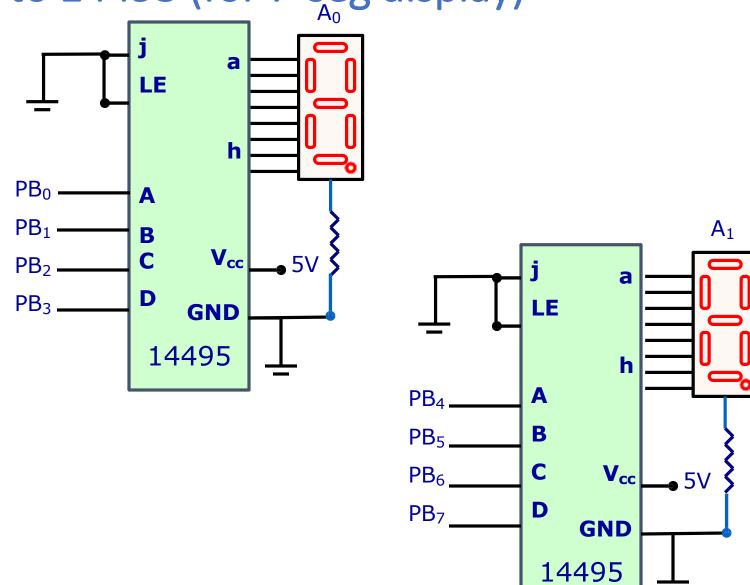
#### **8255**<sup>3</sup> Interface to the processor



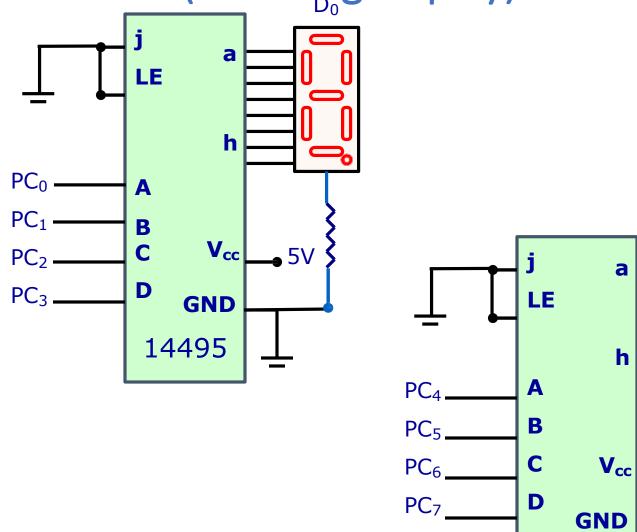
### 8255<sub>3</sub> to 14495 (for 7-seg display)



# 8255<sub>3</sub> to 14495 (for 7-seg display)



# 8255<sub>3</sub> to 14495 (for 7-seg display)



 $\mathsf{D}_1$ 

14495