## CPU and system specifications

The 'Iscpu' command gives the following output:

Architecture: x86\_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

Address sizes: 48 bits physical, 48 bits virtual

CPU(s): 24

On-line CPU(s) list: 0-23 Thread(s) per core: 2 Core(s) per socket: 12

Socket(s): 1

NUMA node(s): 1

Vendor ID: AuthenticAMD

CPU family: 25

Model: 33

Model name: AMD Ryzen 9 5900X 12-Core Processor

Stepping: 0

Frequency boost: enabled

CPU MHz: 3700.000

CPU max MHz: 4950.1948 CPU min MHz: 2200.0000

BogoMIPS: 7386.83 Virtualization: AMD-V L1d cache: 384 KiB L1i cache: 384 KiB L2 cache: 6 MiB L3 cache: 64 MiB

NUMA node0 CPU(s): 0-23

From the output we can see the following:

- 1. specific make of the CPU AMD Ryzen 9 5900X 12-Core Processor
- 2. Base frequency 3.7 GHz
- 3. Maximum frequency 4.9 GHz
- 4. Physical cores 12
- 5. Thread 24

Cache sizes and how they are shared

- 1. L1 data 384 KB or 32 KB/core
- 2. L1 instruction 384 KB or 32 KB/core
- 3. L2 6 MB or 512 KB/core unified caches
- 4. L3 64 MB or 2x32 KB set associative

It should be noted memory is installed in only one channel which makes the peak memory bandwidth:

Peak memory bandwidth (Single) - 23.84 GB/s (Memory or RAM, not the CPU cache) Peak GFLOPs per core - 59.2 GFLOPS/core (Theoretical, formula can be found here https://en.wikipedia.org/wiki/FLOPS)

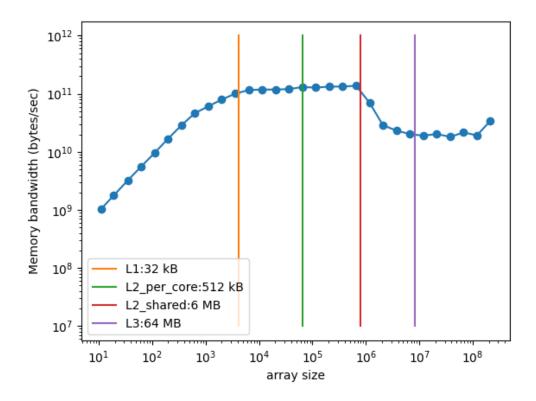
Link to specification sheets:

https://www.amd.com/en/product/10461 https://en.wikichip.org/wiki/amd/ryzen\_9/5900x https://boinc.bakerlab.org/rosetta/cpu\_list.php

## **Benchmarks**

## Memory Bandwidth

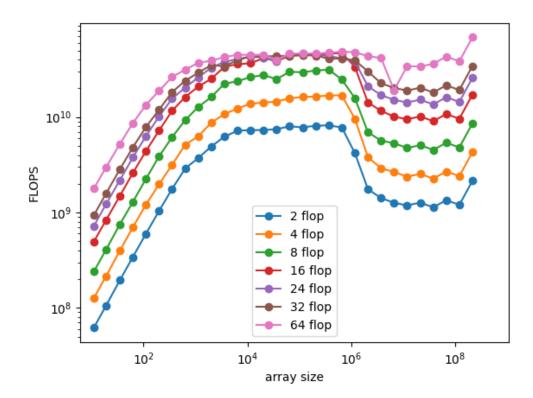
The plot for memory bandwidth vs size of the array is run as follows



The peak memory bandwidth of the CPU was obtained as 127.52 GB/s which occurs during the transition from L1 to L1 cache. It starts dropping as the array size increases. When we come to memory installed, the single channel bandwidth is approximately 20.55 GB/s which is 86% of what is given in the specification sheet.

## **FLOPS**

The plot for FLOPS vs size of the array for various no.of floating point operations is as follows



The peak GFLOPS per core recorded was 48.5508 GFLOPS/core which is approximately 82 percent of the theoretical value. For higher FLOP operations the results are weird once it is pushed into memory hinting at distribution between various pipelines of different cores.

To obtain these results I ran the benchmarking code with *governor* set to performance mode and used the taskset command to specify which CPU the program should use.

The code to generate these results can be found here: https://github.com/Malyadeep/checkCPUperf