

MANAJ MOHAPATRA

Software Engineer

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EXPERIENCE

Software Engineer

IMH Team, Texas Instruments India

📅 Aug 2016 – Present 📍 Bangalore, India

- Developer of an in-house python based software application used for device control, data capture, visualization, analysis, logging and UI creation.
- Working with a team on design and development of automation tool that will work on embedded linux. This tool will be used for high rate data transmission.
- Developer of a register mapping tool that captures device information during design cycle and can be used for export information in required format for further use.

AREA OF INTEREST

- Software Automation
- Embedded Systems

SKILLS

C++, Python, Qt, HTML, CSS, JavaScript ●●●●●●
C, Embedded Linux, Git, MATLAB, Octave ●●●●●●

HARDWARE

FTDI devices, ARM Cortex-M4 ●●●●●●
Zynq zc-706, Digilent Nexys 4, Basys 3 ●●●●●●

EDUCATION

Master of Technology

VLSI Design & Embedded Systems

National Institute of Technology, Rourkela

📅 May 2015 – May 2016 📍 Odisha, India

Bachelor of Technology

Electronics & Instrumentation Engineering

National Institute of Technology, Rourkela

📅 Aug 2011 – May 2015 📍 Odisha, India

AWARD



Automation Champion of the Year
2017 in IMH product Group of Texas
Instruments India

INTERNSHIPS

CFAR model design using MATLAB
and SIMULINK software

DRDO

📅 May 2014 – July 2014

Hardware implementation of Power
meter using XILINX-ISE tool

DRDO

📅 May 2013 – June 2013

COURSES

Introduction to C++

edX

📅 Sept 2015 – Oct 2015

Modelling and Simulation using
MATLAB

Iverson

📅 Apr 2014 – Aug 2014

PROJECT

FPGA Implementation of PAPR Reduction technique in OFDM

- This project aimed to study of Peak-to-Average Power Ratio (PAPR) reduction techniques and develop a model that can maintain high speed data transmission with minimum PAPR.
- Proposed a technique that calculates PAPR using different transformations and sends the most efficient among them with transformation information.