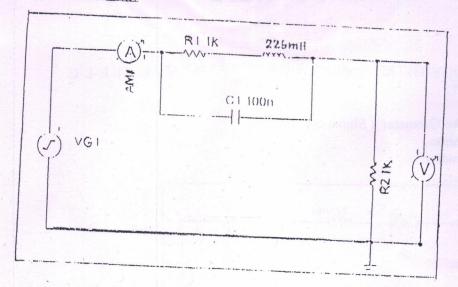
CIRCUIT DIAGRAM COL



Parallel R-L-C Circuit

PROCEDURE:

- 1. Rig up the test circuit as shown in circuit diagram 2, use the component values indicated in the circuit.
- 2. Adjust the signal generator controls so that its output is a sine wave of amplitude 0.02 V and frequency is 10 HZ.
- 3. Apply this input to the test circuit and record the amplitude of output voltage.
- 4. Repent last step for different frequencies mentioned in table 2.

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-	I'RI	QUENCY (HZ.)	CAT IPPERA LANGUAGE
1		1173.	OUTPUT (mV.)
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Table 2.

Plot a graph between output voltage and frequency. Finally note minimum value of voltage, : frequency and half power frequencies from the graph.

OBSERVATIONS:

Minimum voltage		
Resonance Freq.	··········	mV.
Li-16	***********	KH7
mit power trea. (lower)	944	* * * * * * * *
Half power freq. (upper)	***************************************	KHZ

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