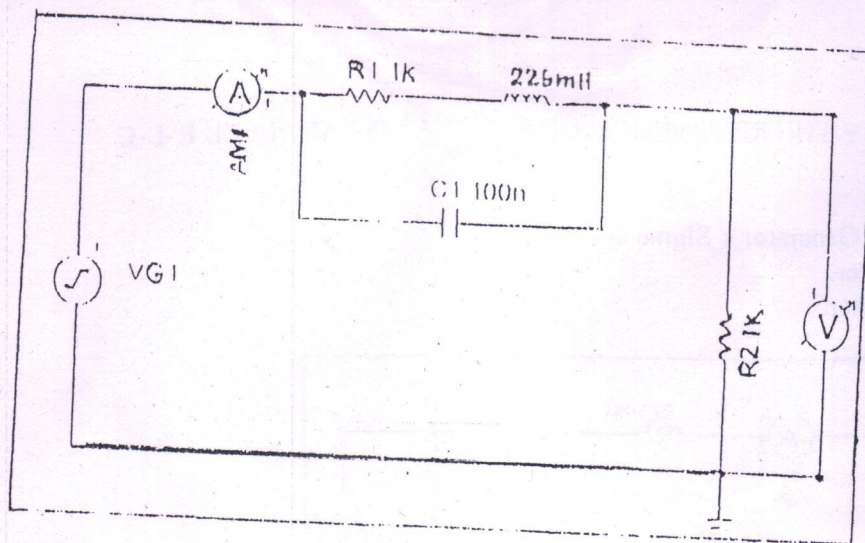


### CIRCUIT DIAGRAM (2):



Parallel R-L-C Circuit

### PROCEDURE:

1. Rig up the test circuit as shown in circuit diagram 2, use the component values indicated in the circuit.
2. Adjust the signal generator controls so that its output is a sine wave of amplitude 0.1V and frequency is 10 HZ.
3. Apply this input to the test circuit and record the amplitude of output voltage.
4. Repeat last step for different frequencies mentioned in table 2.

S. NO.	FREQUENCY (HZ.)	OUTPUT (mV.)

Table 2.

Plot a graph between output voltage and frequency. Finally note minimum value of voltage, resonance frequency and half power frequencies from the graph.

### OBSERVATIONS:

Minimum voltage = ..... mV.  
 Resonance Freq. = ..... KHZ.  
 Half power Freq. (lower) = ..... KHZ.  
 Half power Freq. (upper) = ..... KHZ.