Manav Sachin Patel

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PROFESSIONAL SUMMARY

Final-year Computer Science student at Rutgers University with strong foundations in data structures, algorithms, databases, and computer systems. Skilled in Java, Python, C, SQL, and Assembly, with proven experience through academic projects in data analysis, reverse engineering, and database design. Seeking internship or entry-level opportunities in software development, data engineering, or systems programming.

EDUCATION

Rutgers University – New Brunswick, NJ
- B.S. in Computer Science · 2023–Present
- Expected Graduation: May 2026
- GPA: 3.129 | Dean's List: Fall 2024
- Key Coursework: Data Structures, Discrete Structures I & II, Computer Architecture, Database Management, Linear Algebra

San Francisco State University – San Francisco, CA
- B.S. in Computer Science · Fall 2022 – Spring 2023
- GPA: 3.5

TECHNICAL SKILLS

- Languages: Python, Java, C, SQL, x86 Assembly
- Libraries/Tools: Pandas, NumPy, matplotlib, Jupyter Notebook, VSCode, GDB, objdump
- Core Concepts: Data Structures & Algorithms, Database Management, Computer Architecture, Discrete Mathematics

PROJECTS

Spider-Verse Data Structures Project (Java)
- Implemented graph algorithms: adjacency lists, hash tables with rehashing, DFS, BFS, and Dijkstra's algorithm.

Music Database Project (SQL)
- Designed and normalized a relational schema for a music platform (artists, albums, songs, playlists, users, ratings).
- Wrote 10 SQL queries to analyze genres, user activity, and popular content.

Data Analysis Projects (Python)
- Cleaned and transformed datasets (EU Cities, German Credit, Google Playstore Apps).
- Built visualizations to identify credit risk, app performance, and climate trends.

Bomb Lab – Reverse Engineering Challenge (C, Assembly | Tools: GDB, objdump)
- Reverse-engineered a compiled binary to trace memory, understand control flow, and decode logic.
- Successfully defused all phases while gaining experience in debugging and system-level execution.

Truth Table Generator – Logic Circuit Simulator (C)
- Developed a truth table simulator to evaluate AND, OR, NOT, XOR, Multiplexer, and Decoder gates.
- Implemented bitwise logic, file I/O, and dynamic evaluation of logical circuits.

PROFESSIONAL DEVELOPMENT

- Web Designing — Elite Academy of Information & Technology, 2022
- Object-Oriented Programming with Java — Elite Academy of Information & Technology, 2022
- C Programming — Elite Academy of Information & Technology, 2021