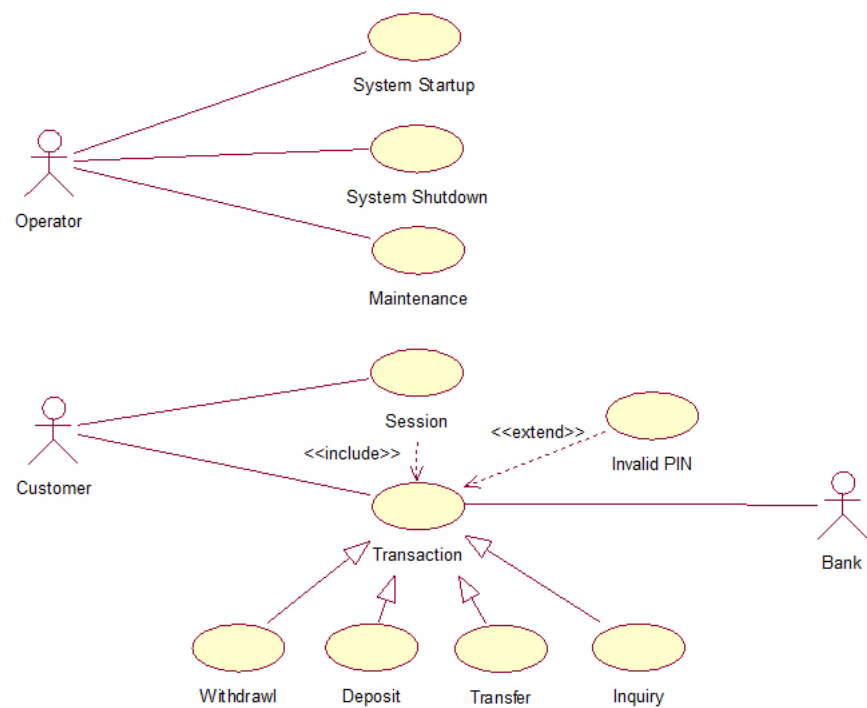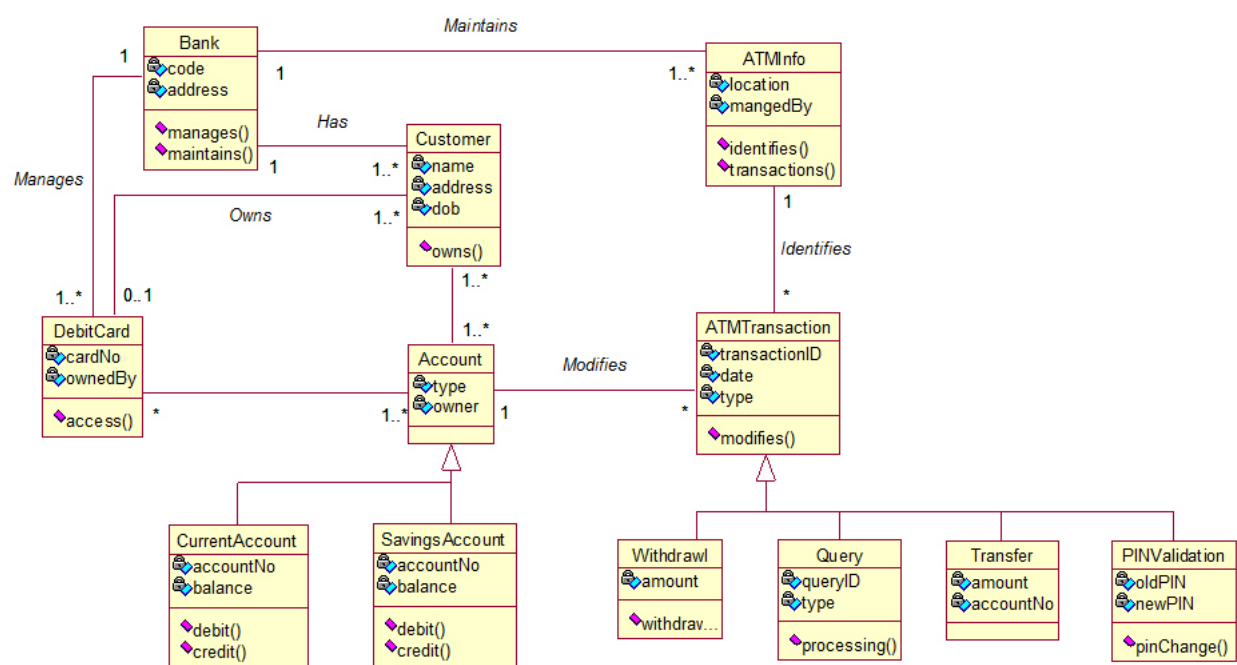# Use case diagram

Class diagram

Sequence diagram

(https://www.startertutorials.com/uml/wp-content/uploads/2013/10/ATM-sequence-diagram.jpg)

Collaboration diagram

(https://www.startertutorials.com/uml/wp-content/uploads/2013/10/ATM-collaboration-diagram.jpg)
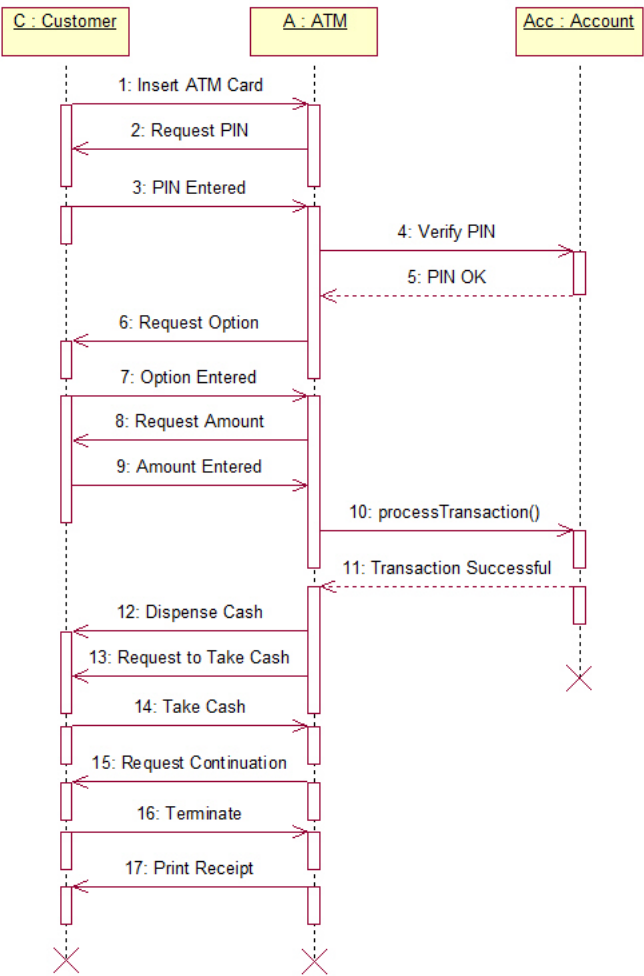
Statechart diagram

(https://www.startertutorials.com/uml/wp-content/uploads/2013/10/ATM-statechart-diagram.jpg)
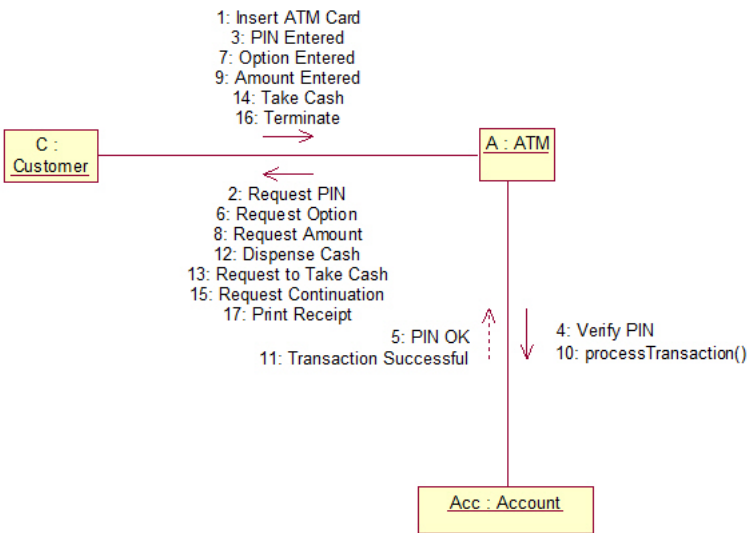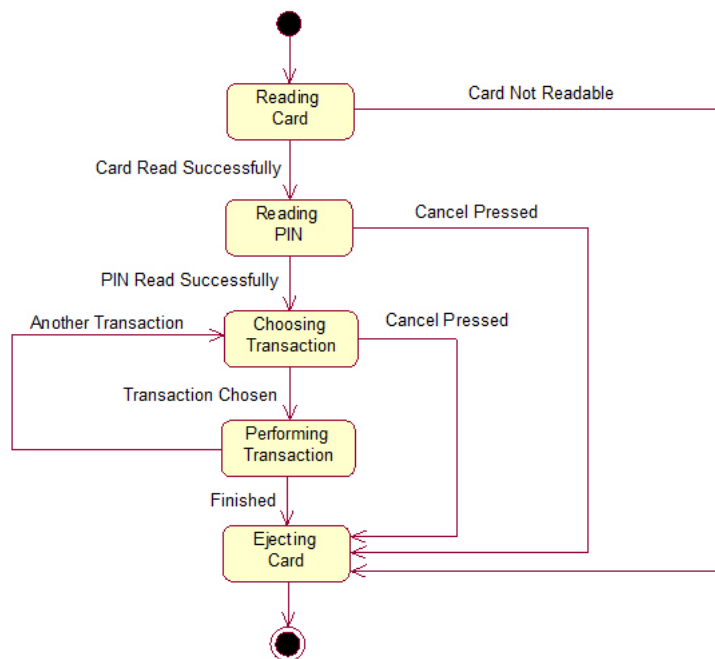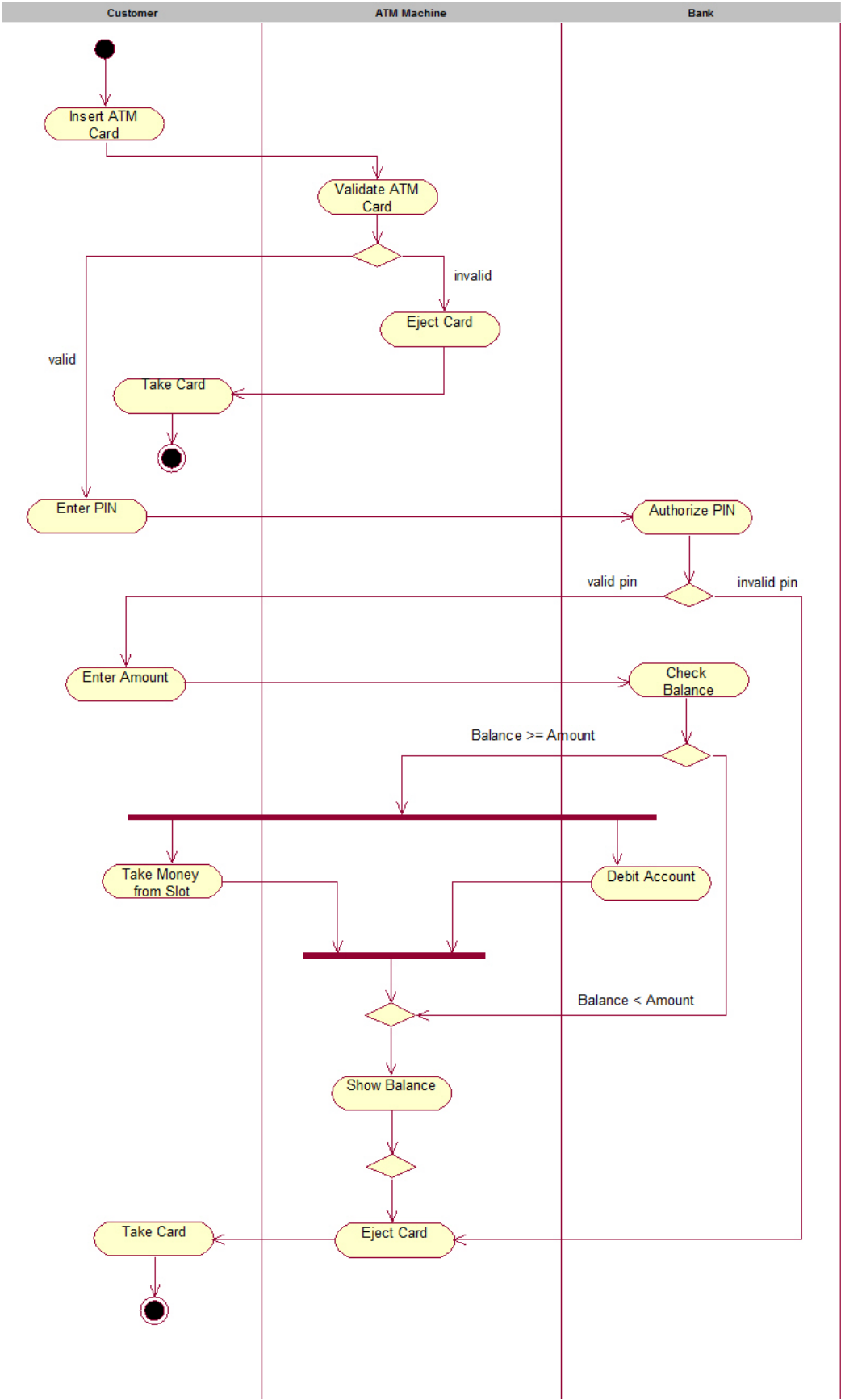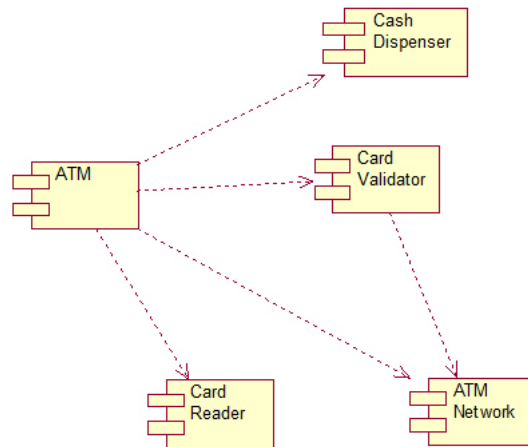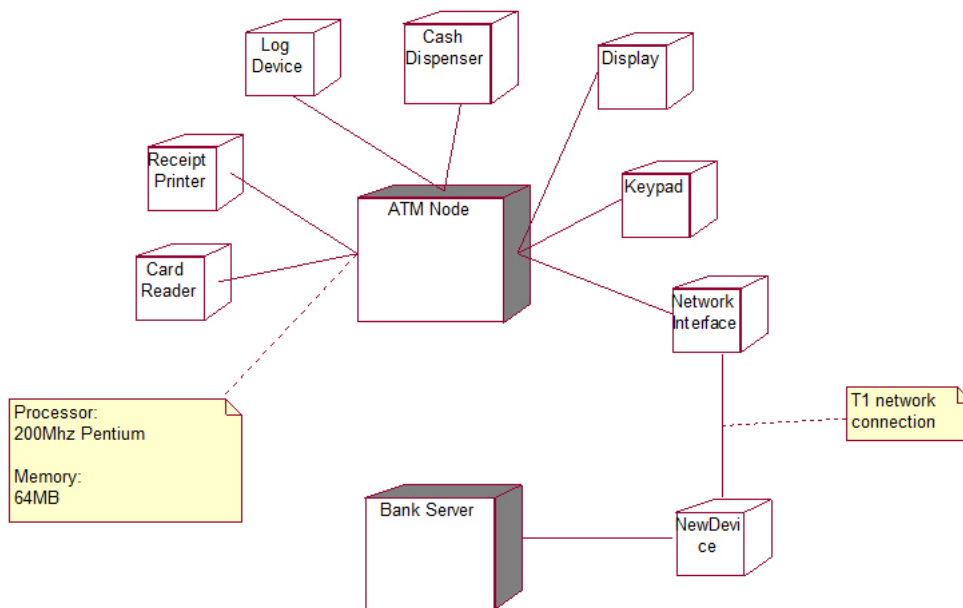
Activity diagram

(https://www.startertutorials.com/uml/wp-content/uploads/2013/10/ATM-activity-diagram.jpg)

Component diagram

(https://www.startertutorials.com/uml/wp-content/uploads/2013/10/ATM-component-diagram.jpg)

Deployment diagram

(https://www.startertutorials.com/uml/wp-content/uploads/2013/10/ATM-deployment-diagram.jpg)

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Suryateja Pericherla, at present is a Research Scholar (full-time Ph.D.) in the Dept. of Computer Science & Systems Engineering at Andhra University, Visakhapatnam. Previously worked as an Associate Professor in the Dept. of CSE at Vishnu Institute of Technology, India.

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