



## ***PUBLISHING AGREEMENT***

**AGREEMENT** made this 25th day of April, 2024, by and between **Elsevier Inc.**, with offices at 50 Hampshire St., 5th Floor, Cambridge, MA 02139, USA (the “Publisher”), and

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(collectively, the “Editor”).

**WHEREAS**, the Work (defined below) will be composed of a number of chapters or articles (the “Contributions”) written by a number or a group of contributors (the “Contributors”); and

**NOW, THEREFORE**, the parties agree to the publication by the Publisher of that certain work tentatively entitled **EXPLORING HARDWARE ADVANCES: FPGA, IOT, MICROCONTROLLERS, AND QUANTUM TECHNOLOGIES**, as more fully described below upon the terms and conditions set forth below: