

SASTRA UNIVERSITY

Shanmugha Arts, Science, Technology & Research Academy
(A University u/s 3 of the UGC Act, 1956)

Tifac Core in Advanced Computing & Information Processing

Upon the recommendation of the Faculty and by the Authority of the Board of Management acting under the powers vested by the Government of India, the University confers on

V. Manikandan

the degree of Master of Technology in VLSI Design

and has placed him in First Class with Distinction in the Examination held in May, 2009.

In witness whereof, we have hereunto affixed our signatures and the Seal of the University, this day, the Tenth of October, 2009 at the University Campus, Thanjavur, India.

Dean

Registrar

Proce-Chancellor

Reg.No.





SASTRA UNIVERSITY

(A University established under section 3 of the UGC Act, 1956)

Thanjavur - 613 402, Tamil Nadu, India.

Cumulative Grade Sheet

M.Tech Degree Examinations 2007 - 2009

Register Number: 010940004

Name of the Candidate: Manikandan, V

Programme: VLSI Design

Course Code	Name of the Subject / Course	Semester	Credit	Grade	Month & Year of Passing
RTS, SCIENCE, TEC	Semester-1	TS, SCIENCE, TE	CHMOTOR	& RESEA	REH ACADEMY (SA
MVLD101R01	BASICS OF VLSI	SCIENCE, TE	4	C	Nov -2007
MVLD102R01	SEMICONDUCTOR PHYSICS AND PROCESSING	1 Ice Ti	CH 4 06	В	Nov -2007
MVLD103R01	MODELLING OF DIGITAL SYSTEMS USING HDL	O I SE, TE	CHN4.0G	A ASE	Nov -2007
MVLD104E01	COMPUTER ARCHITECTURE	1	3	В	Nov -2007
MVLD105E10	DIGITAL SIGNAL PROCESSING SYSTEMS	1 7	CHR3LOG	& BSE	Nov -2007
MVLD106R01	MODELLING LAB	1	2	S	Nov -2007
MVLD107R02	DESIGN LAB - I	1	chadios	В	Nov -2007
	Semester-2		NOTOR	& RESEA	RCH ACADEMY (SA
MVLD201R01	VLSI SYSTEM DESIGN AND TESTING	2	4	Α	May -2008
MVLD202R01	ANALOG VLSI	2	4 10	& Ase	May -2008
MVLD203R01	CAD FOR VLSI	2	3	S	May -2008
MVLD204E03	APPLICATION SPECIFIC INTEGRATED CIRCUITS	2	3	A	May -2008
MVLD205E11	EMBEDDED SYSTEMS DESIGN	2	CHN3LGG	& S	May -2008
MVLD206R01	DESIGN LAB - II	2	CHARLEGE CHARLEGE	A	May -2008
MVLD207R01	MINI PROJECT	SGIE 2 TE, TI	CHM2 06	A A S	May -2008
MVLD208R01	SEMINAR	2	2	A	May -2008
	Semester-3 & 4	RTS SCIENCE, TE	CHAOLOG	& RESE	RCH ACADEMY (SY
MVLD301R01	PROJECT WORK & VIVA VOCE	3 & 4	20	S	May -2009

Total Credits: 63

1. Semesters 1 to 4 total CGPA secured 8.5873

HARRIUGHA ARTS, SCIENCE, TECHNOLOGY & RESEARCH ACADEMY (SASTRA)

Place: SASTRA, THANJAVUR.

Date : 12-06-2009

HANMUGHA

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Controller of Examinations