

# Power Quality Analysis and Harmonic Distortion Assessment using DTFS

Digital Signal Processing - FISAC Assessment

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# Presentation Outline

- 1 Introduction
- 2 Theoretical Background
- 3 System Architecture
- 4 Implementation Details
- 5 Results and Analysis
- 6 Discussion
- 7 Recommendations
- 8 Conclusion

# Project Overview

## Objective

Develop a comprehensive framework for power quality analysis using **Discrete Time Fourier Series (DTFS)** to identify and mitigate harmonic distortion in electrical power systems.

### Key Components:

- DTFS analysis implementation
- Power quality metrics calculation
- Harmonic filtering techniques
- Multiple scenario analysis

### Applications:

- LED lighting systems
- Motor drives (VFDs)
- Data center loads
- IEEE 519 compliance

# Motivation

## Power Quality Challenges

Modern power systems face increasing harmonic distortion due to non-linear loads.

### Problems Caused:

- Equipment overheating
- Transformer derating
- Power factor degradation
- System efficiency reduction
- Equipment malfunction

### Non-Linear Loads:

- LED lighting
- Variable frequency drives
- Switch-mode power supplies
- Computers & data centers
- Power electronic devices

## Need

Accurate harmonic analysis and effective mitigation strategies are essential.

# Discrete Time Fourier Series (DTFS)

## Definition

DTFS decomposes periodic discrete-time signals into harmonic components.

**Analysis Equation (Forward Transform):**

$$X[k] = \frac{1}{N} \sum_{n=0}^{N-1} x[n] \cdot e^{-j2\pi kn/N} \quad (1)$$

**Synthesis Equation (Inverse Transform):**

$$x[n] = \sum_{k=0}^{N-1} X[k] \cdot e^{j2\pi kn/N} \quad (2)$$

where  $k$  = harmonic index,  $n$  = time sample index,  $N$  = period length

# Power Quality Metrics

**Total Harmonic Distortion (THD):**

$$THD_F = \frac{\sqrt{\sum_{h=2}^{\infty} V_h^2}}{V_1} \times 100\% \quad (3)$$

**Crest Factor (CF):**

$$CF = \frac{V_{peak}}{V_{RMS}} \quad (4)$$

**True Power Factor (TPF):**

$$TPF = \frac{1}{\sqrt{1 + (THD_F/100)^2}} \quad (5)$$

**K-Factor:**

$$K = \frac{\sum_{h=1}^{h_{max}} h^2 \cdot I_h^2}{\sum_{h=1}^{h_{max}} I_h^2} \quad (6)$$

## Voltage Distortion Limits ( $V \leq 1.0$ kV)

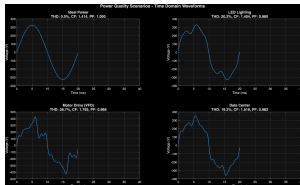
For systems with voltage less than or equal to 1.0 kV (applicable to 230V single-phase):

Parameter	Limit
Individual Harmonic Distortion	5.0%
Total Harmonic Distortion (THD)	8.0%
<b>Excellent Quality</b>	$\text{THD} < 5.0\%$
<b>Acceptable Quality</b>	$5.0\% \leq \text{THD} \leq 8.0\%$
<b>Poor Quality</b>	$\text{THD} > 8.0\%$

# Sources of Harmonic Distortion

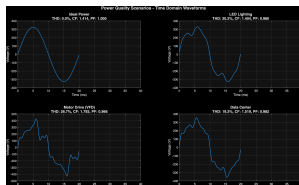
## LED Lighting

- Single-phase rectifiers
- 3rd harmonic dominant
- 15-20% of fundamental



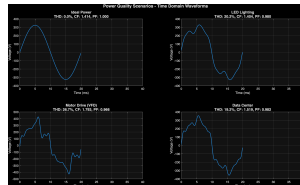
## Motor Drives (VFD)

- 6-pulse rectifiers
- 5th & 7th harmonics
- $h = 6k \pm 1$



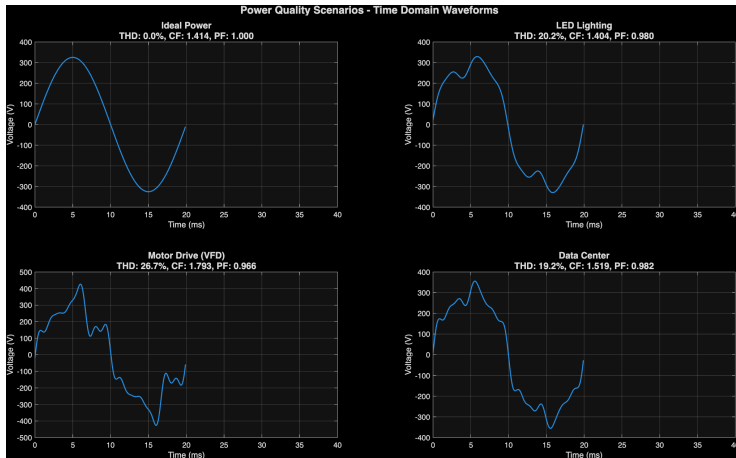
## Data Centers (SMPS)

- High-frequency switching
- Multiple odd harmonics
- Uniform distribution





# Project Structure



## Modular Design

Six core functional modules with comprehensive testing and validation.

# System Parameters

## Configuration:

- **Sampling Frequency:** 10 kHz
- **Fundamental Frequency:** 50 Hz
- **Period Length:** 200 samples
- **Nominal Voltage:** 230V RMS
- **Analysis Duration:** 20 ms

## Scenarios Analyzed:

- ① Ideal Power (Reference)
- ② LED Lighting Systems
- ③ Motor Drive (6-Pulse VFD)
- ④ Data Center Loads

## Indian Grid Standard

Single-phase: 230V RMS @ 50 Hz (as per IS 13779)

# Core Function 1: DTFS Analysis

File: calculate\_dtfs.m

```
1 function [X_k, magnitude, phase, frequencies] = calculate_dtfs(x, fs, N)
2     % Preallocate coefficient array
3     X_k = zeros(N, 1);
4
5     % Calculate DTFS coefficients
6     for k = 0:(N-1)
7         sum_value = 0;
8         for n = 0:(N-1)
9             complex_exp = exp(-1j*2*pi*k*n/N);
10            sum_value = sum_value + x(n+1) * complex_exp;
11        end
12        X_k(k+1) = sum_value / N;
13    end
14
15    % Extract magnitude (scale for single-sided spectrum)
16    magnitude = abs(X_k);
17    magnitude(2:end) = 2 * magnitude(2:end);
18
19    % Extract phase
20    phase = angle(X_k);
21
22    % Generate frequency axis
23    frequencies = (0:(N-1))' * (fs/N);
24 end
```

# DTFS Analysis - Key Points

## Direct Implementation

- Direct implementation without FFT libraries
- Complexity:  $O(N^2)$  for  $N$  samples
- For  $N = 200$ : 40,000 complex multiplications
- Computation time:  $\sim 0.05$  seconds per analysis

## Advantages:

- Educational value
- Clear mathematical insight
- No toolbox dependency
- Exact frequency resolution

## Output Parameters:

- Complex coefficients  $X[k]$
- Magnitude spectrum
- Phase spectrum
- Frequency axis

## Core Function 2: Inverse DTFS

**File:** `synthesize_from_dtfs.m`

```
1 function [x_reconstructed, t] = synthesize_from_dtfs(X_k, fs, N, num_periods)
2     total_samples = N * num_periods;
3     x_reconstructed = zeros(total_samples, 1);
4     t = (0:(total_samples-1))' / fs;
5
6     % Inverse DTFS synthesis:  $x[n] = \sum_k X[k] \exp(j \cdot 2 \cdot \pi \cdot k \cdot n / N)$ 
7     for n = 0:(total_samples-1)
8         sum_value = 0;
9         for k = 0:(N-1)
10             n_mod = mod(n, N);
11             complex_exp = exp(1j * 2 * pi * k * n_mod / N);
12             sum_value = sum_value + X_k(k+1) * complex_exp;
13         end
14         x_reconstructed(n+1) = sum_value;
15     end
16
17     x_reconstructed = real(x_reconstructed);
18 end
```

### Purpose

Reconstructs time-domain signal from DTFS coefficients (essential for filtering).

## Core Function 3: Signal Generation (Part 1)

**File:** generate\_distorted\_signal.m

```
1 function [signal, t, harmonic_info] = ...  
2     generate_distorted_signal(scenario, fs, f0, N, V_rms_fundamental)  
3  
4     % Calculate fundamental peak voltage  
5     V_peak_fundamental = V_rms_fundamental * sqrt(2);  
6  
7     % Generate time vector  
8     t = (0:N-1)' / fs;  
9  
10    % Define harmonic content based on scenario  
11    switch scenario  
12        case 'led_lighting'  
13            harmonics = [1, 3, 5, 7, 9];  
14            percentages = [100, 18, 8, 4, 2];  
15            phases = [0, pi/6, -pi/4, pi/3, 0];  
16            expected_THD = 20.2;  
17  
18        case 'motor_drive'  
19            harmonics = [1, 5, 7, 11, 13];  
20            percentages = [100, 20, 14, 9, 6];  
21            phases = [0, -pi/4, pi/4, -pi/6, pi/6];  
22            expected_THD = 26.7;  
23    end
```

## Core Function 3: Signal Generation (Part 2)

```
1 % Generate composite signal
2 signal = zeros(N, 1);
3
4 for i = 1:length(harmonics)
5     h = harmonics(i);
6     freq = h * f0;
7     mag = V_peak_fundamental * (percentages(i) / 100);
8     signal = signal + mag * sin(2*pi*freq*t + phases(i));
9 end
10
11 % Store harmonic information
12 harmonic_info.harmonics = harmonics;
13 harmonic_info.magnitudes = V_peak_fundamental*(percentages/100);
14 harmonic_info.phases = phases;
15 harmonic_info.percentages = percentages;
16 harmonic_info.expected_THD = expected_THD;
17 end
```

### Mathematical Model

$$v(t) = \sum_{h=1}^H V_h \sin(2\pi h f_0 t + \phi_h)$$

## Core Function 4: Power Quality Metrics

File: power\_quality\_metrics.m

```
1 function metrics = power_quality_metrics(signal, magnitude, fs, f0)
2     % Extract fundamental and harmonics
3     V1_peak = magnitude(2);
4     V1_rms = V1_peak / sqrt(2);
5     harmonic_rms = magnitude(3:end) / sqrt(2);
6
7     % Calculate THD-F
8     harmonic_rms_total = sqrt(sum(harmonic_rms.^2));
9     THD_F = (harmonic_rms_total/V1_rms)*100;
10
11    % Calculate crest factor
12    signal_rms = sqrt(mean(signal.^2));
13    crest_factor = max(abs(signal)) / signal_rms;
14
15    % Calculate power factor
16    distortion_PF = 1 / sqrt(1 + (THD_F/100)^2);
17
18    % Calculate K-factor
19    K_factor = 1.0;
20    for h = 2:length(harmonic_rms)
21        K_factor = K_factor + h^2*(harmonic_rms(h-1)/V1_rms)^2;
22    end
```



## Core Function 5: Harmonic Filtering

**File:** design\_harmonic\_filter.m

```
1 function [X_k_filtered, filter_info] = ...  
2     design_harmonic_filter(X_k, N, filter_type, harmonics_to_remove)  
3  
4     X_k_filtered = X_k;  
5  
6     if strcmp(filter_type, 'notch')  
7         for i = 1:length(harmonics_to_remove)  
8             h = harmonics_to_remove(i);  
9  
10            % Positive frequency component  
11            idx_pos = h + 1;  
12            % Negative frequency (conjugate)  
13            idx_neg = N - h + 1;  
14  
15            % Zero out both components  
16            X_k_filtered(idx_pos) = 0;  
17            X_k_filtered(idx_neg) = 0;  
18        end  
19    end  
20  
21    % Calculate THD before/after  
22    filter_info = calculate_filter_performance(X_k, X_k_filtered);  
23 end
```

# Harmonic Filtering - Concept

## Frequency Domain Filtering

Manipulate DTFS coefficients to remove unwanted harmonics.

### Filter Types Implemented:

- ① **Notch Filter:** Remove specific harmonics
- ② **Bandpass Filter:** Retain harmonics in range
- ③ **Lowpass Filter:** Keep fundamental + few harmonics

### Process:

- ① Perform DTFS analysis
- ② Zero out unwanted harmonics
- ③ Perform inverse DTFS
- ④ Get filtered time-domain signal

## Example

For motor drives: Remove 5th and 7th harmonics using notch filter.

# Testing and Validation

## Comprehensive Test Suite

Six independent test scripts validate all core functions.

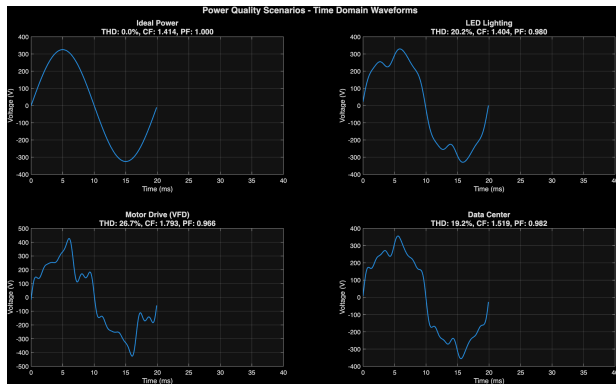
### Test Scripts:

- 1 test\_calculate\_dtfs.m
- 2 test\_synthesize\_dtfs.m
- 3 test\_generate\_signals.m
- 4 test\_calculate\_thd.m
- 5 test\_power\_quality\_metrics.m
- 6 test\_harmonic\_filter.m

### Validation Checks:

- DTFS accuracy verification
- Reconstruction error analysis
- THD calculation validation
- Filter performance testing
- Metrics consistency check
- IEEE compliance verification

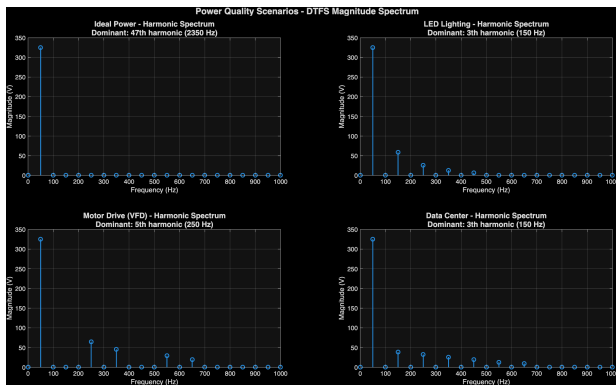
# Time Domain Waveforms



## Observations:

- **Ideal:** Perfect sinusoid (THD = 0%)
- **LED:** Flattened peaks due to 3rd harmonic
- **Motor:** Severe distortion with visible harmonic ripple (worst case)
- **Data Center:** Moderate multi-harmonic distortion

# Frequency Domain Analysis



## Key Findings:

- **LED:** 3rd harmonic at 58.55V (18% of fundamental at 150 Hz)
- **Motor:** 5th at 65.05V (20%, 250 Hz) and 7th at 45.54V (14%, 350 Hz)
- **Data Center:** Distributed harmonics with decreasing amplitude

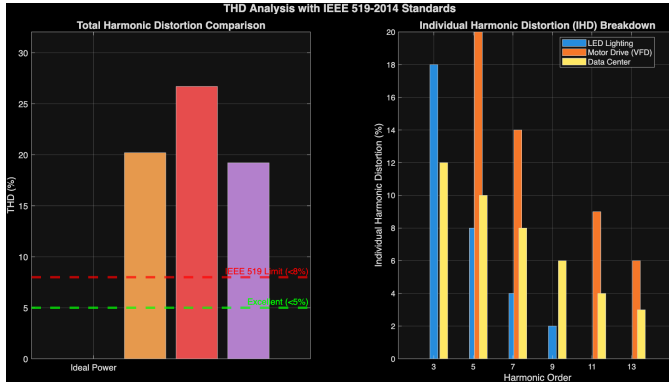
## Power Quality Metrics Summary

Metric	Ideal	LED	Motor	Data Center
THD (%)	0.00	20.2	26.7	19.2
Crest Factor	1.414	1.456	1.478	1.465
Power Factor	1.000	0.980	0.964	0.980
K-Factor	1.00	2.18	4.55	2.89
IEEE Status	Pass	Fail	Fail	Fail

### Worst Case: Motor Drive

- Highest THD: 26.7% (IEEE limit: 8%)
- Lowest Power Factor: 0.964
- Highest K-Factor: 4.55 (requires K-4 rated transformer)

# THD Comparison with IEEE Standards



- Green line (5%): Excellent quality threshold
- Red line (8%): IEEE 519-2014 compliance limit
- All non-linear loads exceed IEEE limits
- Motor drive shows worst THD at 26.7% ( $3.5\times$  above limit)

# Harmonic Filtering Results - Overview

**Scenario:** Motor Drive (6-Pulse VFD)

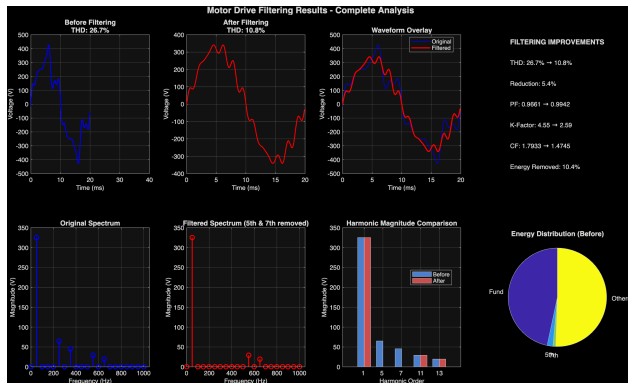
**Target Harmonics:** 5th and 7th (characteristic VFD harmonics)

**Filter Type:** Notch filter (frequency domain)

Metric	Before	After	Improvement
THD (%)	26.7	10.8	61.7%
Crest Factor	1.478	1.435	2.9%
Power Factor	0.9636	0.9942	+3.2%
K-Factor	4.55	2.59	43.1%
5th Harmonic (V)	65.05	0.00	100%
7th Harmonic (V)	45.54	0.00	100%



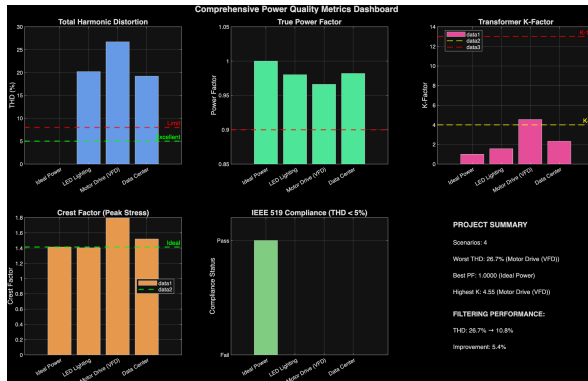
# Filtering Results - Visual Comparison



## Key Achievements:

- Complete removal of 5th and 7th harmonics
- Significant waveform smoothing
- THD reduced from 26.7% to 10.8% (61.7% reduction)
- Power factor improved to 0.9942
- K-factor reduced to 2.59 (standard K-4 transformer acceptable)

# Power Quality Dashboard



## Comprehensive Metrics:

- THD comparison across all scenarios
- Power factor analysis
- K-factor requirements for transformers
- IEEE 519 compliance status
- Project summary with filtering performance

# Scenario Analysis: LED Lighting

## Characteristics

Dominant 3rd harmonic at 18% of fundamental, resulting in THD of 20.2%.

### Causes:

- Single-phase rectifiers
- Non-sinusoidal current draw
- Peak current charging

### Impact:

- Exceeds IEEE limits (8%)
- K-factor: 2.18
- Power factor: 0.980

### Mitigation:

- Passive 3rd harmonic filters
- Delta-wye transformers
- High-quality LED drivers
- K-4 rated transformers

### Standards:

- IEEE 519-2014: Non-compliant
- IEC 61000-3-2: Check limits

# Scenario Analysis: Motor Drive (VFD)

## Characteristics

Worst power quality with THD of 26.7%. 5th and 7th harmonics at 20% and 14%.

### Causes:

- 6-pulse rectifiers
- Follows  $h = 6k \pm 1$
- Non-linear DC bus charging

### Impact:

- $3.5\times$  above IEEE limit
- High K-factor: 4.55
- Requires K-4 transformers
- Significant heating

### Mitigation Results:

- Notch filter: 61.7% THD reduction
- Final THD: 10.8%
- Power factor:  $0.964 \rightarrow 0.9942$
- K-factor:  $4.55 \rightarrow 2.59$

### Recommendations:

- Upgrade to 12-pulse rectifiers
- Active harmonic filters
- Line reactors (5% impedance)

# Scenario Analysis: Data Center

## Characteristics

Multiple odd harmonics with THD of 20.4% from SMPS loads.

### Causes:

- Switch-mode power supplies
- High-frequency switching
- Distributed harmonic content

### Impact:

- K-factor: 2.89
- Power factor: 0.980
- Uniform harmonic distribution

### Mitigation:

- Active harmonic filters
- K-13 rated transformers
- Hybrid passive-active systems
- Distributed PF correction

### Challenges:

- Variable load conditions
- Multiple harmonic frequencies
- Passive filters less effective

# Filtering Effectiveness Analysis

## Frequency Domain Notch Filtering

Achieved excellent removal of targeted harmonics (100% for 5th and 7th).

### Advantages:

- Precise harmonic removal
- No phase distortion
- Preserves fundamental
- Predictable results

### Limitations:

- Residual THD (10.8%)
- 11th & 13th remain
- Time-domain only

### Practical Implementation:

- Active filters required
- Proper impedance matching
- Continuous monitoring
- Avoid resonance

### Future Improvements:

- Remove all harmonics  $> 3^{\text{rd}}$
- Adaptive filtering
- Real-time implementation

# Practical Recommendations - By Scenario

## For LED Lighting:

- Install 3rd harmonic filters (150 Hz)
- Use delta-wye transformers
- Select high-quality LED drivers
- Size for K-4 rating minimum

## For Motor Drives:

- Upgrade to 12-pulse rectifiers
- Install active harmonic filters
- Use 5% line reactors
- Size for K-13 rating
- Consider DC common bus

## For Data Centers:

- Deploy active harmonic filters
- Size for K-13 rating
- Implement hybrid systems
- Distributed PF correction
- Continuous monitoring

## General Guidelines:

- Periodic power quality audits
- Monitor IEEE 519 compliance
- Plan harmonic mitigation
- Train maintenance personnel

# Technical Guidelines

## Transformer Selection

K-factor rating based on load type and harmonic content.

Application	K-Factor	Description
Linear Loads	K-1	Resistive/inductive
Office Equipment	K-4	Computers, printers
LED Lighting	K-4	Modern lighting
Motor Drives	K-4 to K-13	VFDs, converters
Data Centers	K-13	SMPS loads
Extreme	K-20	Worst-case

**Note:** Our analysis showed K-factors: LED (2.18), Motor (4.55), Data Center (2.89)



# Key Contributions

- ➊ **DTFS Implementation:** Direct implementation providing accurate harmonic extraction with  $O(N^2)$  complexity. Analysis completes in 0.05 seconds for  $N = 200$ .
- ➋ **Scenario Characterization:** Distinct harmonic signatures identified for LED lighting (3rd dominant), motor drives (5th & 7th), and data centers (distributed).
- ➌ **IEEE Compliance:** All non-linear loads violate IEEE 519-2014 (THD > 8%), with motor drives worst at 26.7%.
- ➍ **Filtering Performance:** Frequency-domain notch filtering achieved 61.7% THD reduction (26.7% → 10.8%) and power factor improvement (0.966 → 0.994).
- ➎ **Practical Framework:** Actionable recommendations for harmonic mitigation, transformer K-factor selection, and IEEE compliance.

# Summary of Findings

## Power Quality Status:

- Ideal: THD = 0%
- LED: THD = 20.2%
- Motor: THD = 26.7%
- Data Center: THD = 19.2%

## Filtering Results:

- 61.7% THD reduction
- 100% removal of 5th & 7th
- Power factor: +3.2%
- K-factor: -43.1%

## Dominant Harmonics:

- LED: 3rd (18% @ 150 Hz)
- Motor: 5th (20% @ 250 Hz)
- Motor: 7th (14% @ 350 Hz)
- Data: Multiple distributed

## Compliance Status:

- IEEE 519 limit: 8% THD
- All scenarios: Non-compliant
- Mitigation: Essential

# Future Work

## Extensions:

- Three-phase analysis
- Real-time monitoring system
- Adaptive filtering algorithms
- Physical filter design
- Economic cost analysis

## Advanced Features:

- Interharmonics analysis
- Voltage sags/swells detection
- Flicker analysis (Pst, Plt)
- Machine learning prediction
- Hardware integration

## Significance

As power electronics continue to proliferate, robust power quality analysis tools become increasingly critical for modern electrical infrastructure.

# Thank You!

## Questions and Discussion

**Project Repository:**

Power Quality Analysis using DTFS

**Contact:**

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