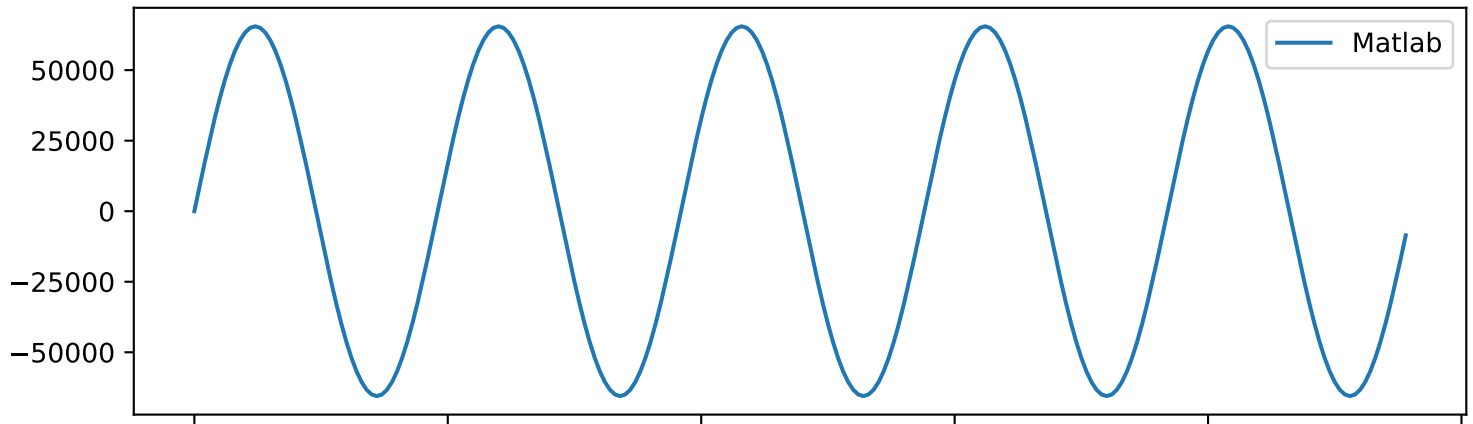
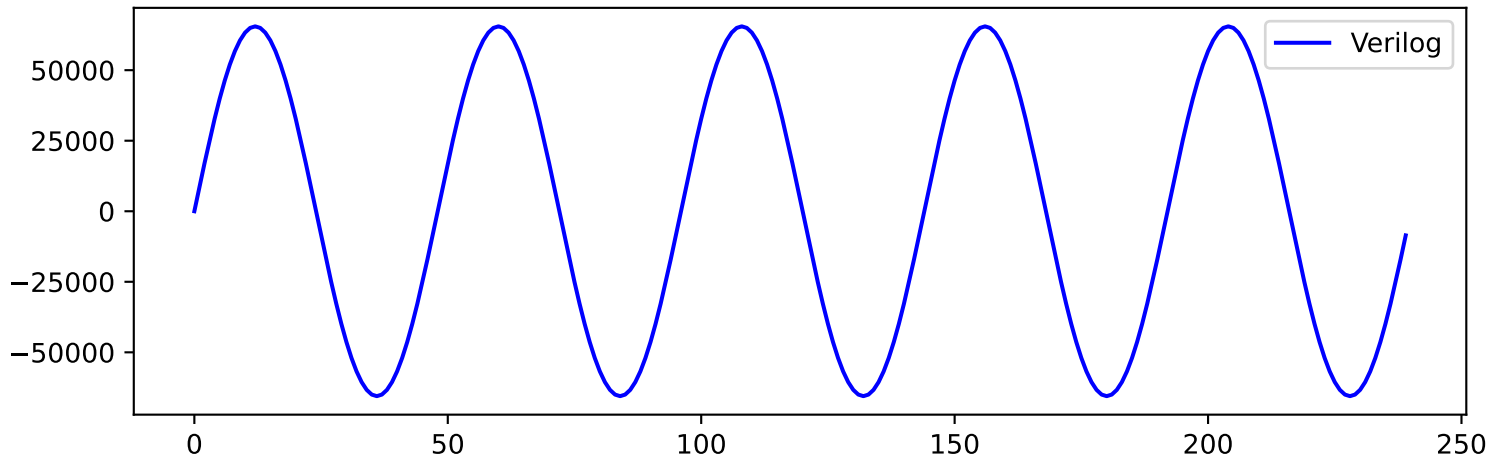


Sum of two numbers

Matlab Output

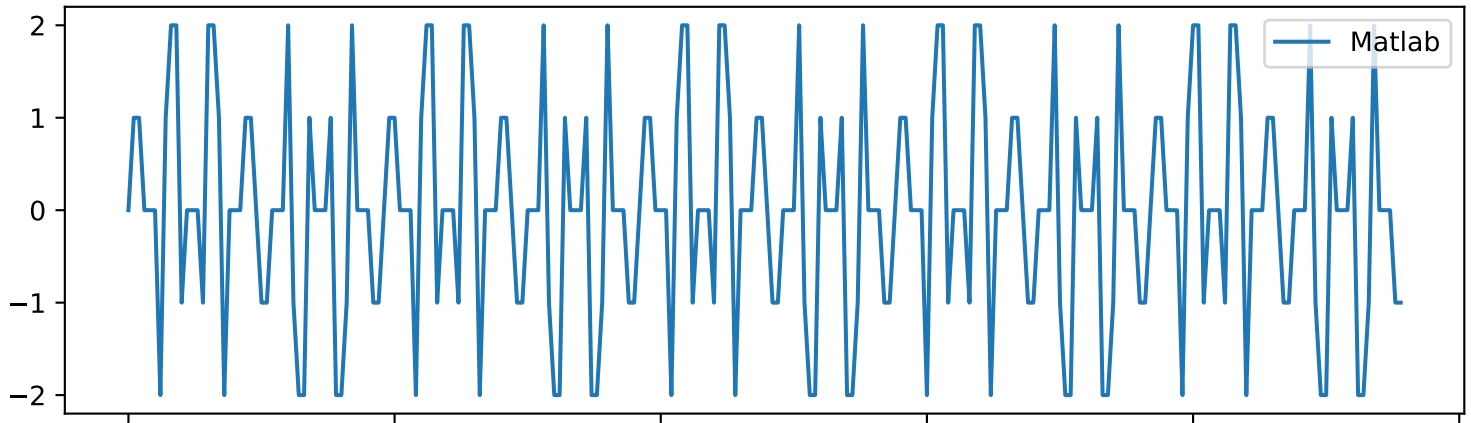


Verilog Output

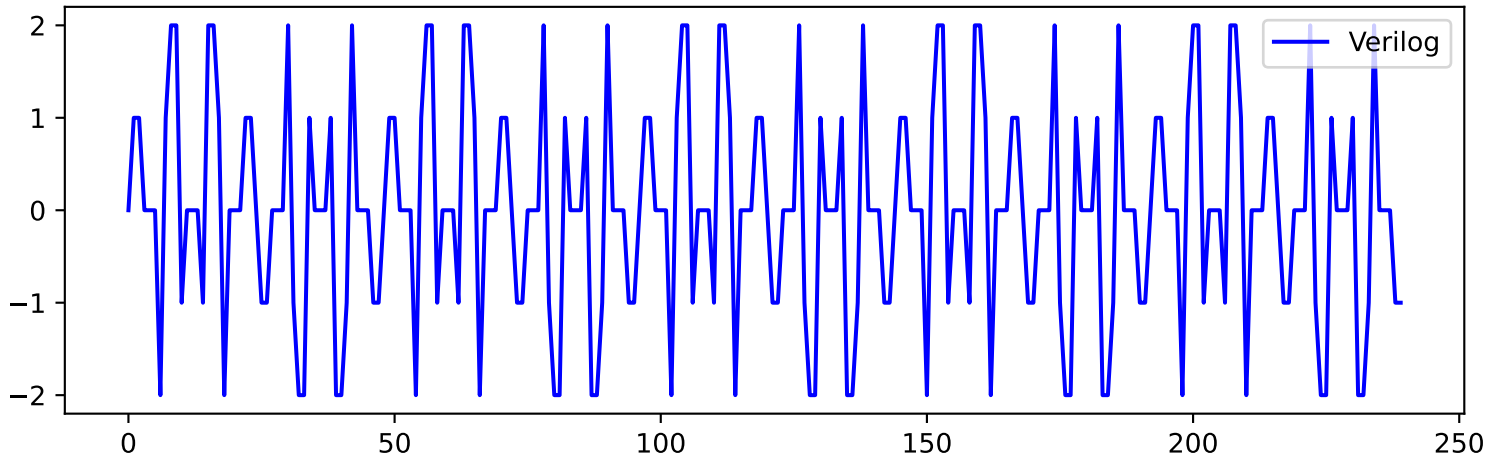


Difference of two Numbers

Matlab Output



Verilog Output



Product of two Numbers

