

Project Proposal

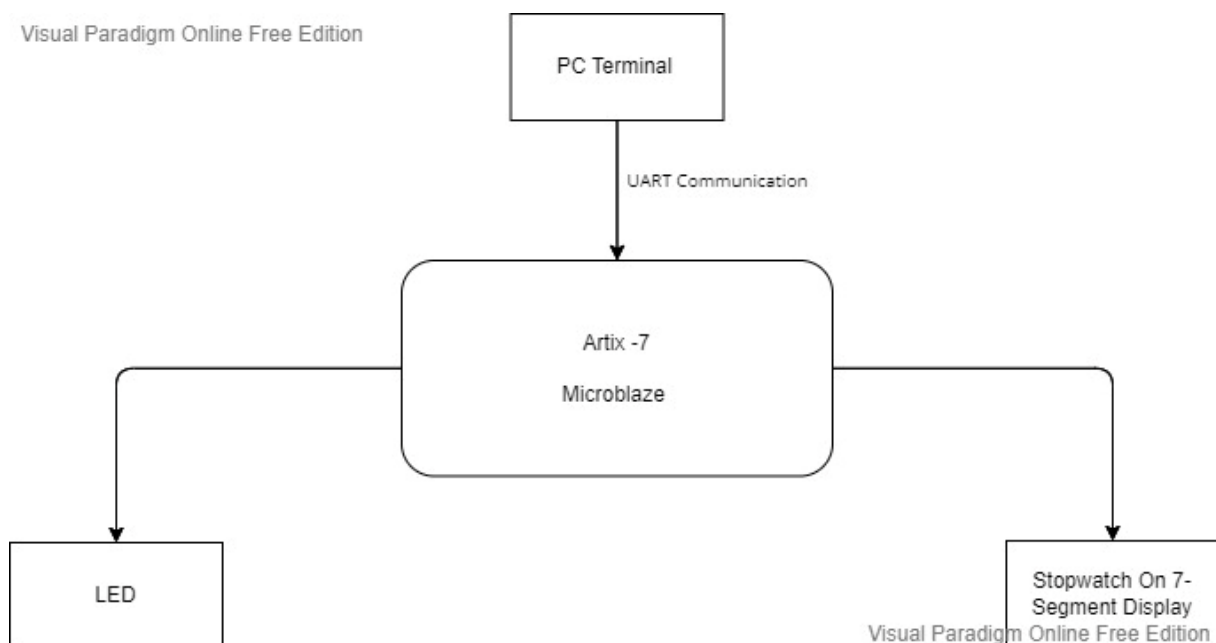
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The main idea of the project is to generate a soft-core processor (Microblaze) in Artix-7 FPGA development board and controlling on-board LED through UART and Running a stopwatch on seven segment display.

Based on the different commands on serial monitor, the on-board LED will behave differently.

The stopwatch will have a pause and reset function.



What Do I Need for This Project?

1. A Nexys A7 100t development board
2. The Vivado Design Suite. This includes Vivado and the Vitis.
3. A JTAG or USB-to-UART cable to program the VC707.

4. A terminal program to send characters over the UART. (Real Team)

GROUP 5

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