Abstract

The project here presents the essence of error free transfer of message between two nodes. To make the communication error free, Error Control and Coding (ECC) is used, which is achieved by using encoder and decoder pair(codec). Thus the work here is to concentrate on channel encoding and decoding techniques. Errors caused in the channel are because of AWGN (Additive White Gaussian Noise), multipath fading, interference and so on. So, to measure the errors BER(Bit Error Rate) is used, which is dependent on SNR(Signal to Noise Ratio). In first phase of the project channel coding techniques are tested for their performance with respect to BER, by simulinting their models in Simulink and BERTool. And in next phase channel coding techniques are simulated in Xilinx and ModelSim, by preparing the verilog codes nad these codes are made to run on FPGA.

Communication

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Transfer of Data in a medium

Communication

- Transfer of Data in a medium
- Transmission Channel

Communication

- Transfer of Data in a medium
- Transmission Channel
- Data Corruption

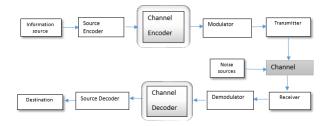


Figure: Block Diagram of Communication System

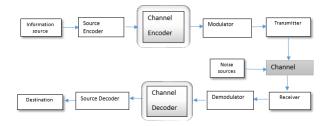


Figure: Block Diagram of Communication System

Channel Encoder Decoder Pair



Channel Coding

Channel Coding

 Inevitable Existence of Errors on any given Communication Channel

Channel Coding

- Inevitable Existence of Errors on any given Communication Channel
- Channel Noises

Detection and Correction of Codes

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- To Achieve Reliable Communication

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- Types of Error Correction Mechanisms

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 - Block Codes
 - Convolutional codes

Hamming Codes

First Class of Linear Block Codes

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- Table Driven Decoding

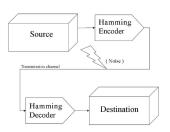
Hamming Codes

• (n,k)Hamming Code

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- Code length: $n=2^m-1$
- No. of information symbols: $k=2^m-m-1$
- m>=3 : Hamming Distance=n-k

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Convolutional Codes

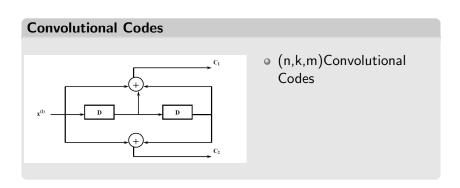
Non Linear Block Codes

- Non Linear Block Codes
- Contains Memory

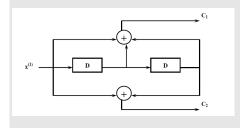
- Non Linear Block Codes
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- Capable of Multiple Error Detection & Correction

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- Employs Trellis Structure

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Convolutional Codes



- (n,k,m)Convolutional Codes
- no of input bits k
- no of encoded bits n
- memory order m
- code rate= k/n

Viterbi Decoding

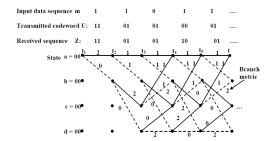
 Maximum Likelihood Decoding Algorithm

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 - Bit Metrics



- Software Tools
 - MATLAB

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Simulink



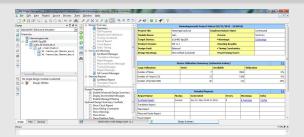
- Graphical Programming Environment
- Libraries of Functional Blocks
- Model Analysis Tools
- Generating Code(C,C++,HDL)

BER Tool



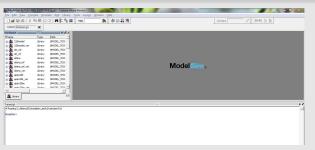
- Bit Error Rate Analysis
- Theoretical Simulation
- Monte-Carlo Simulations

XILINX



- HDL Design Analysis and Synthesis
- Integration With ModelSim
- Behavioural Verification
- FPGA Synthesis

ModelSim



- Implements Verilog and System Verilog Languages
- Test Bench Development

Hardware Tools Used

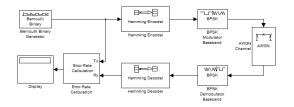
FPGA



- Contains an array of programmable logic blocks.
- Netlist can be generated.
- It has a serial interface called JTAG.

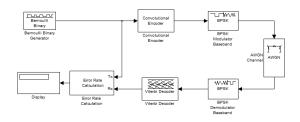


Implementation of Hamming Codes



- Hamming code: (7,4)
- Modulation: BPSK
- Channel:AWGN

Implementation of Convolutional Codes



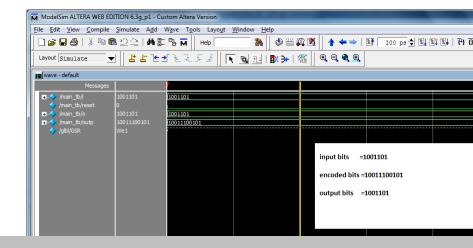
Convolution code :(7,[171 133],171)

Modulation: BPSK

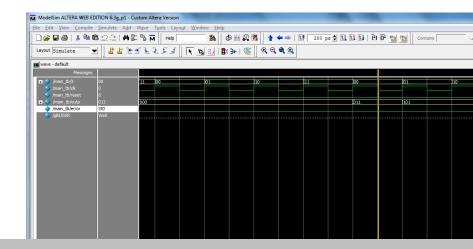
Channel:AWGN



Simulation Results of Hamming Codes



Simulation Results of Convolutional Codes



Comparative Analysis of BER for Hamming and Convolutional Codes

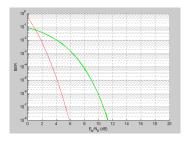


Figure: where,	Green-Hamming and
Red-Convolution	on

• >	⟨ -axis	Eb/N0,	Y-axis:BER
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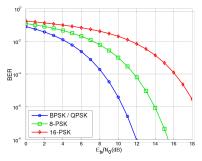
Modulation:BPSK, Channel Model: AWGN

Eb/No	Hamming	Convolutional
1	0.149	0.195
2	0.115	0.125
3	0.093	0.035
4	0.064	0.009
5	0.043	0
6	0.030	0
7	0.020	0
8	0.010	0
9	0.006	0
10	0.002	0

Figure: BER values of Hamming and Convolutional Coding



BER Comparison for Modulation Techniques



- In a limited bandwidth channel, BER increases with the bit rate.
- BER of BPSK and QPSK are almost same.
- BPSK is chosen for the simplicity in circuit.

Conclusion

- BER and SNR Inversely Proportional
- The Simulation Results Show Convolutional Codes Perform Better Than Hamming Codes
- BER is Dependent on Modulation Techniques

Future Scope

- The Work Can be Further Extended on Rayleigh and Ricean Channel Models
- Constructing Adaptive Channel Encoder and Decoders
- SDR Software Defined Radio

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