

GATE-2021-BM-Q3

EE23BTECH11015 - DHANUSH V NAYAK*

Question: Three resistive loads are connected to ideal voltage and current source as shown in circuit below. The voltage V_{AB} across terminals A and B is equal to

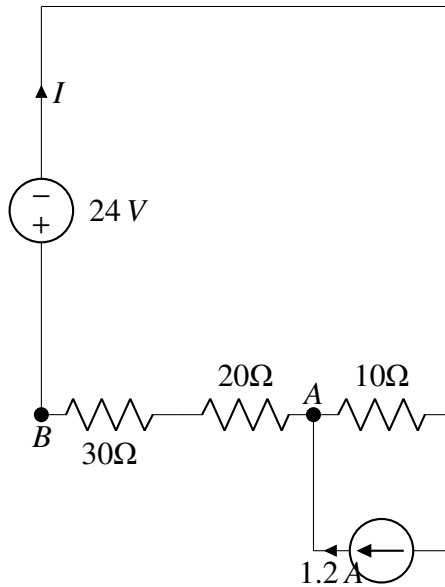


Fig. 1. Circuit Diagram

(GATE-2021-BM-Q3)

Solution:

Using superposition principle:

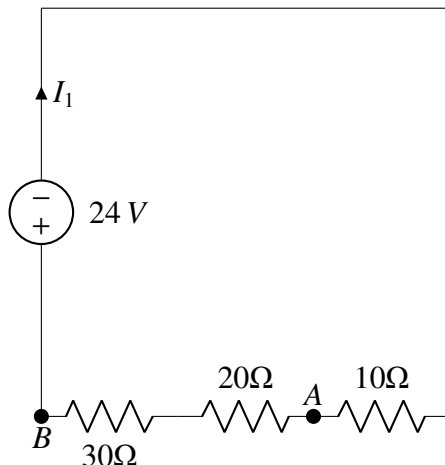


Fig. 2. Circuit considering only voltage source

$$V = I_1 R_{eq} \quad (1)$$

$$R_{eq} = 30 + 20 + 10 = 60\Omega \quad (2)$$

$$(3)$$

Therefore:

$$I_1 = -\frac{24}{60} \quad (4)$$

$$= -0.4A \quad (5)$$

Now, considering current source only

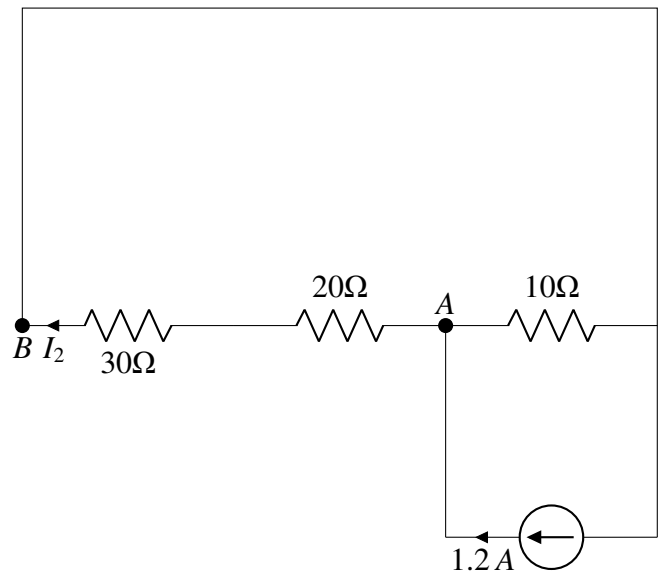


Fig. 3. Circuit considering only current source

Writing KVL around the loop:

$$30(I_2) + 20(I_2) + 10(I_2 - 1.2) = 0 \quad (6)$$

$$60I_2 - 12 = 0 \quad (7)$$

$$I_2 = 0.2A \quad (8)$$

By superposition :

$$I = I_1 + I_2 \quad (9)$$

$$I = -0.2A \quad (10)$$

Thus,

$$V_{AB} = 50 \times I \quad (11)$$

$$V_{AB} = -10V \quad (12)$$