

CODE: H8YL.

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Qs. Paper:

Q1. Draw

$$Y = AB' + BC + CDE'$$

using 2 input NAND gates.

gate input cost = ?

Standard 2 level SOP = ?

Show how Y can be ^{used} _{made} 16 to 1 MUX.

Q2. PI = ?

prime implicant

What makes PI non essential

i/p? to 1 to 4 if $F(W, X, Y, Z) = \sum m$

(6, 7, 12, 13)

Q3. individual & combined min.

$$F_1 = \sum_{ABCD} m(0, 1, 5, 6, 7, 13, 14, 15)$$

$$F_2 = \sum m(4, 5, 6, 10, 11, 12, 13, 14)$$

Q4. $A = (111011)_2$
 $B = (101001)_2$

A-B using
2's complement

How to detect overflow using binary
adder?

usefulness of openable in IC 74257
wibble MUX?

Q1. $Y = \overline{(AB)} \cdot \overline{(BC)} \cdot \overline{(CDE)}$

NAND gate.

Answers.

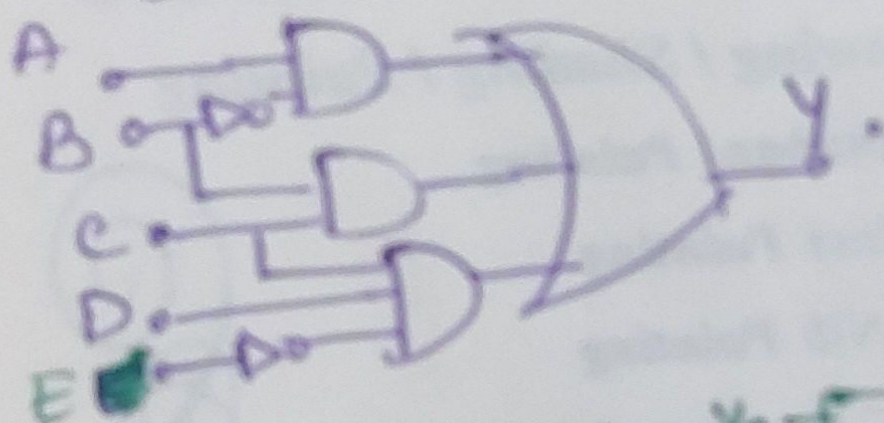
Q1.

$$Y = AB' + BC + CDE'$$

Gate input cost = $T + L + N$
 (6) = $7 + 3 + 2 = 12$

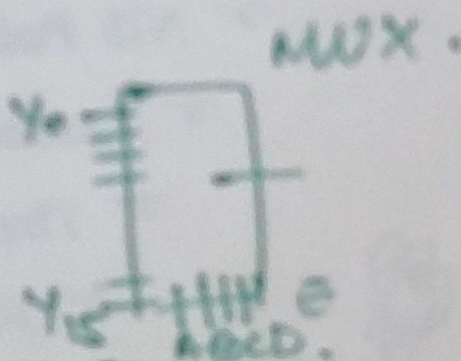
using 2 ml SOP.

$$= 10 + 2 = 12$$



$$Y = A' \cdot Y(0, B, C, D) + A \cdot Y(1, B, C, D)$$

using Shannon's expression.



$$\begin{aligned} Y &= A' \cdot (BC + CD \cdot E') + A \cdot (B' + BC + CDE') \\ &= A' \cdot (B' + B \cdot C + CDE') + A \cdot (B' + BC + CDE') \end{aligned}$$

$$Y = A' \cdot (D, C)$$

$$Y = A' \cdot [Y \cdot W, B, (D, E)] \\ + A \cdot [Y \cdot (1, B, C, D, E)]$$

$$Y = A' \cdot B' (Y \cdot W, 0, C, D, E) \\ + A' \cdot B (Y \cdot W, 1, C, D, E) \\ + A \cdot B' (Y \cdot (1, 0, C, D, E)) \\ + A \cdot B (Y \cdot (1, 1, C, D, E))$$

Q3.

COMBINED.

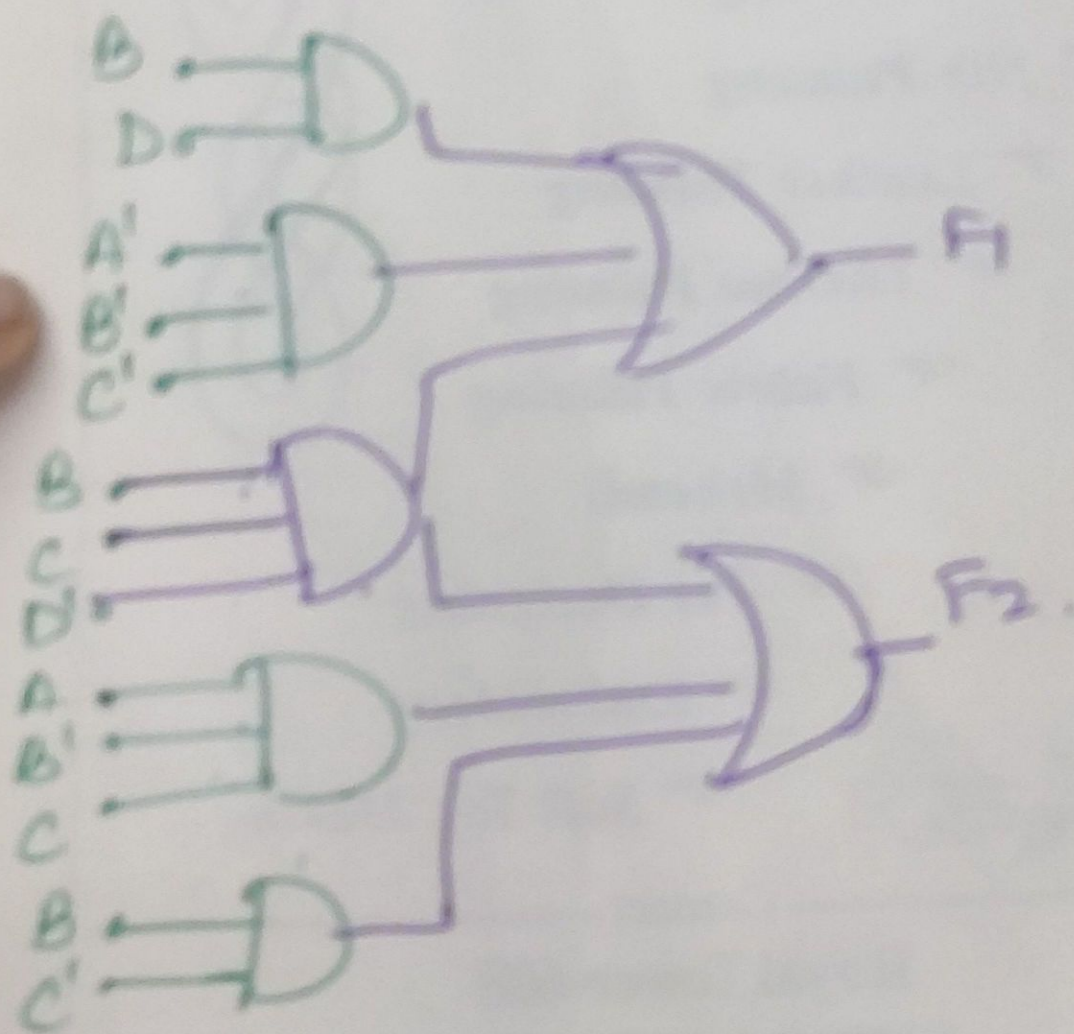
Answers.

$$F_1 = BCD' + BD + A'B'C'$$

$$F_2 = BCD' + BC' + AB'C$$

$$\therefore \text{Total cost} = 18 + 5 = 23$$

\therefore Combined minimization.
 lesser than individual min.
 of total cost of F_1, F_2 .



Q2. INDIVIDUAL. Answers.

$$F_1 = \sum m(0, 15, 6, 9, 13, 14, 15).$$

| AB \ CD | 00 | 01 | 11 | 10 |
|---------|----|----|----|----|
| 00 | 1 | 1 | 0 | 0 |
| 01 | 0 | 1 | 1 | 1 |
| 11 | 0 | 1 | 1 | 1 |
| 10 | 0 | 0 | 0 | 0 |

$$= A'B'C' + BD + BC$$

TC = no. of gate inputs + no. of gates.

$$F_2 = \sum m(4, 5, 6, 10, 11, 12, 13, 14).$$

| AB \ CD | 00 | 01 | 11 | 10 |
|---------|----|----|----|----|
| 00 | 0 | 0 | 0 | 0 |
| 01 | 1 | 1 | 0 | 1 |
| 11 | 1 | 1 | 0 | 1 |
| 10 | 0 | 0 | 1 | 1 |

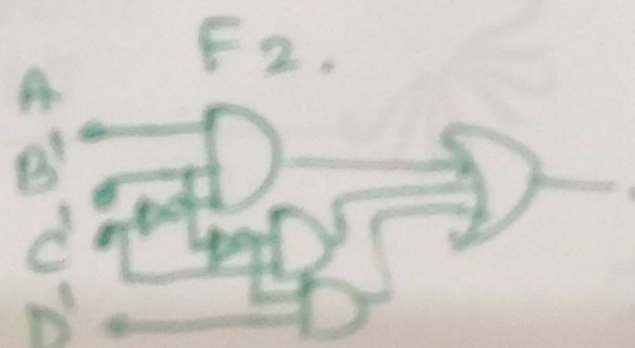
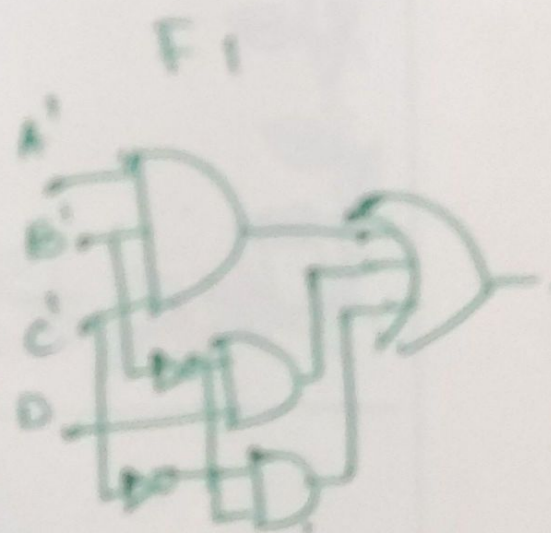
$$= A'BC + BC' + BD'$$

$$TC_{F_1} = 10 + 4 = 14.$$

$$TC_{F_2} = 10 + 4 = 14.$$

$$\text{Total} = 14 + 14$$

Cost individual = 28



Q2.

Answers

Prime Implicant (PI).
 A group of square or rectangle with bunch of adjacent implicants is allowed by definition of Kmap.
 If all groups are primed from Kmap
 → If group covers min-terms that can also be covered by another prime implicant then the PI becomes non-essential.

$$F(W, X, Y, Z) = \sum m(6, 7, 12, 13)$$

| WX \ YZ | 00 | 01 | 11 | 10 |
|---------|----|----|----|----|
| 00 | 0 | 0 | 0 | 0 |
| 01 | 0 | 0 | 1 | 1 |
| 11 | 1 | 1 | 0 | 0 |
| 10 | 0 | 0 | 0 | 0 |

$$\begin{aligned}
 &= ABC' + A'B'C \\
 &= B(AC' + A'B) \\
 &= B(A \oplus C)
 \end{aligned}$$

$$\begin{aligned}
 &= WXY' + W'XY \\
 &= (WY' + W'Y)X \\
 &= X \cdot (W \oplus Y)
 \end{aligned}$$

$$\text{If } 1=X, 2=W, 3=X, 4=Y$$

only then

$$F = XW \oplus XY$$

Essential PI always ^{at least} cover min-terms that can't be covered by any other PI.
 i.e. one final condition.

Q4.

Answers.

$$\begin{array}{r} A \quad 111011 \\ B \quad 101001 \end{array}$$

2's complement of B.

$$\begin{array}{r} -B \quad 010110 \\ + \quad 1 \\ \hline 010111 \end{array} \quad \left\{ \begin{array}{l} \text{invert} \\ +1 \end{array} \right\}$$

$$\therefore \begin{array}{r} A \quad 111011 \\ -B \quad + 010111 \\ \hline 1010010 \end{array}$$

Carry

$$\therefore A - B = 1010010 = (010010)_2$$

Carry.

$$(59)_{10} \quad (41)_{10} = (18)_{10}$$

Overflow can be detected using XOR gate. for carry generated during addition.

Overflow = $C_2 \oplus C_1$, if 2 gates are there

• output enable (en) in 74257 - Nibble mux. when $en=0$, output is high impedance or dependent on the specific device. when $en=1$, mux performs selection