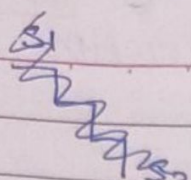


# Assignment 1. (30 Aug)

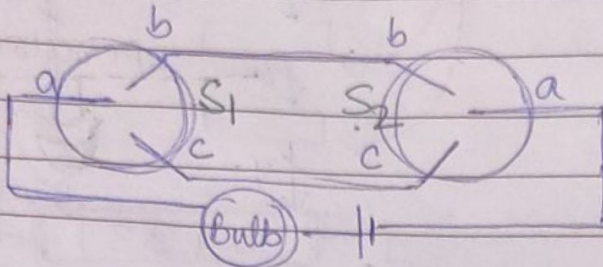
Q1. a) 

(Switch ON)

A	B	out
0	0	0
0	1	1
1	0	1
1	1	0

XOR gate.

(b)



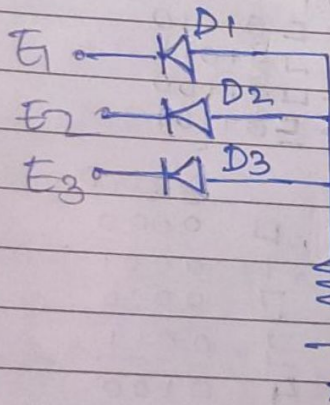
For always lighting of bulb  $\rightarrow$  path unbroken.  
both a connected to b or c.  
ie. Both switch on or off.

$$\Rightarrow S_1 \cdot S_2 + \bar{S}_1 \cdot \bar{S}_2$$

$$\Rightarrow S_1 \oplus S_2 \quad \text{or} \quad S_1 \odot S_2$$

XNOR Gate.

Q2.



diodes are ideal.

$E_1$	$E_2$	$E_3$	$V_0$
1	1	1	1
1	1	0	0
1	0	1	0
0	1	1	0
0	0	1	0
1	0	0	0
0	1	0	0
0	0	0	0

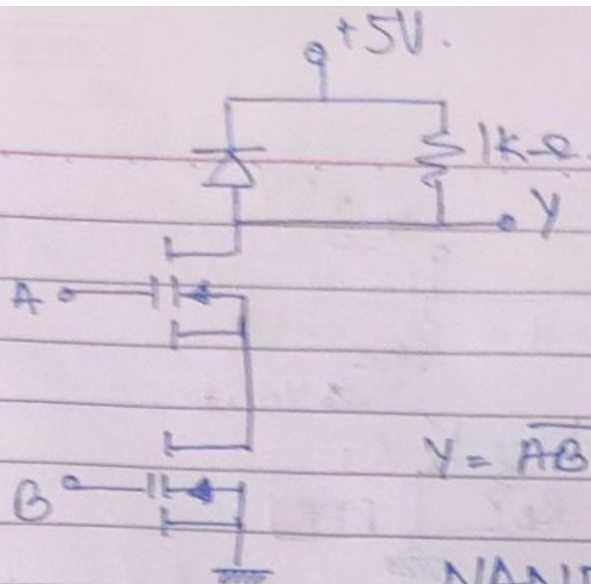
at least any diode short circuited.

diodes PN  $\rightarrow$

$\therefore$  AND Gate.



(3a)

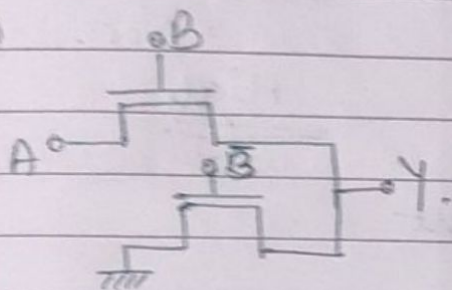


$$Y = \overline{AB}$$

NAND Gate

A	B	Y	nmos ON
1	1	0	∴ Y connected to ground.
1	0	1	} nmos OFF ∴ Y connected to +5V.
0	1	1	
0	0	1	

(b)



A	B	Y
1	1	1
1	0	0
0	1	0
0	0	0

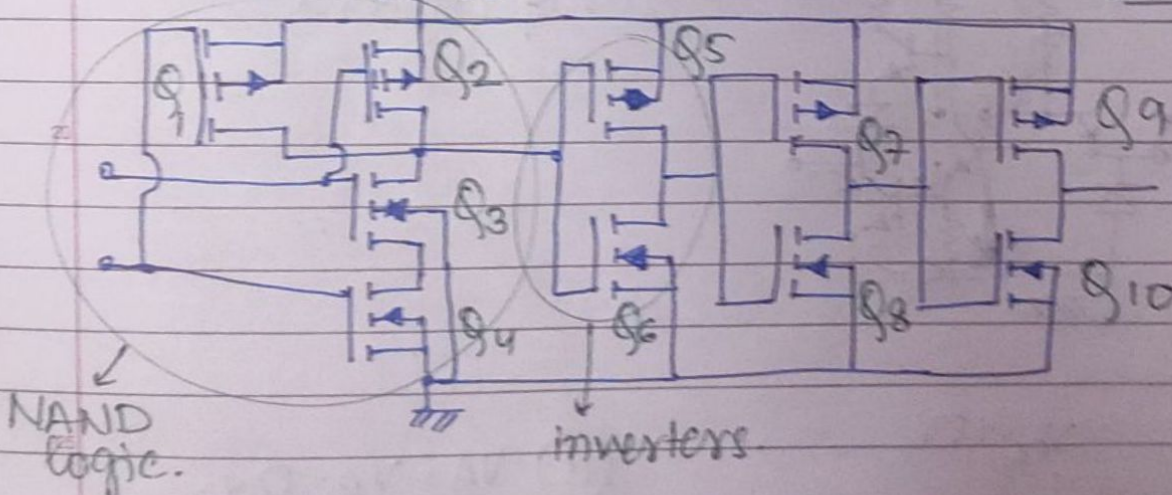
Annotations:   
 - For (1,1): B=1, Y is connected to A.   
 - For (1,0), (0,1), (0,0): Y connected to ground.

AND Gate

$$Y = AB$$

(c) ~~for highlighting both, as both are same~~

AND Gate



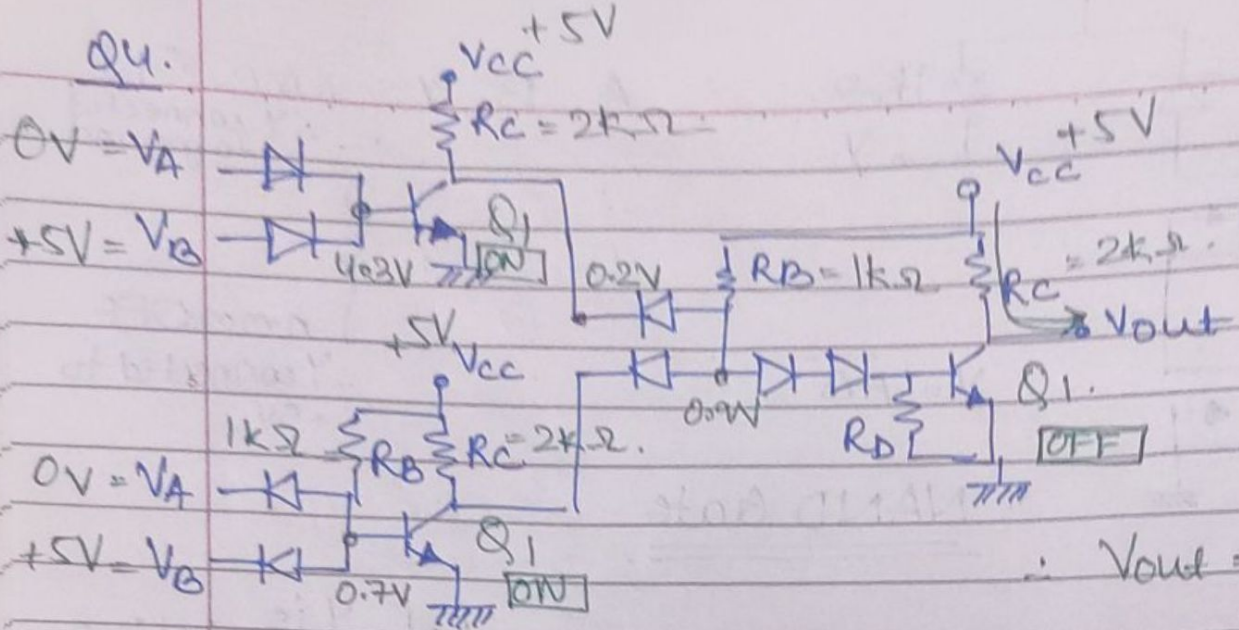
NAND logic.

inverters

NAND + 3 (inverters) → AND gate.

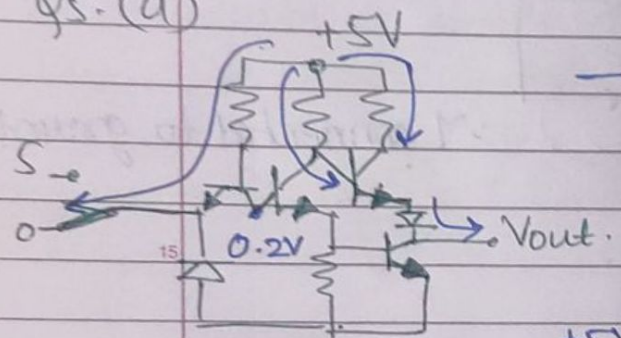


Q4.



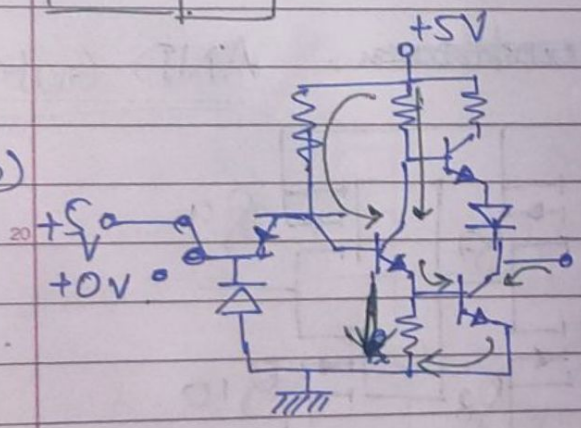
$V_{out} = +5V = V_{CC}$   
High.

Q5. (a)



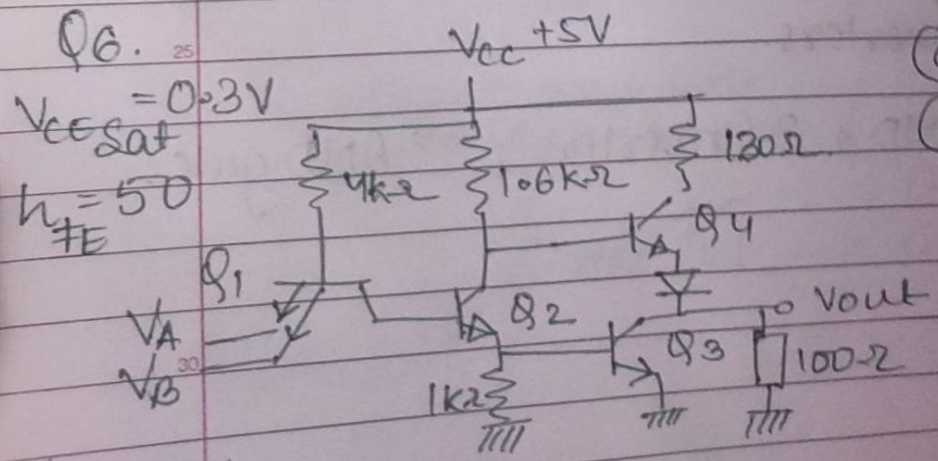
— path of current

(b)



— path of current.

Q6.

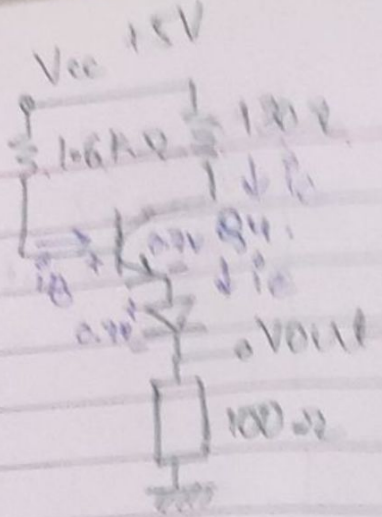


- (a)  $V_A = V_B = 0V$ .
- (b)  $V_A = V_B = 5V$ .

$V_D = 0.7V$



(a)



$$I_C = 50 I_B$$

$$I_E = 51 I_B$$

$$V_{CE\text{ sat}} = 0.3V$$

$$V_{CE} = V_C - V_E$$

$$= (5 - I_E \cdot 130) - (100 I_E + 0.7)$$

$$= -1.93 < V_{CE\text{ sat}}$$

transistor is in saturation.

$$5 - 1600 I_B - 0.7 - 0.7 - 100 I_E = 0$$

$$3.6 = 1600 I_B + 100 I_E$$

$$5 - 130 I_C - 0.3 - 0.7 - 100 I_E = 0$$

$$4 = 130 I_C + 100 I_E$$

$$V_{out} = 100 I_E$$

$$\therefore V_{out} = 3.6V$$

$$V_{BQ2} = 0.3V$$

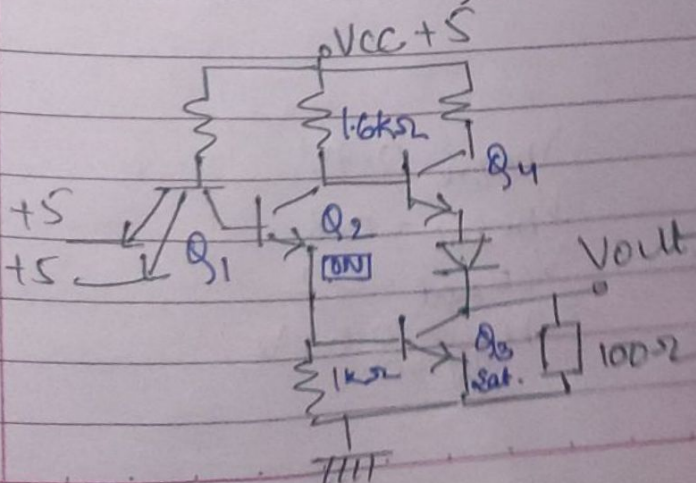
$Q_2 \rightarrow \text{OFF}$

$$I_{CQ2} = I_{BQ1} \approx 0$$

$$3.6 = 100 I_E + 1600 I_B$$

(b)  $V_A = V_B = 5V$ .  $Q_2$  ON  $Q_2(\text{sat})$

$$V_{out} = 0.3V$$





Q7.

$$V_{OL} = 0.9V$$

$$V_{OH} = 4.5V$$

$$V_{IL} = 1.2V$$

$$V_{IH} = 3.2V$$

$$V_{IL} = 1.5V$$

$$V_{IH} = 3V$$

will it be better?

$$N_{ML} = ?$$

$$N_{MH} = ?$$

$$\text{transition width} = ?$$

$$\text{logic swing} = ?$$

$$N_{ML} = V_{IL} - V_{OL}$$

$$= 1.2 - 0.9 = 0.3V$$

$$N_{MH} = V_{OH} - V_{IH}$$

$$= 4.5 - 3.2 = 1.3V$$

$$\text{transition width} = V_{IH} - V_{IL}$$

$$= 3.2 - 1.2 = 2V$$

$$\text{logic swing} = V_{OH} - V_{IH}$$

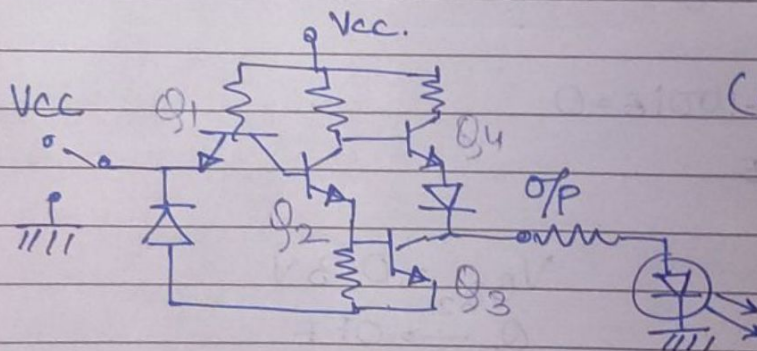
$$= 4.5 - 3 = 1.5V$$

$$N_{ML} = 0.6V$$

$$N_{MH} = 1.5V$$

as noise margins is greater thus it would perform better.

Q8.



(a) Switch on Vcc.

$Q_2 \rightarrow ON$

O/p low.

$\therefore$  LED doesn't glow

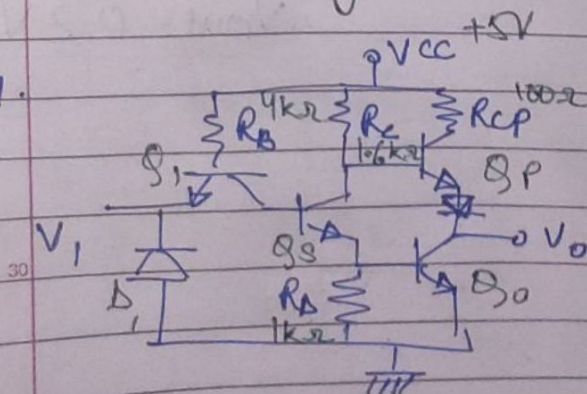
(b) Switch on 0V

$Q_2 \rightarrow OFF$  (as  $V_{in} = 0.2 < 0.7$  base)

O/p high.

LED will glow.

Q9.



$$V_{be(on)} = 0.7V$$

$$V_{be(sat)} = 0.8$$

$$V_{ce(sat)} = 0.2$$

$$V_{be} = 0.65V$$

off  $\rightarrow$  on



by inc.  $V_i$  from  $0 \rightarrow V_{cc}$ .  
 $V_i \in [0, 0.45]$

$Q_1 \rightarrow$  Saturation  
 $Q_2 \rightarrow$  OFF until  $V_i + 0.2 < 6.5$   
 $Q_0 \rightarrow$  OFF  $V_i \leq 0.45$

$$I_{Rc} = I_{BP}$$

$$V_o = V_{cc} - 0.7 - 0.7$$

$$= 3.6 \text{ V}$$

for high  $I_L$ ,  $Q_P \rightarrow$  saturates.  
 $V_o = V_{cc} - 0.8 - 0.8$   
 $= 3.4 \text{ V}$

after  $V_i \in [0.45, 1.15]$

$Q_S \rightarrow$  ON  
 $Q_1 \rightarrow$  Saturation  
 $Q_0 \rightarrow$  OFF  
 $Q_P \rightarrow$  active.

$$A_{Vs} = -\frac{R_c}{R_D} = -1.6 \rightarrow V_i \in [0.45, 1.15]$$

$Q_0 \rightarrow$  OFF until  $V_{RD} \leq 0.65$   
 $Q_0 \rightarrow$  ON when  $V_i + 0.2 - 0.7 - 0.65 = 0$   
 $V_i = 1.15$

$$V_o = V_{cc} - I_{cRc} - 0.8 - 0.8$$

$$= 3.4 - 0.65 \times 1.6$$

$$V_o = 2.36 \text{ V}$$

$$V_{RD} = 0.65$$

$$I_{ES} = 0.65 \text{ mA}$$

$$I_{CS} \approx I_{ES}$$

$Q_S, Q_0 \rightarrow$  active

$Q_1 \rightarrow$  Saturation

$Q_2 \rightarrow$  (active  $\Rightarrow$  OFF)

$$r_d \approx 1 \text{ k}\Omega$$

$$\Delta V_o = A_{Vs} \Delta V_{Es} + A_{Vo} \Delta V_{Eo}$$

$$A_v = A_{Vs} + A_{Vo} = -6.8$$

$$A_{Vs} = -\frac{R_c}{R_D \parallel r_d} = -3.2$$

$$A_{Vo} = \frac{\Delta V_o}{\Delta V_{OQ_0}} = -\frac{\beta_F R_c \text{ eff}}{r_d} = -\frac{\beta_F}{r_d} \left( \frac{r_d + r_d + R_c}{\beta_F} \right) = -3.6$$

$Q_0 \rightarrow$  saturate at  $V_{BE} = 0.8V$

$$V_1 = 0.8 + 0.8 - 0.2$$

$$= 1.4V$$

$V_0 = 0.2$   
as saturation.

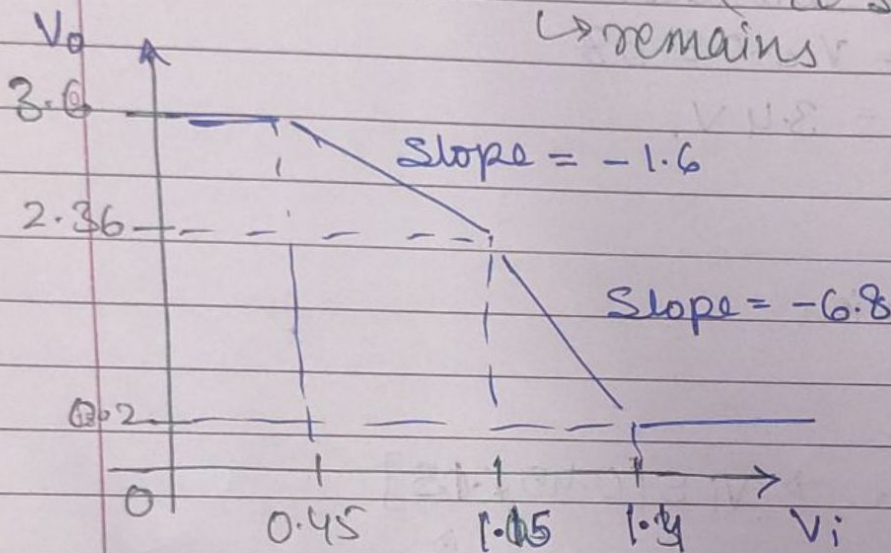
If  $V_{in} \geq 7.4V$ .

$Q_3, Q_0 \rightarrow$  saturates.

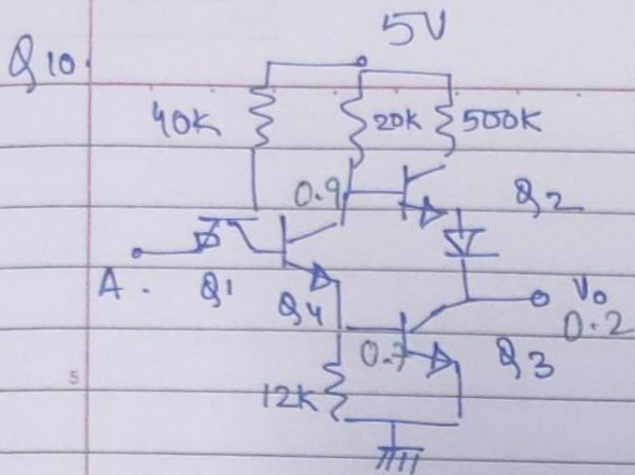
$$V_1 = 0.8 + 0.8 - 0.2$$

$$= 1.4$$

$V_0 = 0.2$  ( $V_{CE}$  saturation)  
 $\hookrightarrow$  remains







Since o/p is low, i/p must be high.  
 Thus,  $Q_4, Q_3$  are in saturation  
 $Q_2 \rightarrow \text{OFF}, Q_1 \rightarrow \text{inverse active mode}$

$$V_{BE_{Q3}} = 0.7V$$

$$V_{CE_{Q4}} = 0.2 + 0.7 = 0.9V$$

$$I_{C_{Q4}} = \frac{5 - 0.9}{20} \text{ mA}$$

$$I_{C_{Q4}} = 0.205 \text{ mA}$$