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19EE10039.

Part 3

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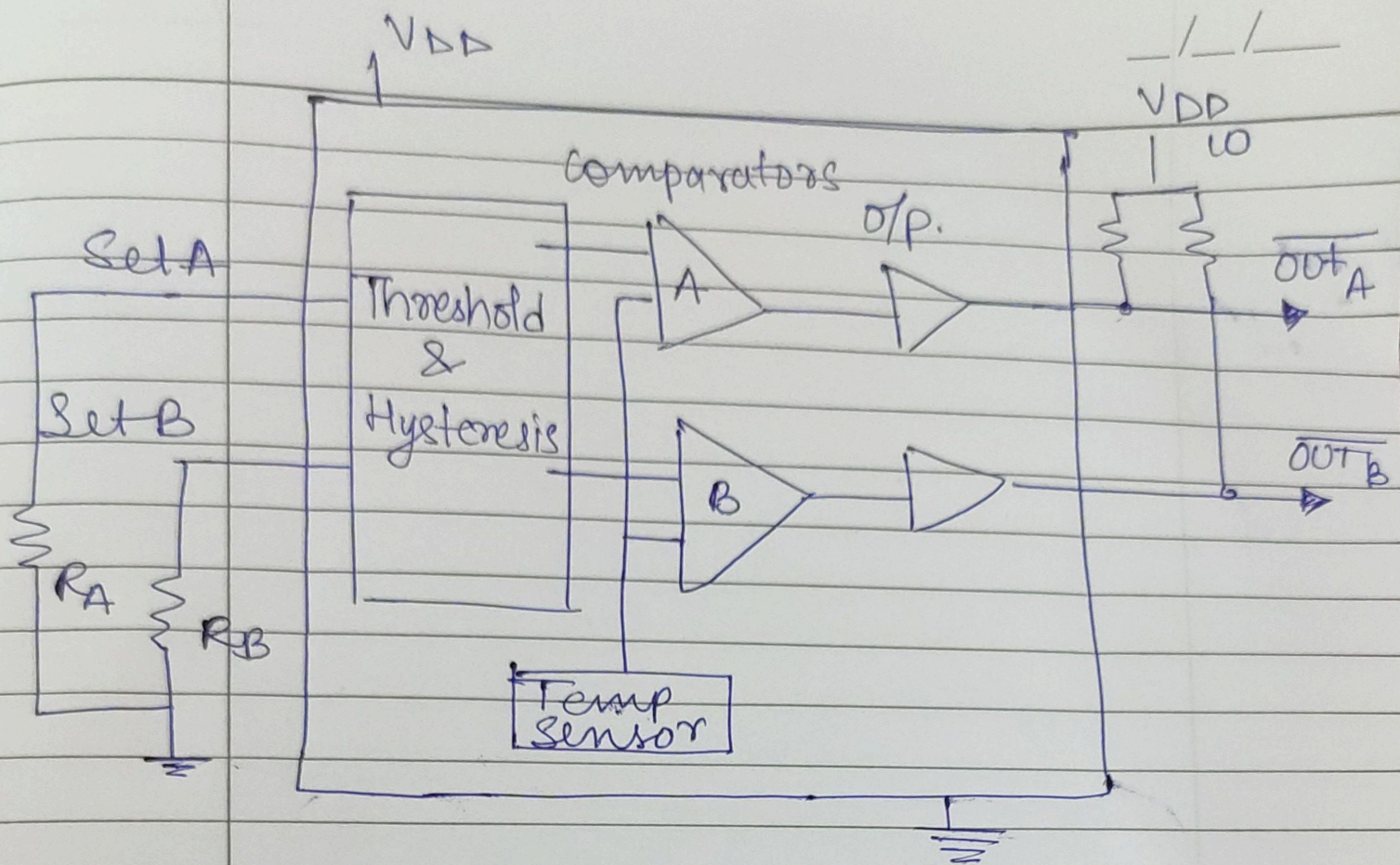
SWITCHED

OFF: when higher than
upper limit

$\left\{ \begin{array}{l} \text{temp} > 26^{\circ}\text{C} \\ \text{temp} < 24^{\circ}\text{C} \end{array} \right\}$

ON: when lower than lower limit.

Scheme without ADC
using 2 sets of comparators
to interrupt



Explanation:-

We have used 2 sets of comparators
Thus the circuit has 2 internal
comparators with 2 output.

We can ~~conjugate~~ configure the
circuit for any desired temp range,
with hysteresis.

Seperate hot and cold trip outputs
generate independent signals which are
interpreted by microprocessor.