1) Check for O divisor

2) 96 divisor \le dividend bits, I bit in quotient, subtract.

3) 96 divisor > dividend, o but in quotient

bring down next dividend but.

4) Do subtraction, if remainder < 0, add divisor back.

start Subtract divider origisher from remainder rigister and store in remainder NO MES Remainder > 0 Restore original reminder Shift the quotient by adding it to divitor register 2 bit left, orgister Also, left shift quotient setting right most register, setting LSB to 0 buit 1 Duift divisor origister 1 bilt NO (not) the nepetrulion

