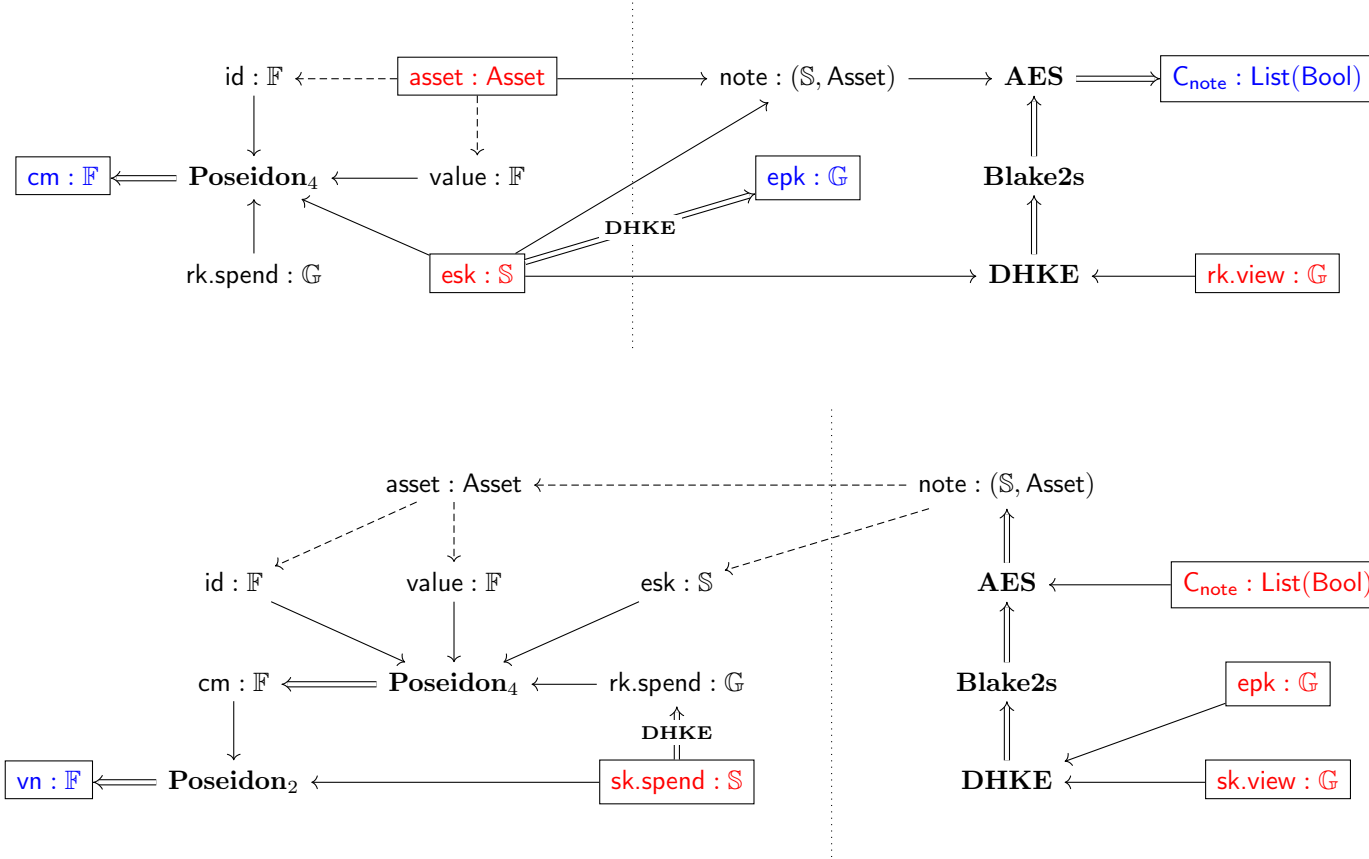


# Receiver and Sender Circuits

NB: Does not include UTXO accumulator proof.



The circuits are on the left of the dotted lines.

$(\rightarrow) := \text{input}$     $(\Rightarrow) := \text{computation}$     $(\dashrightarrow) := \text{projection}$     $(\cdots) := \text{circuit boundary}$     $(x : T) := \text{private input}$     $(x : T) := \text{public output}$