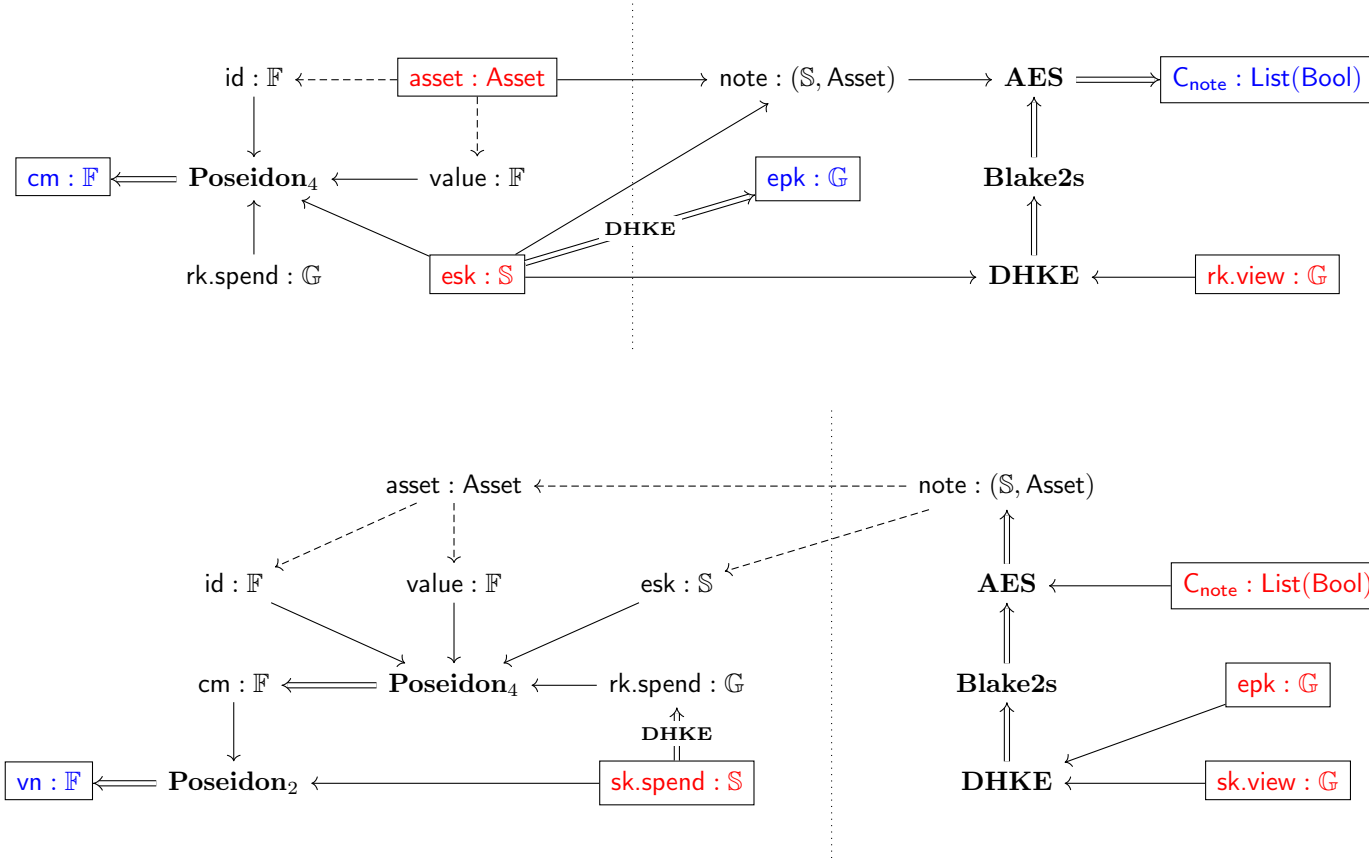


Receiver and Sender Circuits

NB: Does not include UTXO accumulator proof.



The circuits are on the left of the dotted lines.

(\rightarrow) := input (\Rightarrow) := computation (\dashrightarrow) := projection (\cdots) := circuit boundary $(x : T)$:= private input $(x : T)$:= public output