

"OpenACC\_version/vecAdd\_openacc.c", line 53: **warning:** last line of file ends without a newline

```
}  
^
```

main:

38, Loop is parallelizable

Generating Tesla code

38, #pragma acc loop gang, vector(128) /\* blockIdx.x threadIdx.x \*/

38, Generating implicit copyin(b[:n]) [if not already present]

Generating implicit copyout(c[:n]) [if not already present]

Generating implicit copyin(a[:n]) [if not already present]

44, FMA (fused multiply-add) instruction(s) generated

45, FMA (fused multiply-add) instruction(s) generated