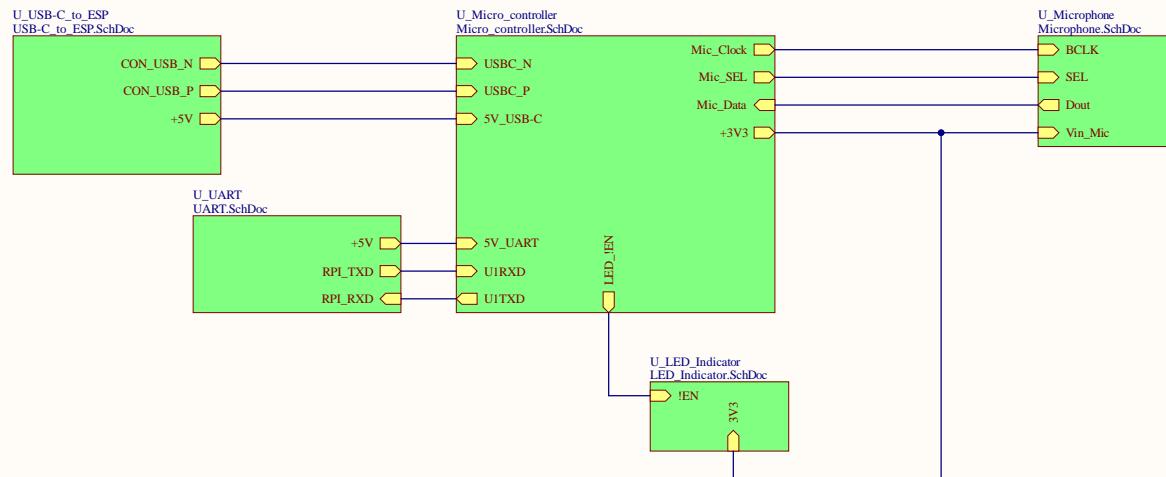


1 2 3 4 5 6

A

A



B

B

C

C

D

D

Title Senior Design PCB Top Level		
Size B	Number	Revision V0
Date: 2/15/2026	Sheet of Manuel Posada	
File: C:\Users\...\PCB_TOP_LEVEL.SchDoc	Drawn By: Manuel Posada	

1 2 3 4 5 6

1 2 3 4 5 6

A

A

B

B

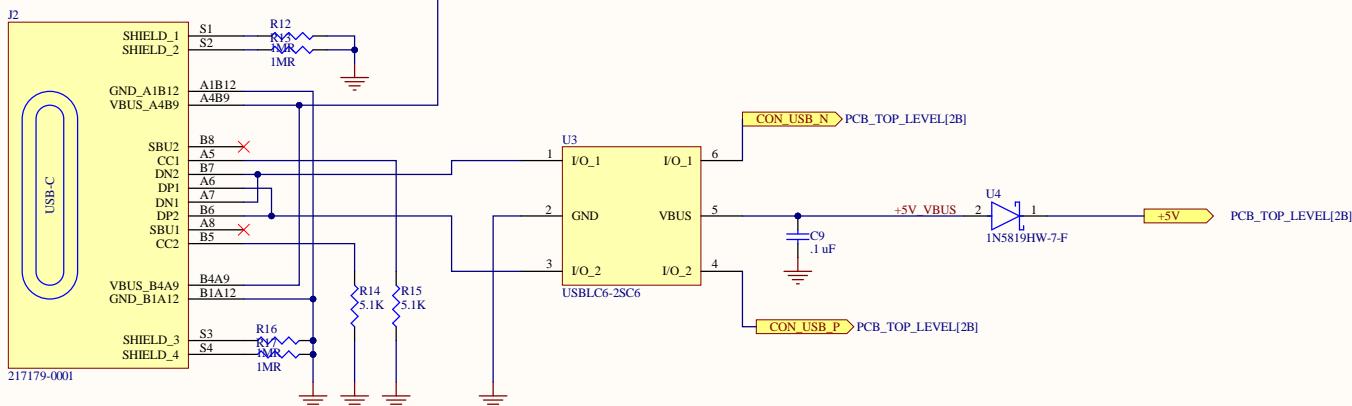
C

C

D

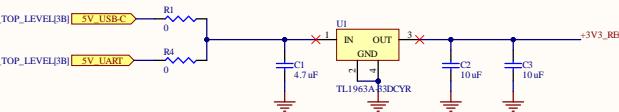
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USB-C CONNECTOR

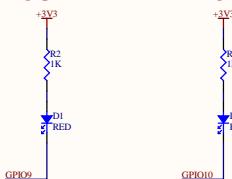


Title USB-C_to_ESP		
Size B	Number	Revision V0
Date: 2/15/2026	Sheet of 1	Manuel Posada
File: C:\Users\...\USB-C_to_ESP.SchDoc	Drawn By:	Manuel Posada

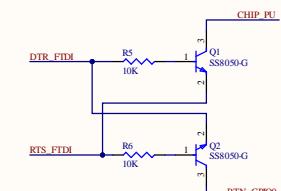
1 2 3 4 5 6



DEBUG LED 1 DEBUG LED 2

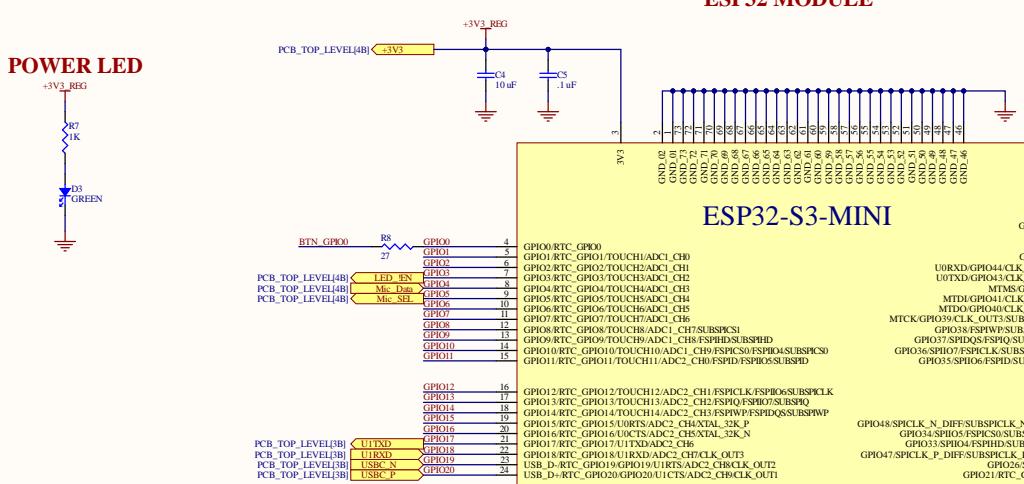


SERIAL SIGNALS HANDLING

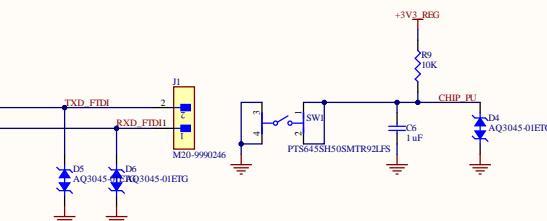


ESP32 MODULE

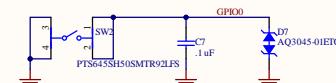
POWER LED



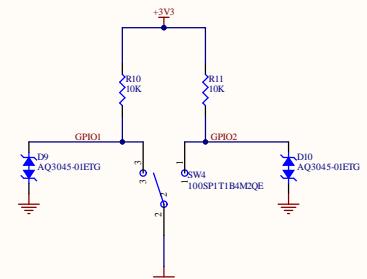
RESET BUTTON



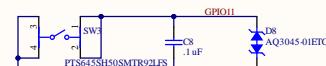
BOOT / USER BUTTON



MODE SWITCH



USER BUTTON



Title		Micro-controller	
Size C	Number		Revision V0
Date:	2/15/2026	Sheet of	<u>Manuel Posada</u>

A

A

B

B

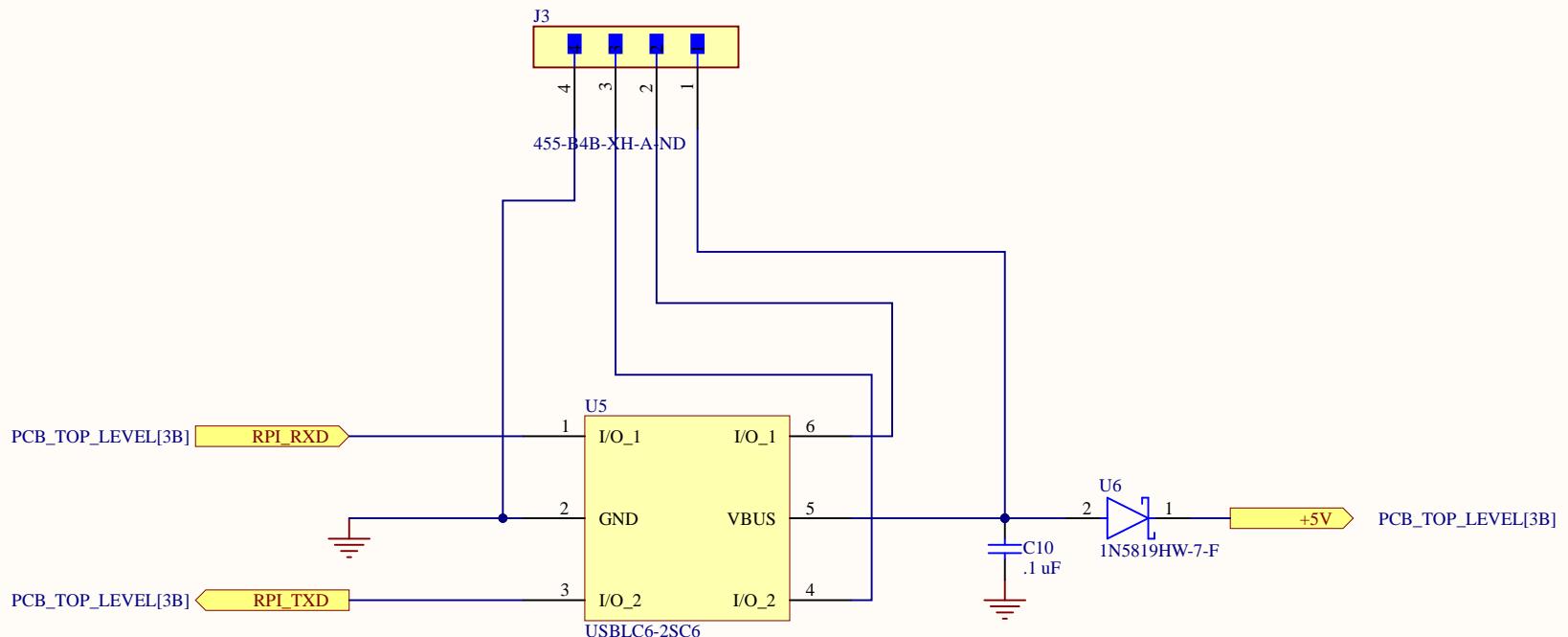
C

C

D

D

UART



Title

UART

Size

A

Number

Revision

V0

Date: 2/15/2026 Sheet of Manuel Posada

File: C:\Users\...\UART.SchDoc Drawn By: Manuel Posada

A

A

B

B

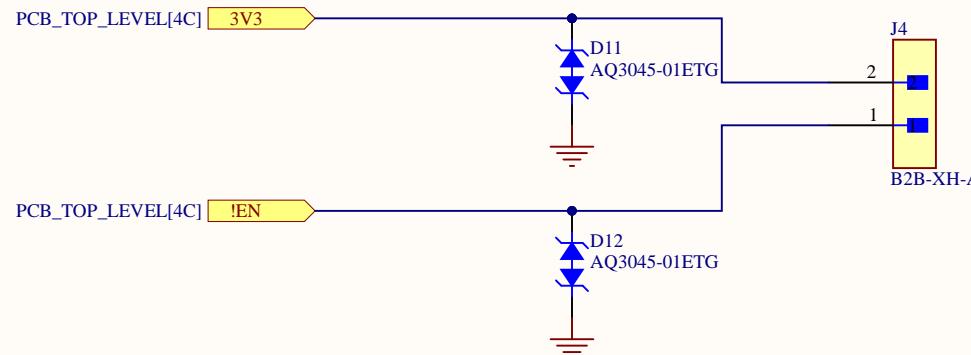
C

C

D

D

LED Indicator

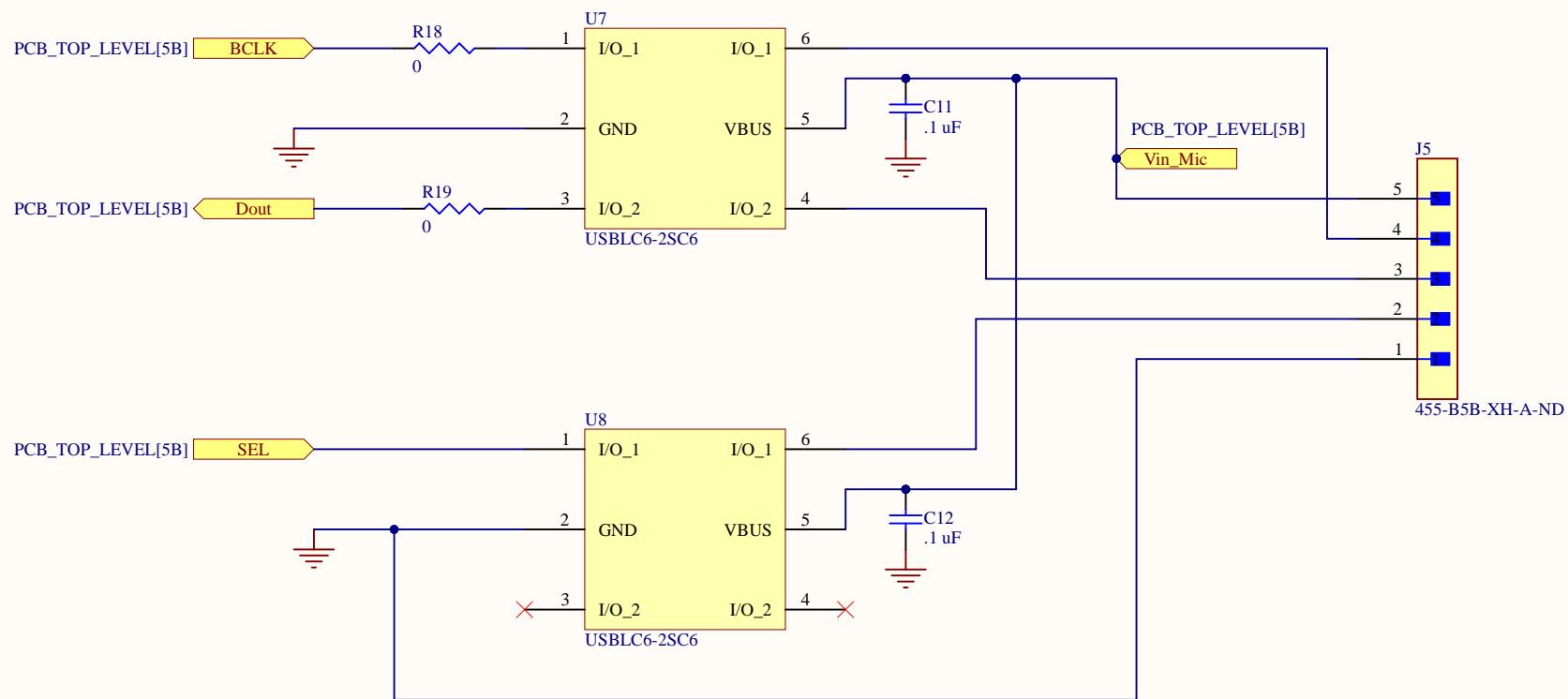


Title		
Size	Number	Revision
A		
Date: 2/15/2026		Sheet of
File: C:\Users\..\LED_Indicator.SchDoc		Drawn By:

A

A

MICROPHONE



B

B

C

C

D

D

Title		
Microphone		Revision
Size	Number	
A		V0
Date:	2/15/2026	Sheet of
File:	C:\Users\...\Microphone.SchDoc	Drawn By:
		Manuel Posada