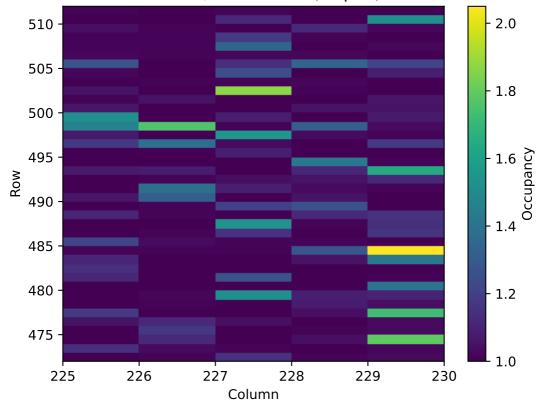


Chip W14R12 Version 4244b9c Registers: IBIAS = 50, ITHR = 20, ICASN = 15, IDB = 100, ITUNE = 53, VRESET = 143, VCASC = 228, VCASP = 93

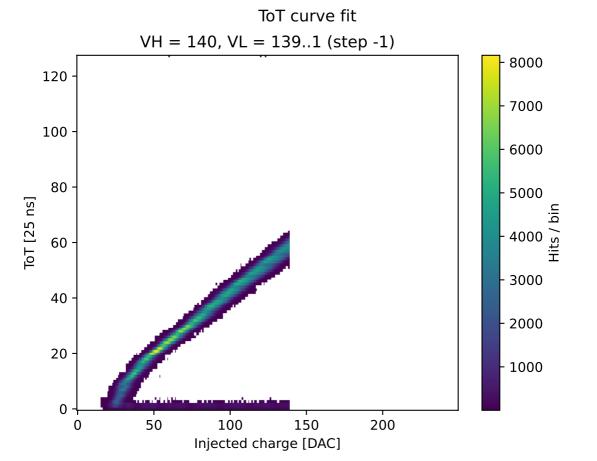
Hits map VH = 140, VL = 139..1 (step -1)



Noisy pixel

```
(225, 485), occupancy = 1.21
(225, 498), occupancy = 1.46
(225, 499), occupancy = 1.52
(225, 505), occupancy = 1.27
(226, 490), occupancy = 1.31
(226, 491), occupancy = 1.38
(226, 496), occupancy = 1.38
(226, 498), occupancy = 1.76
(227, 479), occupancy = 1.52
(227, 481), occupancy = 1.27
(227, 487), occupancy = 1.54
(227, 489), occupancy = 1.2
(227, 497), occupancy = 1.56
(227, 502), occupancy = 1.87
(227, 504), occupancy = 1.25
(227, 507), occupancy = 1.33
(228, 484), occupancy = 1.28
(228, 489), occupancy = 1.26
(228, 494), occupancy = 1.42
(228, 498), occupancy = 1.31
(228, 505), occupancy = 1.34
(229, 474), occupancy = 1.79
(229, 477), occupancy = 1.73
(229, 480), occupancy = 1.4
(229, 483), occupancy = 1.42
```

(229, 484), occupancy = 2.05(229, 493), occupancy = 1.64 (229, 505), occupancy = 1.21 (229, 510), occupancy = 1.51



Threshold distribution VH = 140, VL = 139..1 (step -1) 50 40 Pixels / bin 00 20 10 0

17.5

20.0

22.5

25.0

Threshold [DAC]

27.5

30.0

32.5

Threshold map

