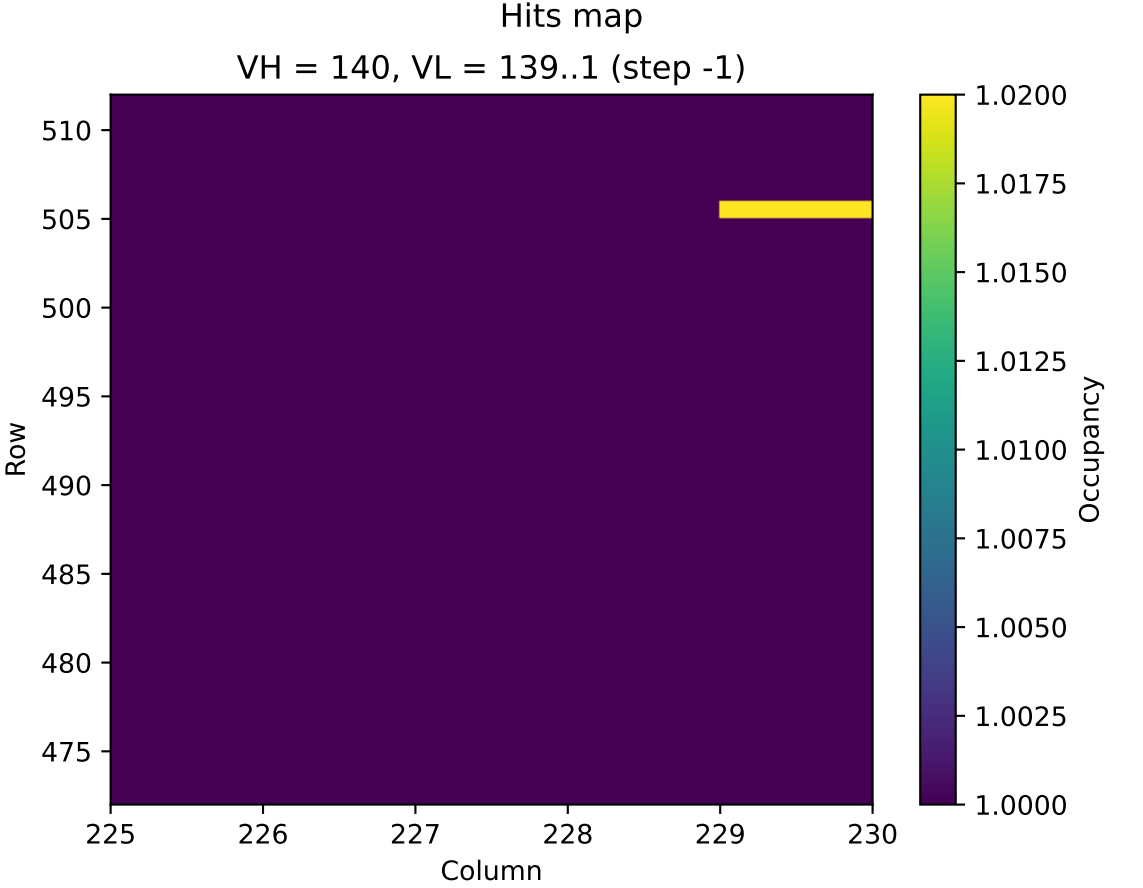
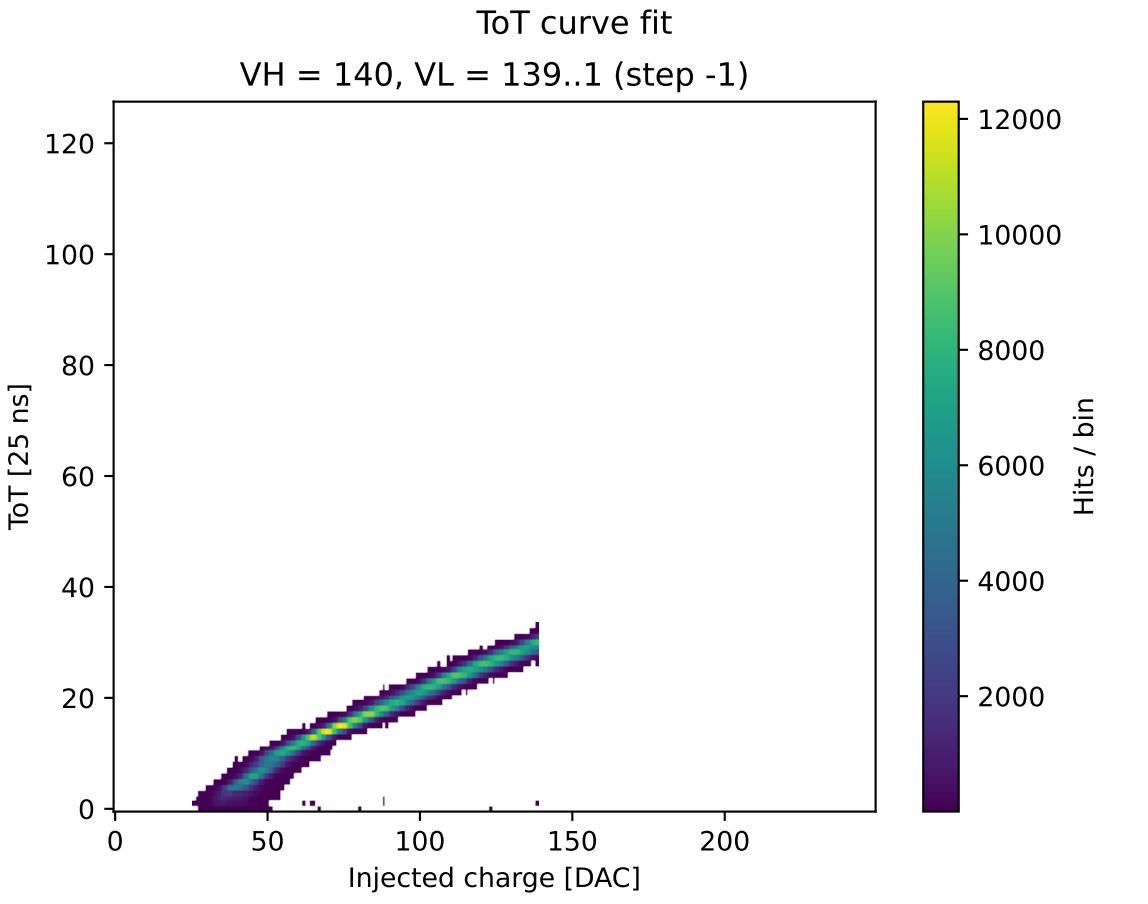
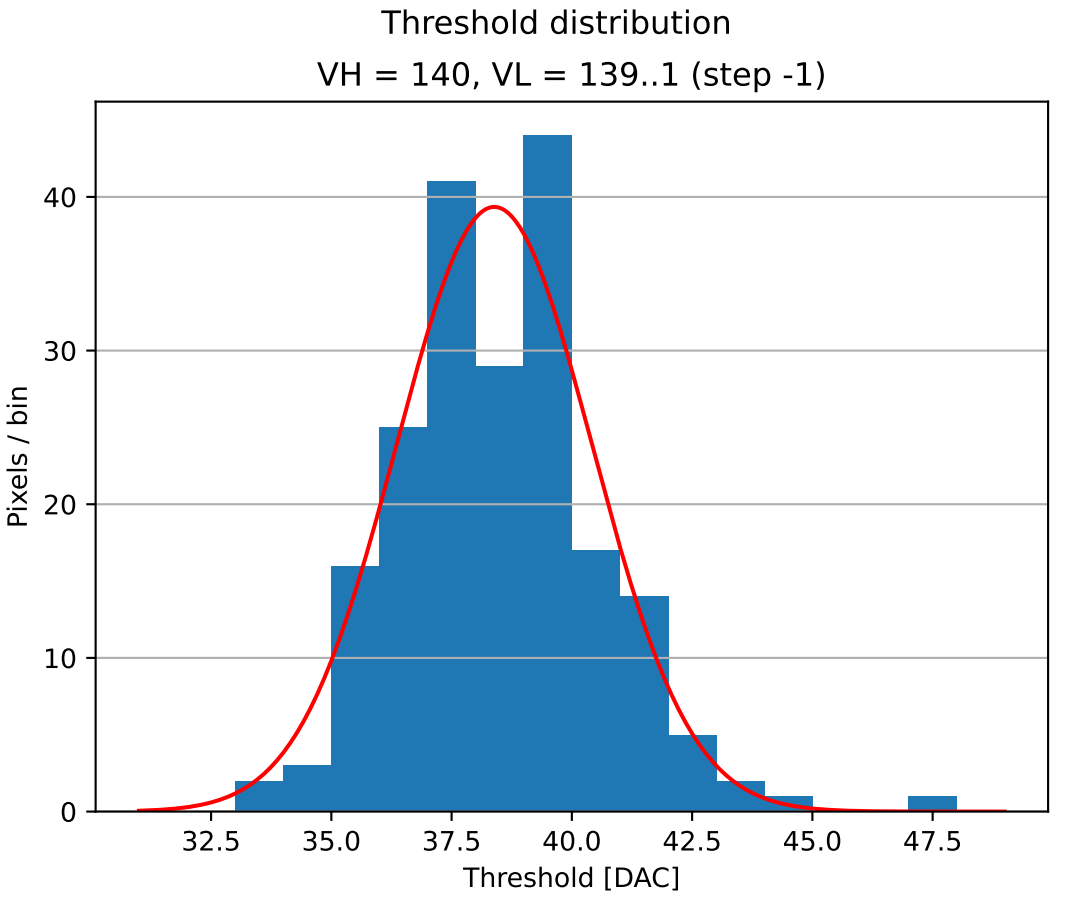
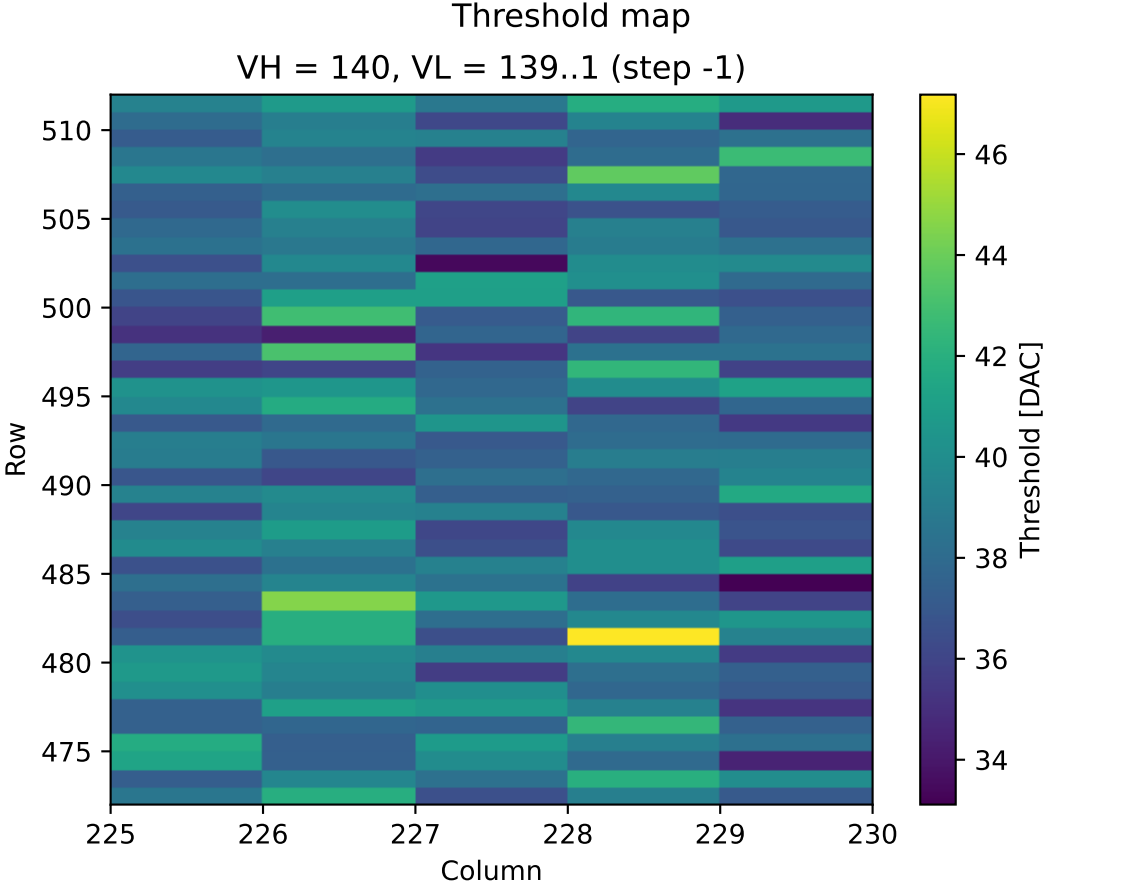
/mnt/c/Users/client/Desktop/tesi/tesi/Analisi/W14R12/simulations_comparison/ithr40 - icasn/20220913_175537_threshold_scan_interpreted.h5
Chip W14R12
Version 4244b9c
Registers: IBIAS = 50, ITHR = 40, ICASN = 10, IDB = 100, ITUNE = 53, VRESET = 143, VCASC = 228, VCASP = 93


Hits map
VH = 140, VL = 139..1 (step -1)
Row
510
505
500
495
490
485
480
475
Column
225
226
227
228
229
230
Occupancy
1.0200
1.0175
1.0150
1.0125
1.0100
1.0075
1.0050
1.0025
1.0000


ToT curve fit
VH = 140, VL = 139..1 (step -1)
ToT [25 ns]
Injected charge [DAC]
Hits / bin
0
20
40
60
80
100
120
0
50
100
150
200
2000
4000
6000
8000
10000
12000


Threshold distribution
VH = 140, VL = 139..1 (step -1)
Pixels / bin
0
10
20
30
40
32.5
35.0
37.5
40.0
42.5
45.0
47.5
Threshold [DAC]


Threshold map
VH = 140, VL = 139..1 (step -1)
Row
510
505
500
495
490
485
480
475
Column
225
226
227
228
229
230
Threshold [DAC]
46
44
42
40
38
36
34