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ECE-C304 Lab – Assignment # 2

(2A,2B,2C due 4th Week, 2D,2E,2F,2G due 5th Week)

The lab is based on the handout supplied

*Note: Always use the part no CY8C29466-24PXI*

Lab 2A-Posted Interrupts

2A-1 What does this code do?

**This code will increment Port 1 every second. The LEDs will light up to represent the bits 0-4.**

2A-2 Do the LEDs operate as you predicted in 2A-1?

**Yes**

2A-3 With the sleep timer parameter at 8 Hz what should be the effect on the LEDs?

**Port 1 will increment every 1/8th of a second.**

2A\_4 Do the observations confirm your prediction? **Yes**

2A-5 Identify the register and the bits which determine the sleep timer period.

**Register:**  OSC\_CR0 **Bits:** 3 & 4

2A-6 Change the sleep timer parameter back to 1 Hz in the GUI but add code to change it to 8 Hz at

run time using information from 2A-5. Confirm you get the same LED behavior as in 2A-4

**(submit code)**

#include <m8c.h> // part specific constants and macros

#include "PSoCAPI.h" // PSoC API definitions for all User Modules

extern unsigned char bShadow = 0;

void main(void)

{

OSC\_CR0 ^= 0x10;

PRT1DR = 0;

while(1){

while((INT\_CLR0 & 0x40) == 0);//wait til set

INT\_CLR0 = INT\_CLR0 & ~0x40; //clear it

bShadow++;

PRT1DR = bShadow;

}

}

2A-7 Does the sleep timer frequency depend on the CPU clock? To determine this, change the

CPU clock to 750 KHz in the code (do not use GUI). Find the registers and bits to achieve this.

Does this change alter the LED behavior?

**No**

2A-8 Convert the entire code of 2A-1 to Assembly language and show that it works. **(submit code)**

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Lab 2B- Pending Interrupts

2B-1 Why might you want to use the sleep mode instead of polling for the posted interrupt?

2B-2 Why might you want to poll instead of putting the system into sleep mode?

2B-3 Do this exercise without using the built-in routines or the GUI. You have to find the

appropriate registers and bits to set the drive mode for the pins , to put the system

in sleep mode, the sleep timer frequency etc. and write code for these **(submit code)**

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Lab 2C – Global Interrupts

2C-1 How much code space ia available at this vector location?

2C-2 Suppose more space is needed, what can you do?

2C-3 Why is it not necessary to clear the posted interrupt in this case?

2C-4 What does the Interrupt Service Routine (ISR) do?

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Lab 2D – Assembly Language ISRs

2D-1 How many bytes are needed for this instruction?

2D-2 Suppose ljmp in location 64h is changed to lcall, what other changes are required?

Is it advisable to do so? Why or why not?

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Lab 2E – C Language ISRs

2E- 1 Why can’t the function accept parameters or return a result?

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Lab 2F – Software Interrupts

2F-1 In previous labs the posted interrupt was cleared without adding code to set ENSWINT to

Zero. Why did this work?

2F-2 How should this program work now?

2F-3 Does the actual operation agree with your prediction?

2F-4 What is the purpose of software interrupts?

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Lab 2G – Creating very small ISRs

2G-1 How many bytes does this handler take?

2G-2 How should the output behave?

2G-3 Does actual operation agree with your prediction?

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