





Workpackage 4 Verification & Validation Strategy Model-Based Testing in openETCS

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openETCS@ITEA2 Project

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Verification and Validation

Model-Based Testing



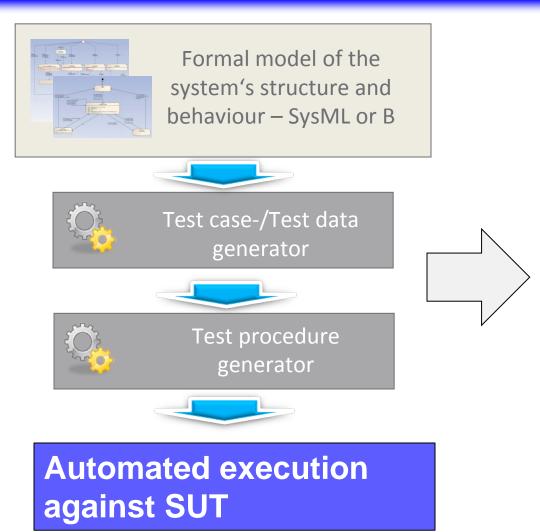
Model-Based Testing (MBT): instead of manual test case identification, test data calculation and test procedure coding,

- Create model specifying expected behaviour of system under test (SUT)
- Identify relevant test cases from model in an automated way
- Calculate test data by means of constraint solver
- Generate test procedures in model-based development style



Model-Based Testing Paradigm





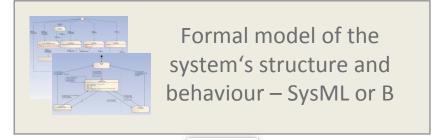




Model-Based Testing Paradigm



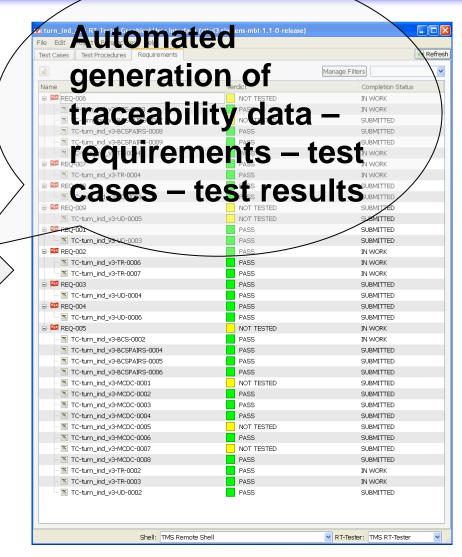








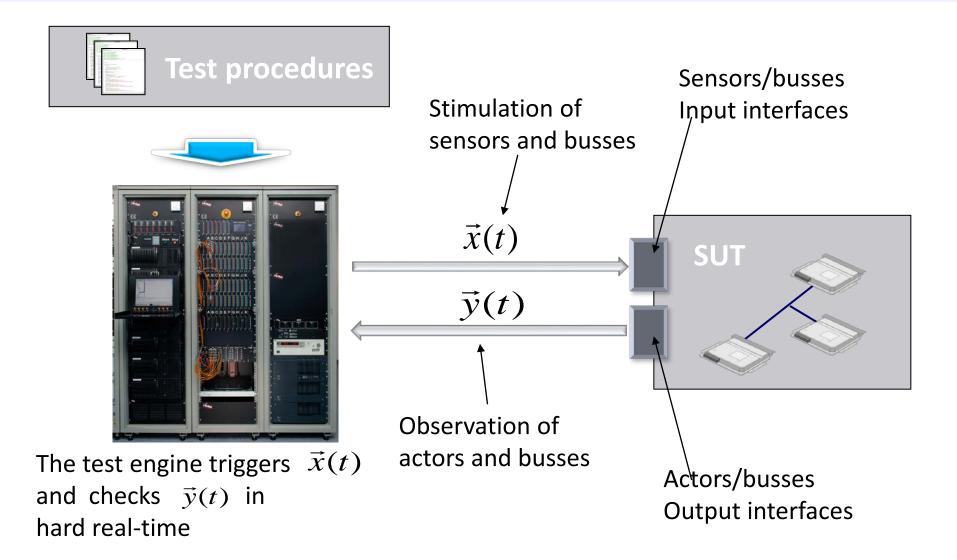
Automated execution against SUT





Model-Based Testing Paradigm – System Test







MBT Application Scenarios



Model-based development approach in openETCS suggests model-based testing approach

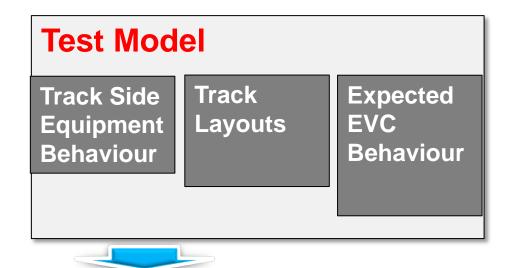
Three application scenarios for model-based testing in openETCS

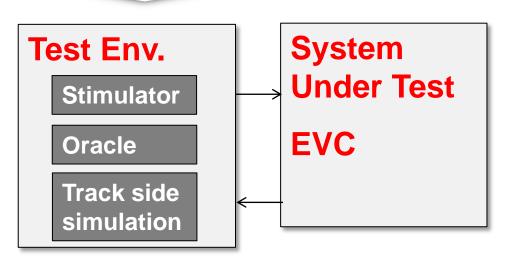
- System integration testing
- Software integration testing
- Object code verification



MBT in System Integration Testing







Model components for testing the EVC

- Expected behaviour of EVC
- Track layouts with different variants allowing for verification of all crucial properties of the EVC
- Simulations of track-side equipment behaviour



MBT for Object Code Verification



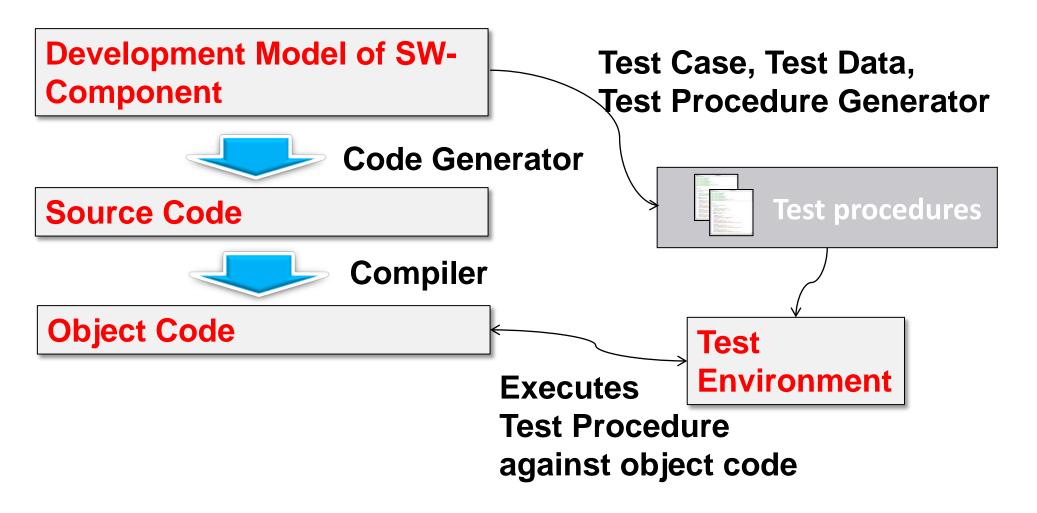
Objective: find efficient method for verifying that generated source code complies with model, and compiled object code complies with source code (or model)

- Use automated test suite derived from SW component model to show that source and object code comply with model
- No code generator and code generator validation required



MBT for Object Code Verification







Conclusion



Model-based testing has considerable benefits for openETCS

- Efficiency gain by automated test case, test data, and test procedure generation from the software component's sub-model
- Testing can always be performed on 3 levels
 - Model-in-the-loop V&V for the model
 - Software-in-the-loop Verify code
 - Hardware-in-the-loop V&V for integrated HW/SW system





Thank you for your attention!



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