



**General Sir John Kotelawala Defence University**  
**Department of Electrical, Electronics and Telecommunication Engineering**  
**Module Descriptor – Digital Signal Processing**

Module Code	ET 3142	Module Title	Digital Signal Processing			
Credits	2	Hours/ Semester	Lectures	24	Prerequisites	ET2103
GPA/ NGPA	GPA		Continuous Assessments/ Tutorials	12		
Module Objectives		To provide the students with the knowledge of Digital Signal processing				
Learning Outcomes		After the completion of this module, the learner will be able to, <b>LO1: Discuss the fourier transform in discrete time and discrete frequency domain</b> <b>LO2: Design digital FIR filters for given specifications</b> <b>LO3: Design digital IIR filters for given specifications</b> <b>LO4: Analyze a given filter for performance and stability</b>				
Contents		<b>Fourier transforms, sampling</b> A/D conversion and quantization, D/A conversion, Polyphase decomposition, Polyphase DFT filter banks, Bandpass sampling				<b>LO1</b>
		<b>The Discrete Fourier Transform</b> Definition of DFT and relation to Z-transform, Properties of the DFT, Linear and periodic convolution using the DFT, Zero padding, spectral leakage, resolution and windowing in the DFT				<b>LO1</b>
		<b>Filters</b> Averaging filter, Recursive smoother, First-order notch filter, Second-order unity gain resonator, All-pass filters, Comb filters, Equalization filters, Group delay, linear phase, all-pass, minimum phase, Stability and stability verification methods.				<b>LO4</b>
		<b>Digital filter design</b> Finite impulse response (FIR): Window design techniques, Kaiser window design technique, Equiripple approximations				<b>LO2</b>
		<b>Infinite impulse response (IIR):</b> Bilinear transform method, Examples of bilinear transform method, Structures and properties of FIR and IIR filters and review: IIR - Direct, parallel and cascaded realization, FIR – Direct and cascaded realizations, Coefficient quantization effects in digital filters				<b>LO3</b>
Laboratory/ Practical Sessions		<ul style="list-style-type: none"><li>Filter design using IIR, FIR</li><li>Signal Processing Using FFT</li><li>Testing filter stability in MatLab</li></ul>				<b>LO1-4</b>
Method of Assessment		Continuous assessments : 30% End semester examination : 70%				
References		<ol style="list-style-type: none"><li>Alan V. Oppenheim, (2009). Discrete-Time Signal Processing,3rd Edition, Pearson.</li><li>Monson Hayes, (2011). Schaums Outline of Digital Signal Processing, 2nd Edition, McGraw-Hill Education.</li></ol>				

Programme Outcomes												
PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	