

TSM_embHardw Preparatory Reading Lecture 1

There are two readings to prepare for the first lecture.

Reading 1:

Every year the EE Times group, the people behind embedded.com, publish an embedded survey. The one for 2013 can be found at

<http://de.slideshare.net/StephanCadene/embedded-mar1913>

Your task is to read this slide-show paying particular attention to the following slides:

The methodology: on slide 5

Current Embedded Design Environment: 14, 16, 17, 20, 22;

Embedded Design Process: 24, 25, 27-28;

Operating Systems: 32-34, 37, 42;

Microprocessors: 50-54, 61, 66-67;

FPGA's Memory, LCD's: 72-73;

Reading 2:

The V-Model is an accepted software development methodology. A short explanation, to be read, can be found here:

<http://support.sas.com/resources/papers/proceedings11/124-2011.pdf>