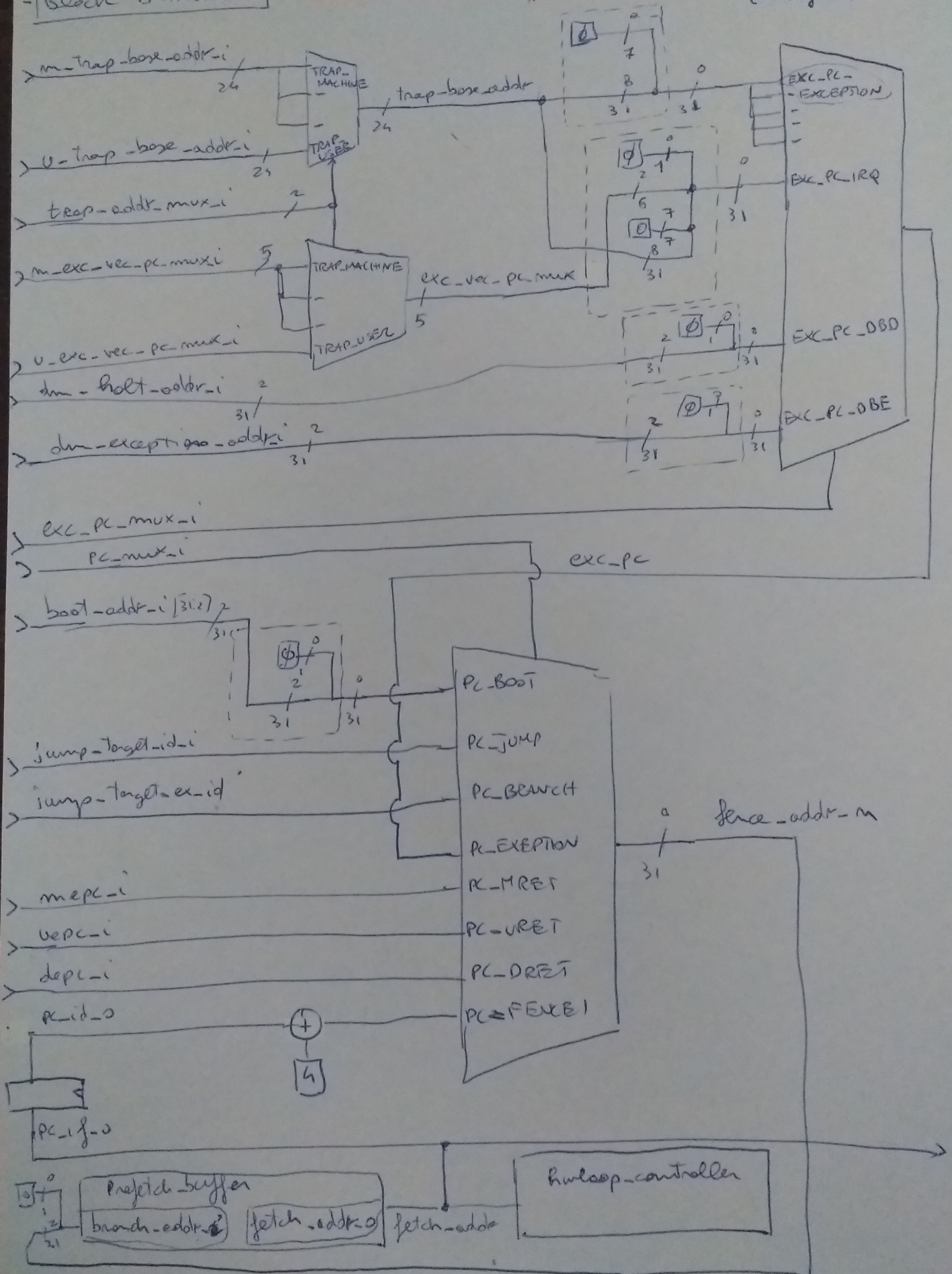


Program counter definition

PROGRAM COUNTER DEFINITION (PC-1f-0)

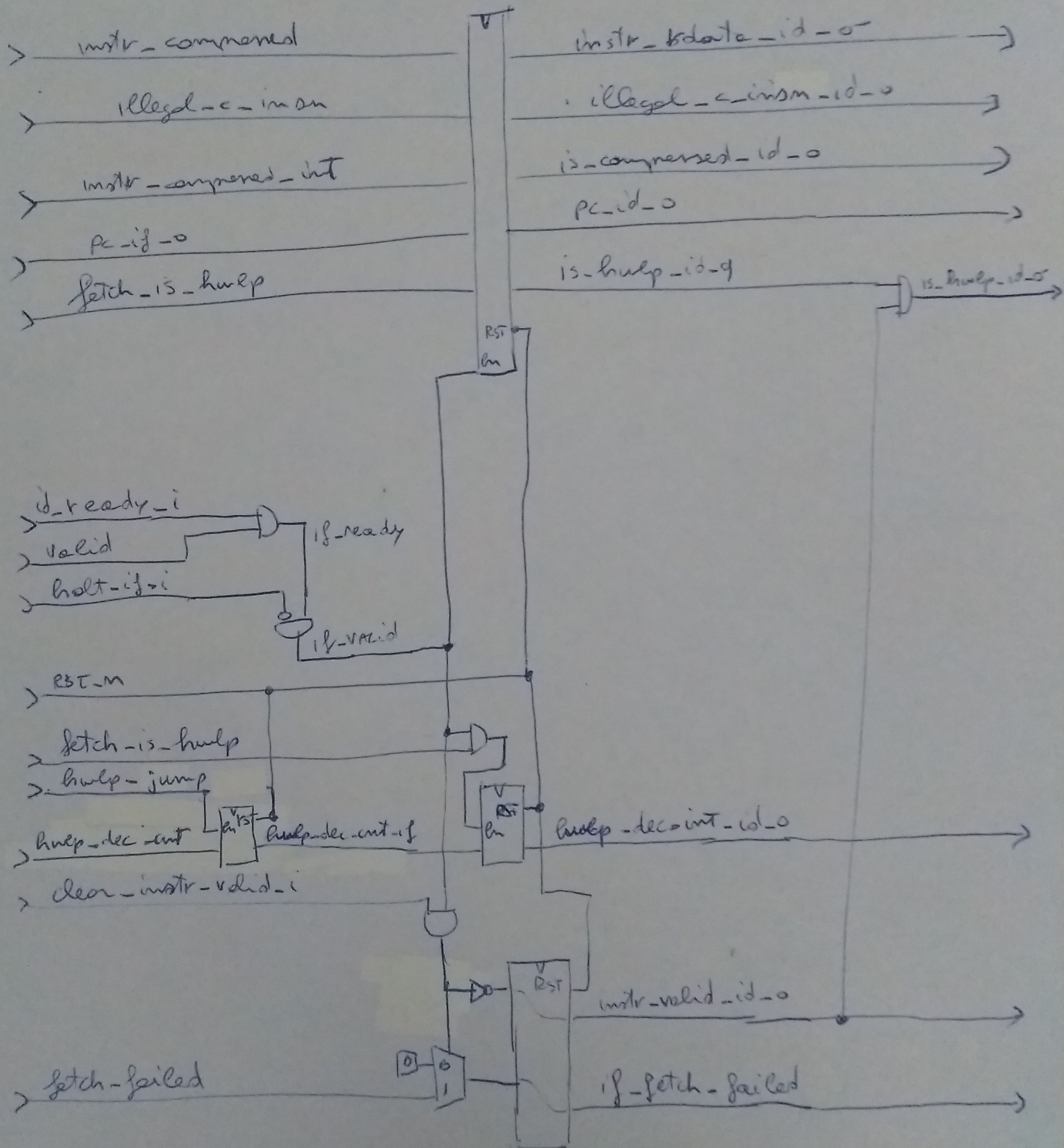
- Block diagram

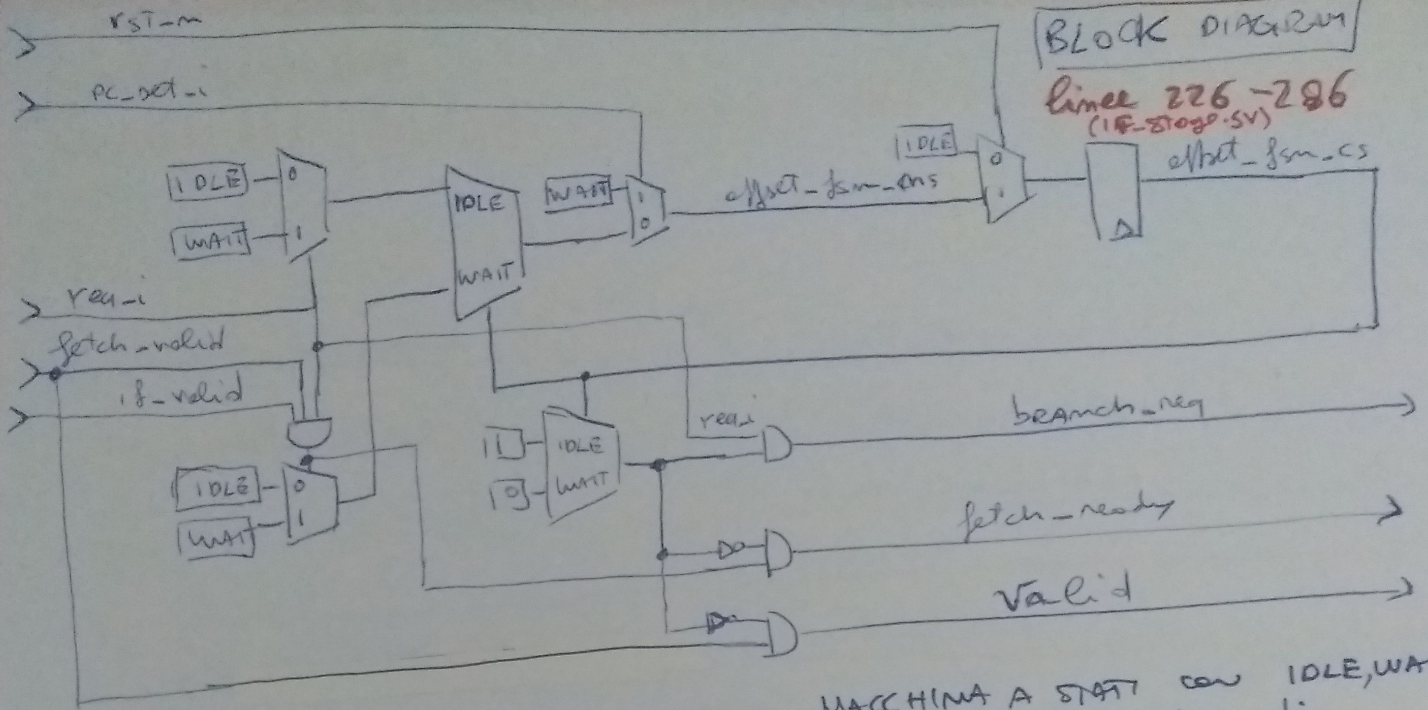
lines 138-224, 289-349



Lime 349-607

(IF-Stage - SV) REGISTRI DI PIPELINE dell'IF STAGE





MACCHINA A STATI con IDLE, WAIT
 per la generazione di
 $branch-req$, $fetch-ready$ & $valid$

Flow DIAGRAM

