Description: Prefetch buffer that decher instructions. This cut overlay long utical paths to the instruction cache overlay long utical paths to the instruction cache overlay long autical paths to the instruction cache overlay long autical paths to the instruction cache overlay long controller tested deside prefetch buffer:

- prefetch - controller

- fetch - fife

- obi-interferce

Block sox

requi	fetch-volid-o
branch-i branch-addrii	félan-addr-o
getch-ready-i instr-gnt-i instr-rdota-i	goes to (instr-req-0 instr linstr-oddr-0 men
instr-ruslid_i instr-err-i	buffer {busy=>
_instr_err_pmp-i	

