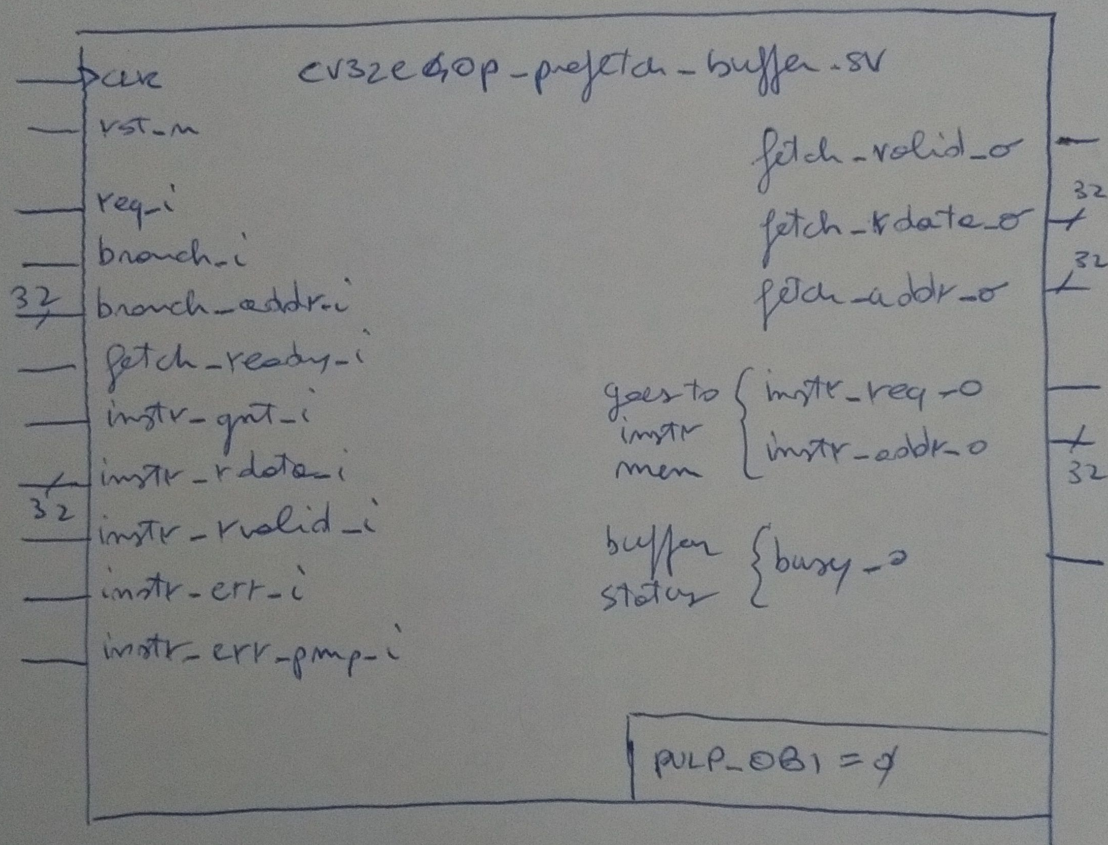


Description: Prefetch buffer that caches instructions. This unit overlay long critical paths to the instruction cache. There are 3 components created inside prefetch buffer:

- prefetch-controller
- fetch-fifo
- obi-interface

Block box



cv32e6pp-prefetch-buffer Internal block DIAGRAM

