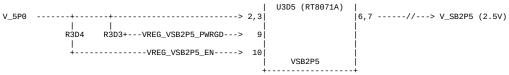
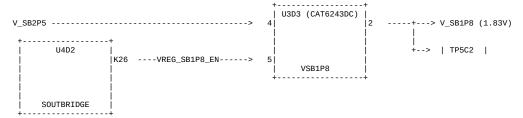
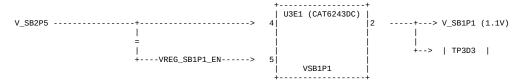
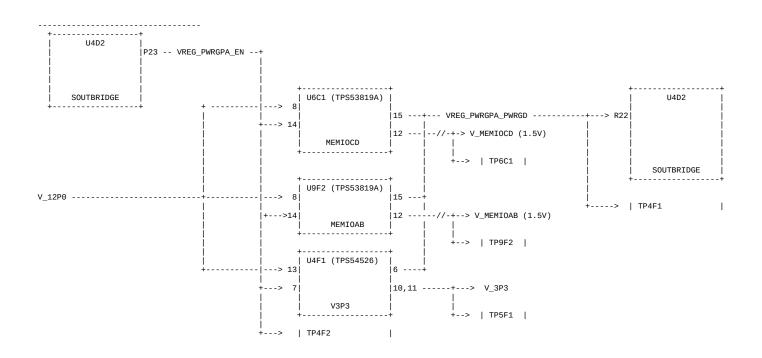


++	4	++
U3E2 (TPS53819A)		U4D2
V_12P0 VREG_V5P0_PWRGD 8 15 VREG_V5P0_PWRGD	-+> N22	l I
+		
U4D2		l l
N21 VREG_V5P0_EN+> 14 12//-+> V_5P0	1 !	
V5P0	1 !	l l
		l I
+> TP3E3		l I
+> TP3E1		SOUTBRIDGE
SOUTBRIDGE	1 +	++
++		
	1	
	+>	TP3E2









V_1P8STBY	+ R6R21 R6R21 VBAT	+ 5+> V_BAT(1.8V) - -	
V_3P3> 3	+	+> TP4D3 + 5+> V_SOC1P8(1.83V) 	
İ	VS0CPLL	+> TP3F1	
	_	_	
ļ +> 1	U6C3(CAT6241)	 5/ U6C6 /-+> V_BURN(1.9V)	
+ VREG_BURN_EN_R> 3 	 	 	
	+	+	
 +> 1,2	U6D1(CAT6241)	 7,8+> V_FUSE(0.95V)	
++			
U4D2 R21 VREG_PWRGPB_EN+> 4		+> TP6D1 	
	VFUSE +	 +	
	+	+	++
SOUTBRIDGE	U5F3 (TPS53819A)	 15+ VREG_PWRGPB_PWRGD+> P21	U4D2
V_12P0 >	 VREG, NBCORE		
	+	.+	
 > 1	U5E2 (NCP5269)	9+	
V_12P0/ V_5P0/ V_S0C1P8	 VREG, MEMCORE	+> 15 +>V_MEMCORE (1.25v - 0.95V) 	TP4F5
	+	+ +> TP5E2	
 +> 10	+	+ 	
V_3P3	 VREGS, VSOCPHY +	 6,7+>V_SOCPHY (0.95V) 	
+>	TP4F3	+> TP4F4 	

	+	-
	U7E1	
V_MEMIOAB>		< V_SOCPHY
V_MEMIOCD>		< V_3P3
V_CPUCORE>		< V_SOC1P8VDD
		< V_S0C1P8
V_GFXCORE>		< V_NBCORE
		< V_FUSE
		< V_MEMCORE
		< V_BAT
		< V_BURN
	 APU +	-

