

Project Name: Toucan Eye

Title: Audio

Author: Marcin Jagaczewski

Date: 02.03.2018

Version: 0.1

Wakeup pins: PA0, PC13, PE6, PA2, PC5
SPI1 - SPI_XXX_USB
SPI2 - BT_XXX
USART1 - DBG_UART_XX
USART2 - BT_UART_XX
I2C1 - DC_XXX
I2C2 - CAM_SIO_X
TIM1_CH1 - CAM_XVCLK
SYS_WKUP1 - BUTTON
SYS_WKUP2 - BT_IRQ

U1A	
BUTTON K3	PA0
SPI_CLK_USB L2	PA1
BT_UART_TX L3	PA2
EN1 BATMAN M3	PA3
CAM_HSYNC K4	PA4
DAC_CPU L4	PA5
CAM_PIXCLK J4	PA6
EN2 BATMAN M4	PA7
CAM_XVCLK D11	PA8
DBG_UART_TXD10	PA9
DBG_UART_RXC10	PA10
DUSB_MCU_N B12	PA11
DUSB_MCU_P B11	PA12
JTMS/SWDIO B10	PA13
JTCK/SWCLK A10	PA14
JTDI B9	PA15

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U1B	
SPI_CS_USB M5	PB0
MIC_DATIN J5	PB1
LED H5	PB2
JTDO/SWO B6	PB3
SPI_MISO_USB A5	PB4
SPI_MOSI_USB B5	PB5
DC_SCL D5	PB6
CAM_VSYNC C5	PB7
CAM_D6 B4	PB8
BT_CS A3	PB9
CAM_SIO_C L9	PB10
CAM_SIO_D K9	PB11
CAM_RESET L10	PB12
BT_SCK M11	PB13
CAM_PWDN K10	PB14
	PB15

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U1C	
MIC_LR H3	PC0
BT_MOSI J2	PC1
BT_MISO H4	PC2
AMP_GAIN J3	PC3
AMP_SDb L5	PC4
AMP_SDb K5	PC5
CAM_D0 D12	PC6
CAM_D1 E9	PC7
CAM_D2 E10	PC8
CAM_D3 C12	PC9
CAM_D8 C9	PC10
CAM_D4 A9	PC11
CAM_D9 D9	PC12
BT_IRQ E4	PC13
	PC14
	PC15

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U1D	
PSRAM_D2 C8	PD0
PSRAM_D3 B8	PD1
	PD2
CAM_D5 D8	PD3
PSRAM_NOE C7	PD4
PSRAM_NWE D7	PD5
BT_UART_RX B7	PD6
PSRAM_NE1 E7	PD7
PSRAM_D13 H10	PD8
PSRAM_D14 H9	PD9
PSRAM_D15 H8	PD10
PSRAM_A16 G11	PD11
PSRAM_A17 G9	PD12
PSRAM_A18 G10	PD13
PSRAM_D0 G8	PD14
PSRAM_D1 G7	PD15

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U1E	
PSRAM_NBL0 A2	PE0
PSRAM_NBL1 C4	PE1
PSRAM_ZZ C3	PE2
PSRAM_A19 B3	PE3
PSRAM_A20 D3	PE4
PSRAM_A21 C2	PE5
CAM_D7 D4	PE6
PSRAM_D4 G6	PE7
PSRAM_D5 K7	PE8
PSRAM_D6 J7	PE9
PSRAM_D7 L8	PE10
PSRAM_D8 H7	PE11
PSRAM_D9 M9	PE12
PSRAM_D10 J8	PE13
PSRAM_D11 M10	PE14
PSRAM_D12 K8	PE15

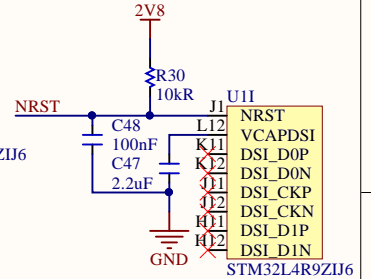
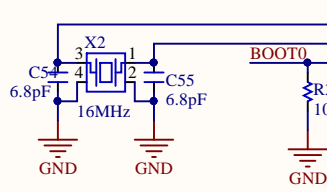
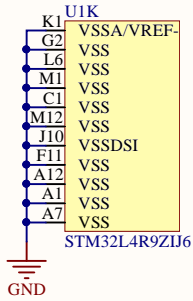
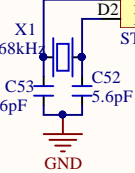
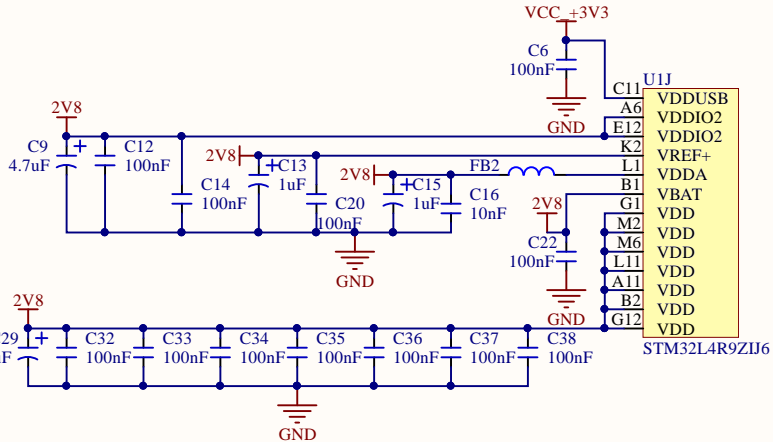
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U1F	
PSRAM_A0 E3	PF0
PSRAM_A1 E2	PF1
PSRAM_A2 E1	PF2
PSRAM_A3 E5	PF3
PSRAM_A4 F3	PF4
PSRAM_A5 F4	PF5
MANBAT_INT F2	PF6
	PF7
	PF8
STAT_BATMAN G4	PF9
INT_BATCHAR G3	PF10
	PF11
PSRAM_A6 G5	PF12
PSRAM_A7 J6	PF13
PSRAM_A8 M7	PF14
PSRAM_A9 H6	PF15

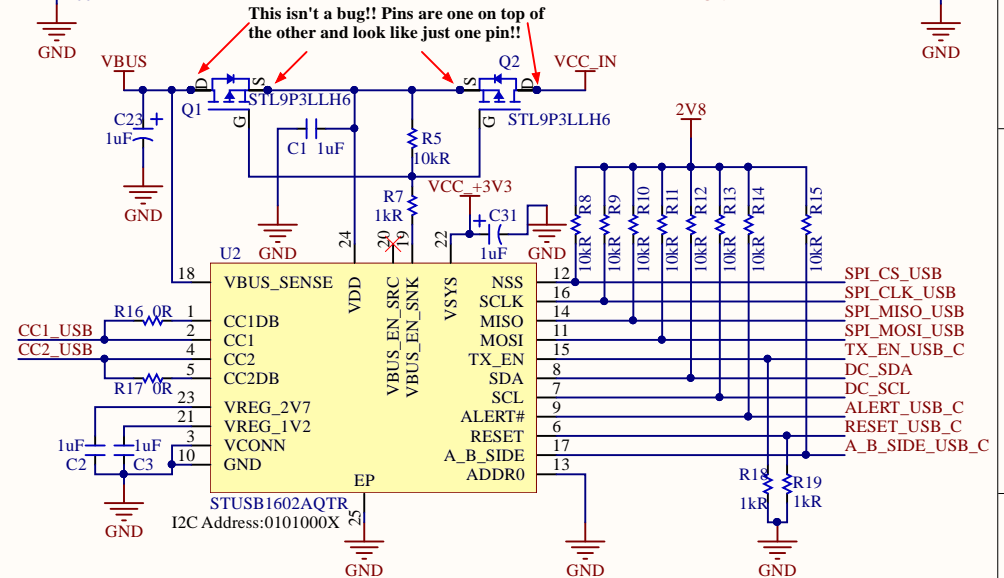
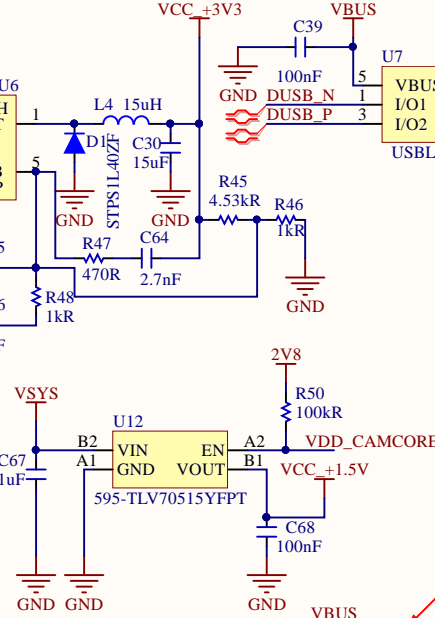
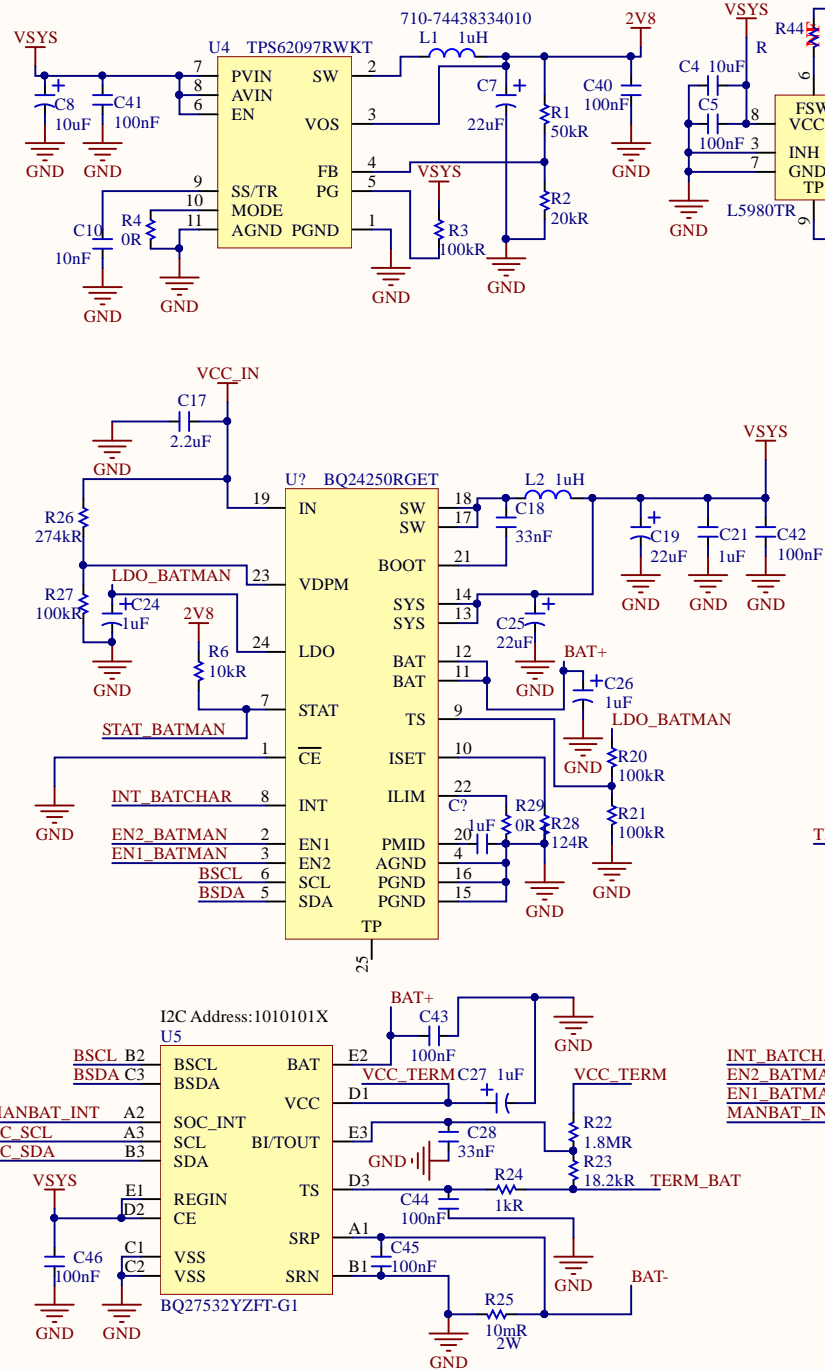
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U1G	
PSRAM_A10 L7	PG0
PSRAM_A11 M8	PG1
PSRAM_A12 F12	PG2
PSRAM_A13 F7	PG3
PSRAM_A14 F10	PG4
PSRAM_A15 F8	PG5
TX_EN_USB_C F9	PG6
MIC_CKOUT E11	PG7
ALERT_USB_C E8	PG8
B_SIDE_USB_C F6	PG9
RESET_USB_C E6	PG10
LIS_INT D6	PG12
DC_SDA C6	PG13

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Project Name:	Toucan Eye
Title:	CPU
Author:	Marcin Jagaczewski
Date:	02.03.2018
Version:	0.1



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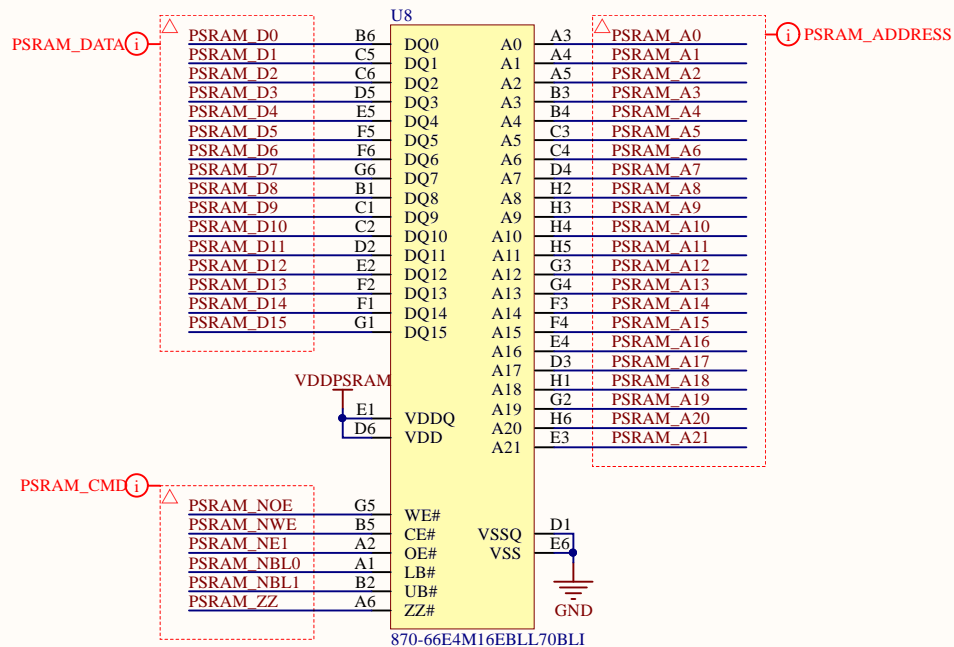
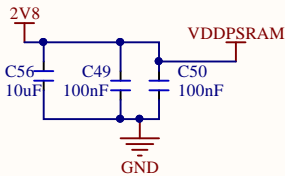
Project Name: Toucan Eye

Title: Power DC

Author: Marcin Jagaczewski

Date: 02.03.2018

Version: 0.1



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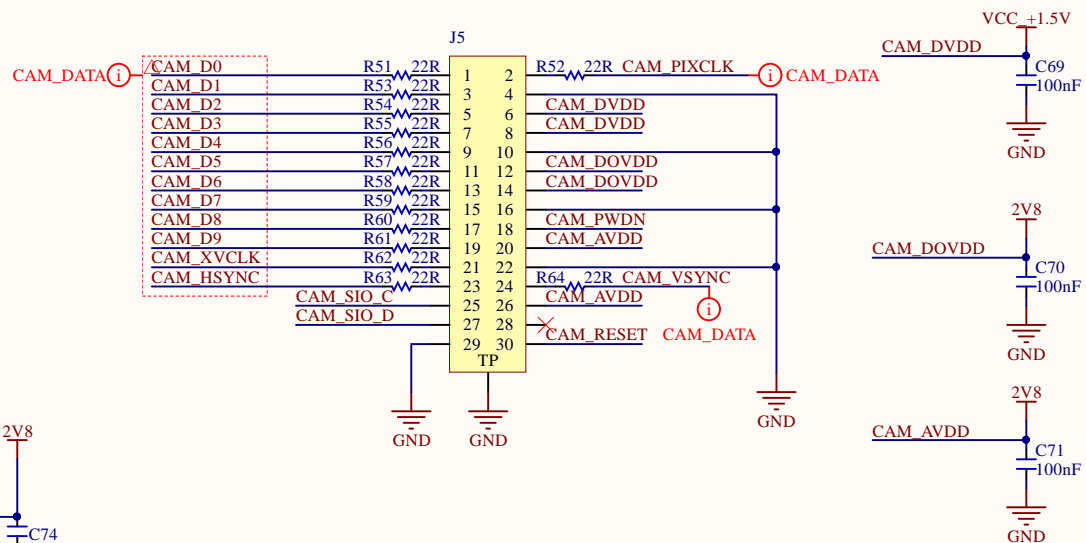
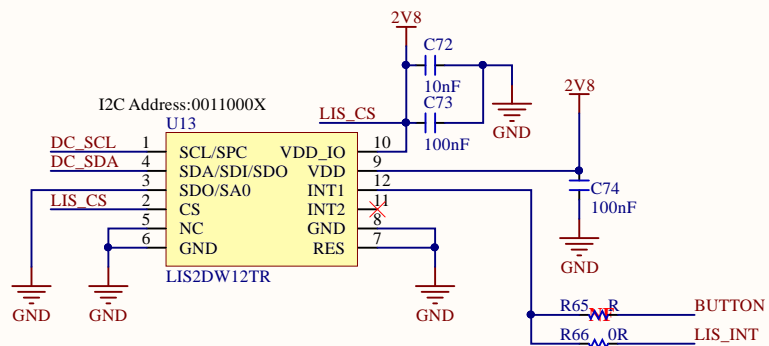
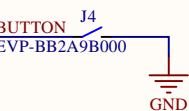
Project Name: Toucan Eye

Title: PSRAM

Author: Marcin Jagaczewski

Date: 02.03.2018

Version: 0.1



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Project Name: Toucan Eye

Title: Switch and camera

Author: Marcin Jagaczewski

Date: 02.03.2018

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