

| | | | | | |
|---|---|---|---|---|---|
| 1 | 2 | 3 | 4 | 5 | 6 |
| A | | | | | |
| B | | | | | |
| C | | | | | |
| D | | | | | |
| 1 | 2 | 3 | 4 | 5 | 6 |

ESQUEMATICOS

Fonte



File: fonte.kicad_sch

SMP



File: smp.kicad_sch

MCU

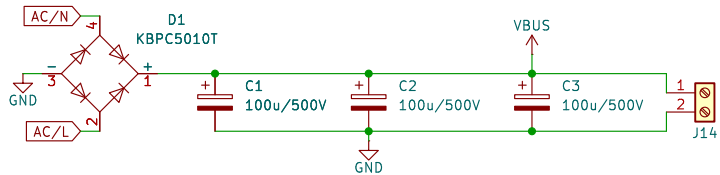


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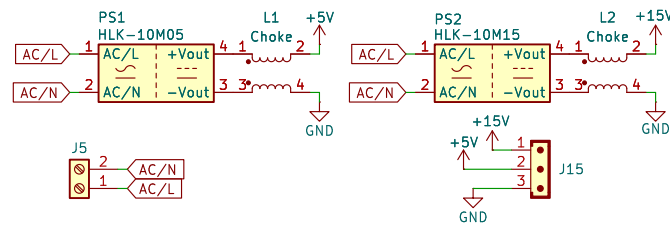
| | | |
|----------------------------|------------------|---------|
| UFPEL | | |
| Autor: José Fonseca | | |
| Sheet: / | | |
| File: TCC.kicad_sch | | |
| Title: Inversor de Bancada | | |
| Size: A4 | Date: 2025-02-21 | Rev: V1 |
| KiCad E.D.A. 8.0.1 | | Id: 1/4 |

H1 MountingHole_M3 H2 MountingHole_M3 H3 MountingHole_M3 H4 MountingHole_M3

BARRAMENTO CC



FONTES CHAVEADAS DE BAIXA TENSÃO



GSIC
UFPEL

Autor: José Fonseca

Sheet: /Fonte/
File: fonte.kicad_sch

Title: Fonte de Entrada do Inversor

Size: A4 Date: 2025-02-22

KiCad E.D.A. 8.0.1

Rev: V1

Id: 2/4

