

Distributed Systems

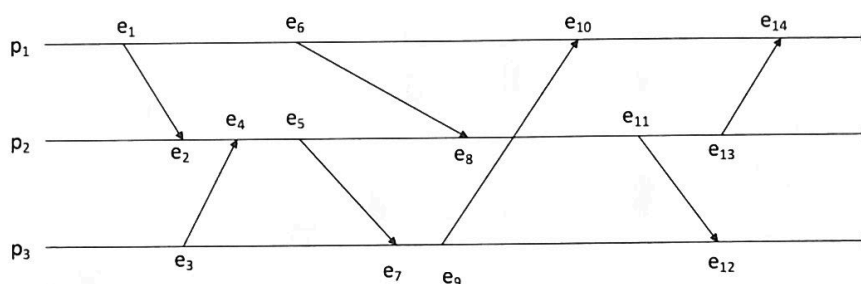
05/07/2024

(6 CFU)

Family Name _____ Name _____ Student ID _____

Ex 1: Introduce the leader election problem. Discuss how it is possible to build an oracle implementing the leader election abstraction in a synchronous system and explain why it is not possible to build such an oracle in an eventually synchronous system.

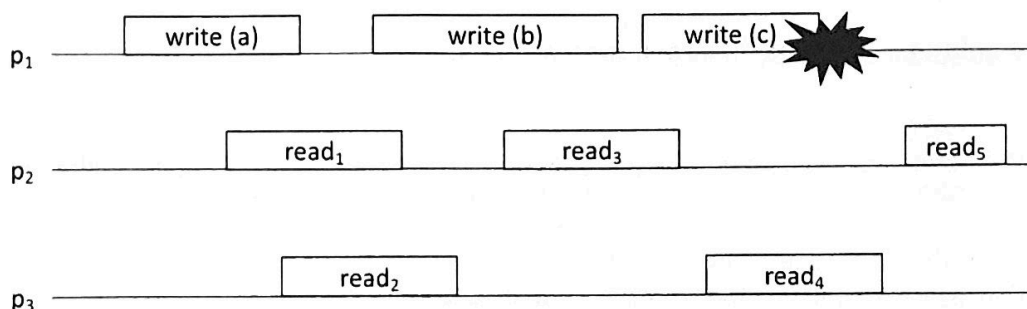
Ex 2: Let us consider the following execution history



Let us denote with $ck(e_i)$ the logical clock associated to event e_i . Considering the execution history shown in the figure above, assess the truthfulness of every statement and provide a motivation for your answer:

1	If we use scalar clocks for timestamping events, then $ck(e_6) > ck(e_5)$	T	F
2	e_2 happened before e_3 (according with Lamport's definition of happened-before)	T	F
3	If we use scalar clocks for timestamping events, then $ck(e_{14}) = ck(e_{10}) + 1$	T	F
4	e_6 and e_7 are concurrent events	T	F
5	If we use scalar clocks for timestamping events, then $ck(e_9) < ck(e_{11})$	T	F
6	If we use vector clocks for timestamping events, then $ck(e_{13}) = [4, 6, 4]$	T	F
7	If we use vector clocks for timestamping events, then $ck(e_1) = ck(e_3)$	T	F
8	If we use vector clocks for timestamping events, then $ck(e_5)$ and $ck(e_{10})$ are not comparable	T	F
9	If we use vector clocks for timestamping events, then $ck(e_8) < ck(e_{10})$	T	F
10	If we use vector clocks for timestamping events, then $ck(e_4) = [1, 1, 1]$	T	F

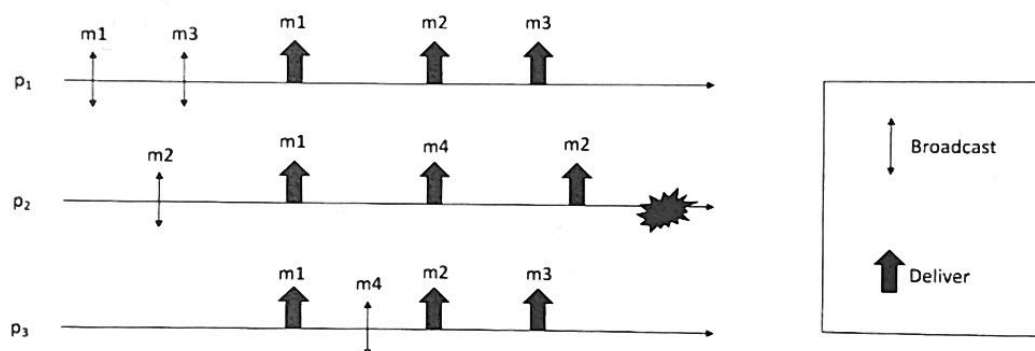
Ex 3: Let us consider the following execution history



Assuming that the initial value stored in the register is 0, assess the truthfulness of every statement and provide a motivation for your answer:

1	If the proposed run refers to a regular register, then a is a valid value for $read_1$	T	F
2	If the proposed run refers to a regular register, then 0 is not a valid value for $read_3$	T	F
3	If the proposed run refers to a regular register, then a is not a valid value for $read_2$	T	F
4	If the proposed run refers to a regular register, then b is a valid value all read operations	T	F
5	If the proposed run refers to a regular register, then $read_3$ may return only a and b	T	F
6	If the proposed run refers to an atomic register, then $read_1$ and $read_2$ may return different values	T	F
7	If the proposed run refers to an atomic register, then $read_3$ returns b if and only if $read_2$ returns b	T	F
8	If the proposed run refers to an atomic register, then $read_4$ and $read_5$ must always return the same value	T	F
9	If the proposed run refers to an atomic register and $read_4$ returns c , then $read_5$ may returned b or c	T	F
10	If the proposed run refers to an atomic register then $read_5$ can never return the value c	T	F

Ex 4: Consider the partial execution in the following figure



Given the run depicted in the figure state the truthfulness of the following sentences:			
a	The run satisfies the Reliable Broadcast specification	T	F
b	The run satisfies the Best Effort Broadcast specification	T	F
c	The strongest ordering property satisfied is SUTO	T	F
d	The WUTO ordering property is satisfied	T	F
e	The run satisfies the FIFO ordering property	T	F
f	The run satisfies the Causal ordering property	T	F
g	If correct processes add the delivery of m_4 as last message, then the resulting run satisfies the Reliable Broadcast specification	T	F
h	If correct processes add the delivery of m_4 as last message, then the resulting run satisfies TO (UA, WNUTO)	T	F
i	If correct processes add the delivery of m_4 between the delivery of m_2 and m_3 , then the resulting run does not satisfy causal order	T	F
l	If p_2 adds the delivery of m_4 as last message, then the resulting run satisfies the Reliable Broadcast specification	T	F

For each point, provide a justification for your answer

Ex 5: Consider a distributed systems composed by n processes p_1, p_2, \dots, p_n , with a unique identifier that are structured as a binary tree. Messages are exchanged between processes over the edges of the tree which acts like perfect point-to-point links. Each process p_i has stored the identifiers of its neighbors into the local variables FATHER, R_CHILD e L_CHILD representing respectively the father of p_i , the right child and the left child (if they exists).

- Write the pseudo code of an algorithm implementing a primitive of total order broadcast assuming no process fails.

- b) Discuss which properties of the total order are violated in the presence of even a single crash of a process.
- c) Discuss how to modify your algorithm assuming that processes may fail by crash but are equipped with a perfect failure detector.

According to the Italian law 675 of the 31/12/96, I authorize the instructor of the course to publish on the web site of the course results of the exams.

Signature: _____

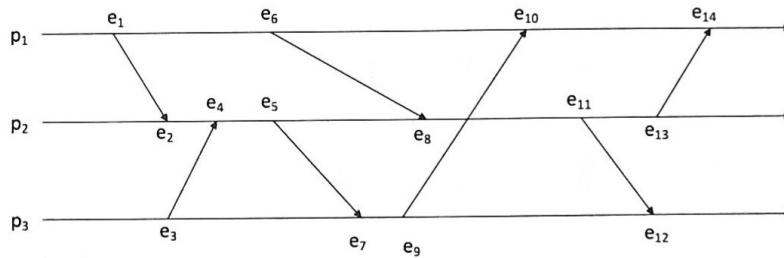
Ex 1: Introduce the leader election problem. Discuss how it is possible to build an oracle implementing the leader election abstraction in a synchronous system and explain why it is not possible to build such an oracle in an eventually synchronous system.

ELECTING A LEADER CONSISTS OF IDENTIFYING A PROCESS THAT ACTS AS A COORDINATOR BETWEEN DISTRIBUTED PROCESSES. A LEADER CAN MANAGE EXCLUSIVE ACCESS TO A SHARED RESOURCE.

IN A SYNCHRONOUS SYSTEM, IT'S POSSIBLE TO BUILD AN ORACLE FOR THE ELECTION BY EXPLOITING THE FACT THAT THE MESSAGES ARE DELIVERED WITHIN A WELL KNOWN TIME AND THAT THE FAULTS CAN BE RELIABLY DETECTED USING A PFD. A POSSIBLE APPROACH IS TO ASSIGN EACH PROCESS A UNIQUE ID AND ENSURE THAT THE PROCESS WITH THE HIGHEST ID BECOMES THE LEADER, ENSURING THAT ALL THE PROCESSES ELECT THE SAME LEADER IN A FINISHED NUMBER OF STEPS.

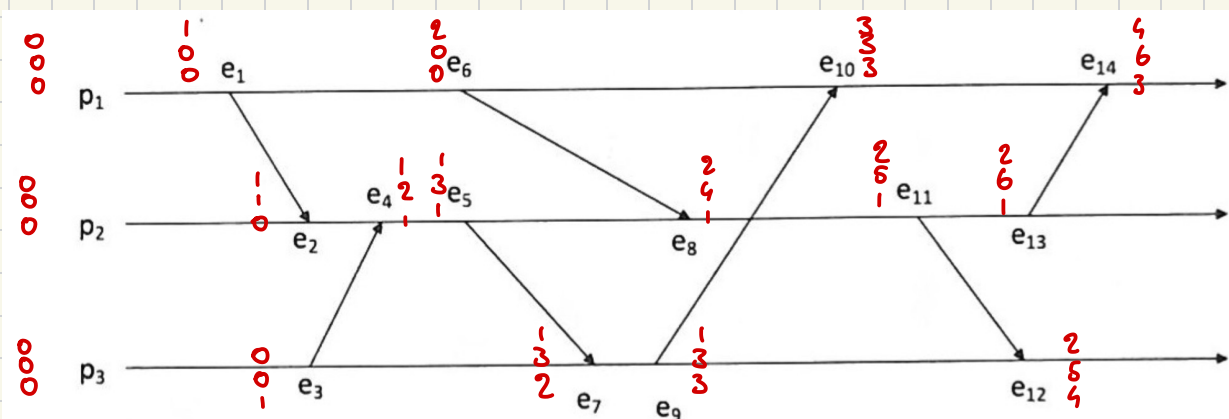
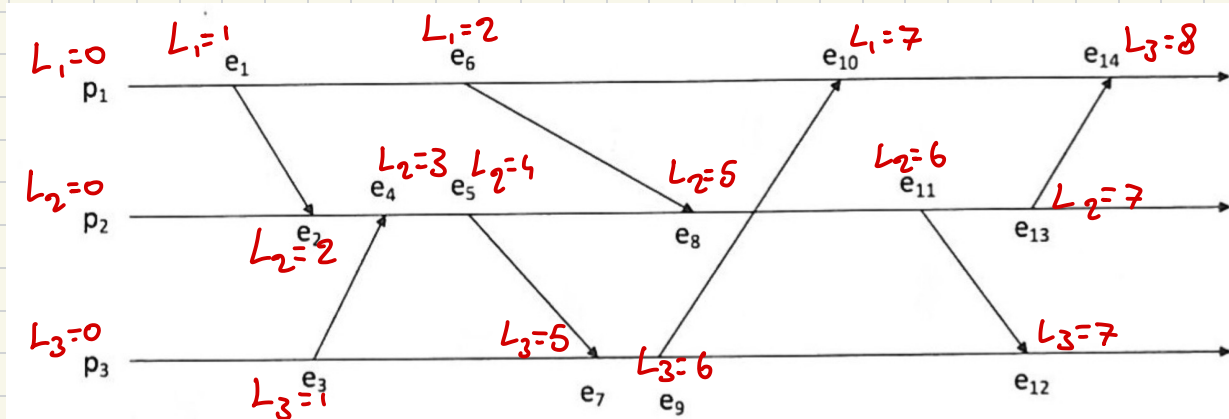
IN AN EVENTUALLY SYNCHRONOUS SYSTEM, THE ABSENCE OF A WELL KNOWN LIMIT ON COMMUNICATION TIMES AND THE DETECTION OF FAULTS PREVENTS THE CONSTRUCTION OF A PERFECT ORACLE. A PROCESS COULD BE ERRONEOUSLY CONSIDERED FAILED AND REPLACED AS A LEADER, LEADING TO INFINITE CHANGES IN THE ROLE OF LEADER WITHOUT EVER ACHIEVING A DEFINITE STABILIZATION.

Ex 2: Let us consider the following execution history

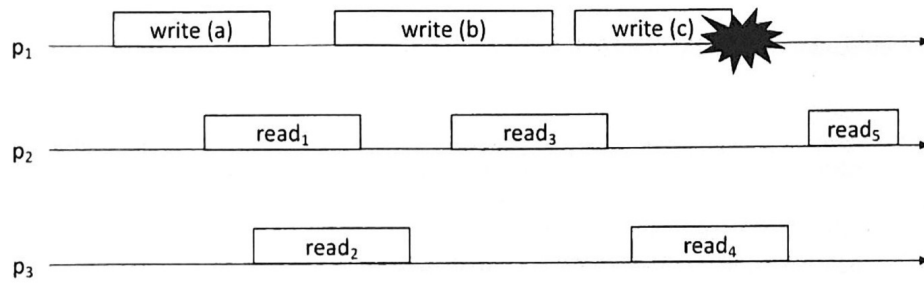


Let us denote with $ck(e_i)$ the logical clock associated to event e_i . Considering the execution history shown in the figure above, assess the truthfulness of every statement and provide a motivation for your answer:

1	If we use scalar clocks for timestamping events, then $ck(e_6) > ck(e_5)$	T	<input checked="" type="checkbox"/>
2	e_2 happened before e_3 (according with Lamport's definition of happened-before)	T	<input checked="" type="checkbox"/>
3	If we use scalar clocks for timestamping events, then $ck(e_{14}) = ck(e_{10}) + 1$	<input checked="" type="checkbox"/>	F
4	e_6 and e_7 are concurrent events	<input checked="" type="checkbox"/>	F
5	If we use scalar clocks for timestamping events, then $ck(e_9) < ck(e_{11})$	T	<input checked="" type="checkbox"/>
6	If we use vector clocks for timestamping events, then $ck(e_{13}) = [4, 6, 4]$	T	<input checked="" type="checkbox"/>
7	If we use vector clocks for timestamping events, then $ck(e_1) = ck(e_3)$	T	<input checked="" type="checkbox"/>
8	If we use vector clocks for timestamping events, then $ck(e_5)$ and $ck(e_{10})$ are not comparable	T	<input checked="" type="checkbox"/>
9	If we use vector clocks for timestamping events, then $ck(e_8) < ck(e_{10})$	T	<input checked="" type="checkbox"/>
10	If we use vector clocks for timestamping events, then $ck(e_4) = [1, 1, 1]$	T	<input checked="" type="checkbox"/>



Ex 3: Let us consider the following execution history

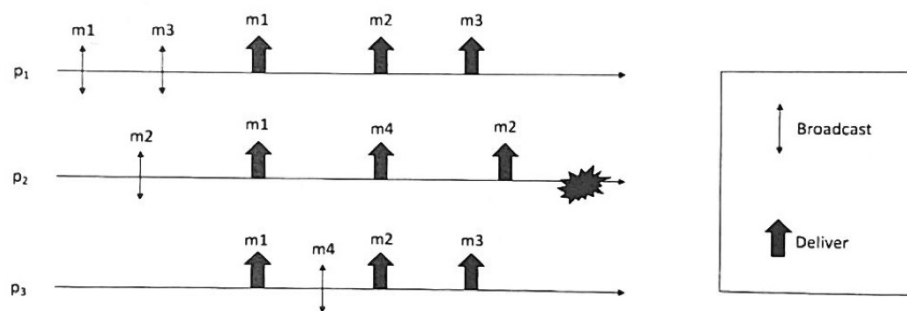


Assuming that the initial value stored in the register is 0, assess the truthfulness of every statement and provide a motivation for your answer:

1	If the proposed run refers to a regular register, then a is a valid value for read ₁	✓	F
2	If the proposed run refers to a regular register, then 0 is not a valid value for read ₃	✓	F
3	If the proposed run refers to a regular register, then a is not a valid value for read ₂	T	✗
4	If the proposed run refers to a regular register, then b is a valid value all read operations	✓	F
5	If the proposed run refers to a regular register, then read ₃ may return only a and b	T	✗
6	If the proposed run refers to an atomic register, then read ₁ and read ₂ may return different values	✓	F
7	If the proposed run refers to an atomic register, then read ₃ returns b if and only if read ₂ returns b	T	✗
8	If the proposed run refers to an atomic register, then read ₄ and read ₅ must always return the same value	T	✗
9	If the proposed run refers to an atomic register and read ₄ returns c , then read ₅ may returned b or c	T	✗
10	If the proposed run refers to an atomic register then read ₅ can never return the value c	T	✗

- 1) $R_1()$: 0, a, b
- 2) $R_3()$: a, b, c
- 3) $R_2()$: a, b, c
- 4) $R_5()$: b, c
- 5) $R_3()$: a, b, c
- 6) ONE CAN READ 0 AND THE OTHER a
- 7) $R_3()$: b ALSO IF $R_2()$: a
- 8) R_5 CAN RETURN c AND R_4 b
- 9) R_6 MUST RETURN c TO SATISFIE ATOMIC REGISTER
- 10) IT CAN

Ex 4: Consider the partial execution in the following figure

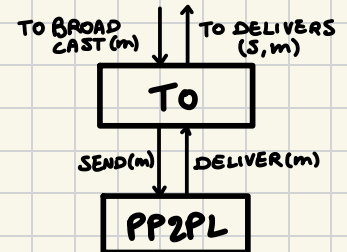
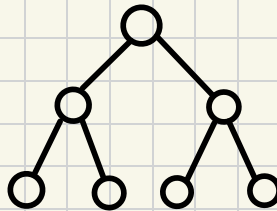


Given the run depicted in the figure state the truthfulness of the following sentences:		
a	The run satisfies the Reliable Broadcast specification	T X
b	The run satisfies the Best Effort Broadcast specification	T X
c	The strongest ordering property satisfied is SUTO	T X
d	The WUTO ordering property is satisfied	✓ F
e	The run satisfies the FIFO ordering property	✓ F
f	The run satisfies the Causal ordering property	✓ F
g	If correct processes add the delivery of m_4 as last message, then the resulting run satisfies the Reliable Broadcast specification	✓ F
h	If correct processes add the delivery of m_4 as last message, then the resulting run satisfies TO (UA, WNUTO)	✓ F
i	If correct processes add the delivery of m_4 between the delivery of m_2 and m_3 , then the resulting run does not satisfy causal order	T X
l	If p_2 adds the delivery of m_4 as last message, then the resulting run satisfies the Reliable Broadcast specification	T X

- VALIDITY IS NOT SATISFIED, m_4 IS NOT DELIVERED BY p_4
- VALIDITY IS NOT SATISFIED, m_4 IS NOT DELIVERED BY p_4
- THE STRONGEST IS WUTO
- IF p AND q BOTH TO DELIVER m AND m' , THEN p TO DELIVERS m BEFORE m' IF AND ONLY IF q TO DELIVERS m BEFORE m' .
- $m_1 \rightarrow m_3$ FIFO ORDER
- CASUAL ORDER = FIFO + LOCAL
 $m_1 \rightarrow m_3$ FIFO ORDER / $m_1 \rightarrow m_4$ LOCAL ORDER
- VALIDITY IS NOW SATISFIED
- p_2 DOESN'T SATISFY WNUTO
- THE RUN SATISFIES CASUAL
- VALIDITY IS NOT SATISFIED, m_4 IS NOT DELIVERED BY p_4

Ex 5: Consider a distributed systems composed by n processes p_1, p_2, \dots, p_n , with a unique identifier that are structured as a binary tree. Messages are exchanged between processes over the edges of the tree which acts like perfect point-to-point links. Each process p_i has stored the identifiers of its neighbors into the local variables FATHER, R_CHILD e L_CHILD representing respectively the father of p_i , the right child and the left child (if they exists).

- Write the pseudo code of an algorithm implementing a primitive of total order broadcast assuming no process fails.
- Discuss which properties of the total order are violated in the presence of even a single crash of a process.
- Discuss how to modify your algorithm assuming that processes may fail by crash but are equipped with a perfect failure detector.



UPON EVENT $\langle To, INIT \rangle$ DO

VALUES = \emptyset

FATHER = GET(FATHER, SELF) SE NULL È IL LEADER

R.CHILD = GET(R.CHILD, SELF)

L.CHILD = GET(L.CHILD, SELF)

$\tau_s = 0$ USED BY LEADER

NEXT. $\tau_s = 0$ USED BY ALL

UPON EVENT $\langle To, BROADCAST | [m, P] \rangle$ DO

IF FATHER == NULL THEN

$\tau_s = \tau_s + 1$

IF L.CHILD != NULL

TRIGGER $\langle PP2PL, SEND | [BRDCST, m, \tau_s, P] \rangle$ TO L.CHILD

IF R.CHILD != NULL

TRIGGER $\langle PP2PL, SEND | [BRDCST, m, \tau_s, P] \rangle$ TO R.CHILD

ELSE

TRIGGER $\langle PP2PL, SEND | [REQ, m, SELF] \rangle$ TO FATHER

UPON EVENT $\langle PP2PL, DELIVER | [REQ, m, P] \rangle$ FROM q

IF FATHER != NULL THEN

TRIGGER $\langle PP2PL, SEND | [REQ, m, P] \rangle$ TO FATHER

ELSE

TRIGGER $\langle To, BROADCAST | [m, P] \rangle$

UPON EVENT $\langle PP2PL, DELIVER | [BRDCST, m, \tau_s', P] \rangle$ FROM q

VALUES = VALUES $\cup \{m, \tau_s', P\}$

WHEN $\exists (m, \tau_s', P) \in \text{VALUES}$ SUCH AS $\tau_s' = \text{NEXT.}\tau_s$ THEN

VALUES = VALUES $\setminus \{m, \tau_s', P\}$

NEXT. $\tau_s = \text{NEXT.}\tau_s + 1$

TRIGGER $\langle To, DELIVER | m, P \rangle$

- b) WE WOULD BREAK VALIDITY, SINCE WE MAY LOOSE MESSAGES SO A BRDCAST REQ MAY NEVER BE DELIVERED BY ALL PROCESSES.
- c) IF WE HAD A CRASH OF A LEAF, NO PROBLEM. IN THE CASE OF A NODE IN BETWEEN WE MAY NEED TO CREATE A PARTITION. WE ALSO NEED TO CLEAR THE BUFFER OF FAILED PROCESSES AND UPDATE NEXT. \mathcal{I}_S ACCORDING "IF NEXT. $\mathcal{I}_S \equiv \mathcal{I}_S$. OF. FAILED. PROCESS THEN NEXT. \mathcal{I}_S++ ".