Marco Antônio Teixeira

Master's degree in Computer Science / Electrical Engineer | System Test Engineer

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Qualifications Summary

- Professional with more than 14 years experienced with Verification and Validation of Automotive Electronic Systems like Tracker devices, Electronic Control Units (ECUs) and Instrument Clusters, acquired in multinational companies.
- Master's degree title and some published articles, all of them related to the FPGA technology.
- Professor of Digital Systems and Architecture and Organization of Computers on Computer Science graduation courses.
- Electrical Engineer with Electrotechnical emphasis.
- Great experience on designing electrical power distribution of industrial plants and commercial establishments.

Main Achievements

- Project test manager of Trackers and Telematic electronic devices.
- Test Engineer, responsible for writing and executing functional tests of commercial vehicles, using CANoe/CANalyser Vector's tools and in accordance to ASPICE framework roles.
- Responsible for Software Integration tests of Electronic Control Units (ECUs) of passenger vehicles.
- Teaching graduation courses like Digital Systems and Computer Organization and Architecture by using Reconfigurable Computing tools (Quartus™ IDE – Altera®).
- Great experience in designing of electrical power distribution on industrial and commercial buildings, specifying electrical components of Illumination, power distribution and electrical AC Motors Command and Control centers.

- design of illumination system parts and specification of electrical circuits parts to control and protection of AC Motors.

Academic Titles

- Master Degree in Computing Science and Computational Mathematics – USP – Universidade de São Paulo (2000 – 2002).

  - Monograph: “Técnicas de reconfigurabilidade dos FPGAs da família APEX 20K”.

- Electrical Engineer with Electrotechnical emphasis – UNESP – Universidade Estadual Paulista (1989 – 1993).
- Technician in Electrotechnical – EESG José Martimiano da Silva (1982 – 1986).
- Industrial Electrician – SENAI – Serviço Nacional de Aprendizagem Industrial (1981 – 1982).

Journals Published Full Articles

- TEIXEIRA, Marco Antônio; MARQUES, E. . Uma Plataforma de Hardware Dinamicamente Reconfigurável. Seminário Integrado de Software e Hardware - SEMISH, Unicamp - Campinas, 2003.
- TEIXEIRA, Marco Antônio; RIBEIRO, A. A. L. ; WOLF, D. F. ; OSORIO, L. F. . ARCHITECT-R: A System for Reconfigurable Robots Design - An Overview and Initial Results. International Conference on Very Large Scale Integration, Montpellier, p. 60-64, 2001.
- TEIXEIRA, Marco Antônio; MARQUES, E. ; SCATENA, J. ; MEZENCIO, R. ; WOLF, D. F. ; RIBEIRO, A. A. L. . A Pipeline Hardware Implementation For An Artificial Neural Network. ENIA Encontro Nacional de Inteligencia Artificial, Fortaleza, 2001.
- TEIXEIRA, Marco Antônio; MARQUES, E. ; WOLF, D. F. ; RIBEIRO, A. A. L. . ARCHITECT: Um Sistema de Computação Reconfigurável. CORE2000 - Workshop de Computação Reconfigurável, Marília - SP, p. 42-45, 2000.

Professional Experience

Marelli Sistemas Automotivos Ltda. (08/2021 – Current)

Responsible to evaluate new modules of Software integration, related to Body Control Modules (BCMs) of passenger vehicles.

The activities are executed using CANalyser™ tool. Based on customers specifications, all software modules are checked. Project issues are stored on specific management tool (IBM – Rational Team Concert).

Continental Brasil Industria Automotiva (08/2007 – 08/2021)

Test Engineer of product like Instrument Clusters (ICs) and Electronic Control Units (ECUs), writing and executing functional test cases. Development of automated test cases based on proprietary automations tools.

Project Test Manager of Tracker and Telematic Devices, responsible to writing and executing functional test cases, based on internal specifications.
Acquired experience with CANoe® and CANalyser® Vector™ tools and with ASPICE rules and guidelines.

Centro Universitário Adventista de São Paulo – UNASP (02/2004 – 07/2007)

Professor of disciplines like Digital Systems, Computer Architecture and Organization and Introduction to C Language.

Development of practical classes by using Reconfigurable Computing development kits from Altera™ – Quartus II®.

Fundação Euripides de Marília (02/2001 – 12/2003)

Professor of disciplines like Digital Systems, Computer Organization and Architecture and VHSIC Hardware Description Language (VHDL).

Development of practical classes by using Reconfigurable Computing tools from Altera™ – Quartus II®.

Monribe – Industrial Electrical Buildings (02/1999 – 03/2000)

Designer of electrical panels related to illumination, power distribution and control and protection of high-power electrical motors. Commissioning of industrial electrical installations and High-Voltage cabins.

VCP – Votorantin Celulose e Papel (02/1994 – 08/1996)

Resident project designer of industrial illumination, control and power distribution. Development of technical designs, electrical diagrams and electrical part lists.

IR – Projects and mountings consulting (12/1985 – 03/1989)

Project designer of electrical installations related to Sugar and Alcohol factories. Designer of Command and control panel diagrams and power distribution of electrical generators.

Courses and other skills

- Graphical Computing (USP – 2003)
- Computational Vision (USP – 2003)
- Digital Video Processing (USP – 2003)
- Python Programming Language (Coursera – 2020)
- Master Native English – Speaking Skills and Grammar (Udemy – 2021)
- English language (EF-English Live On-Line course – ongoing)
- C Programming Language (Udemy – ongoing)
- MATLAB® and Simulink® Onramp training courses (MathWorks™ – 2021)
- CAPL – An Event Driven Programming Language (Vector™)

Language skills

Native language: Portuguese

English language: Advanced writing and reading.
Good conversation.