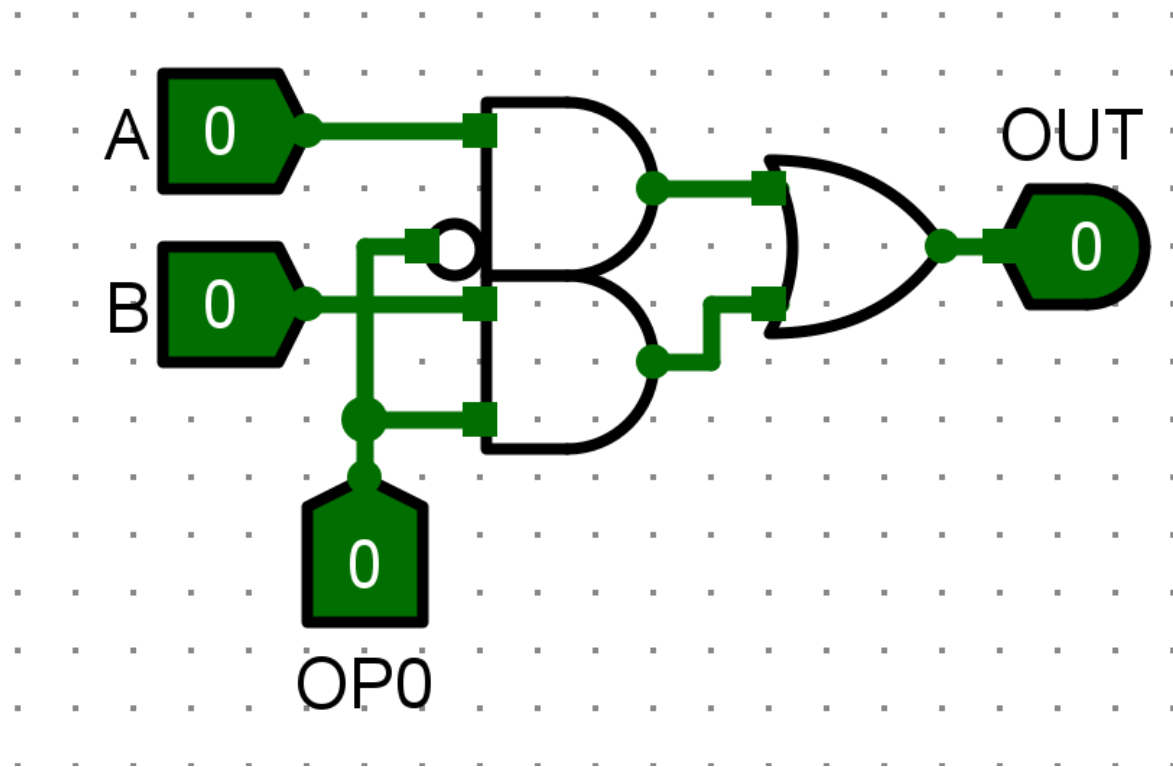
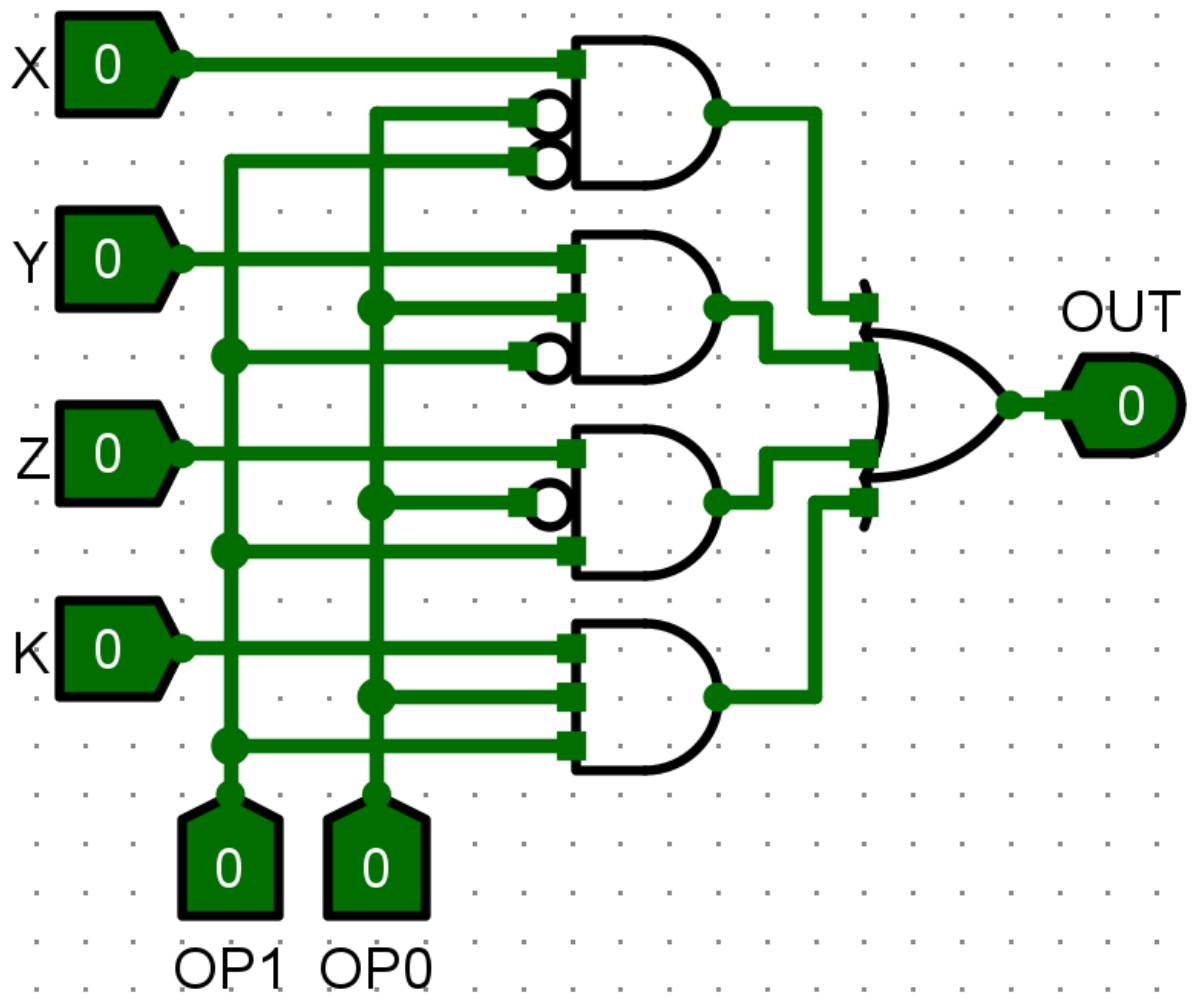


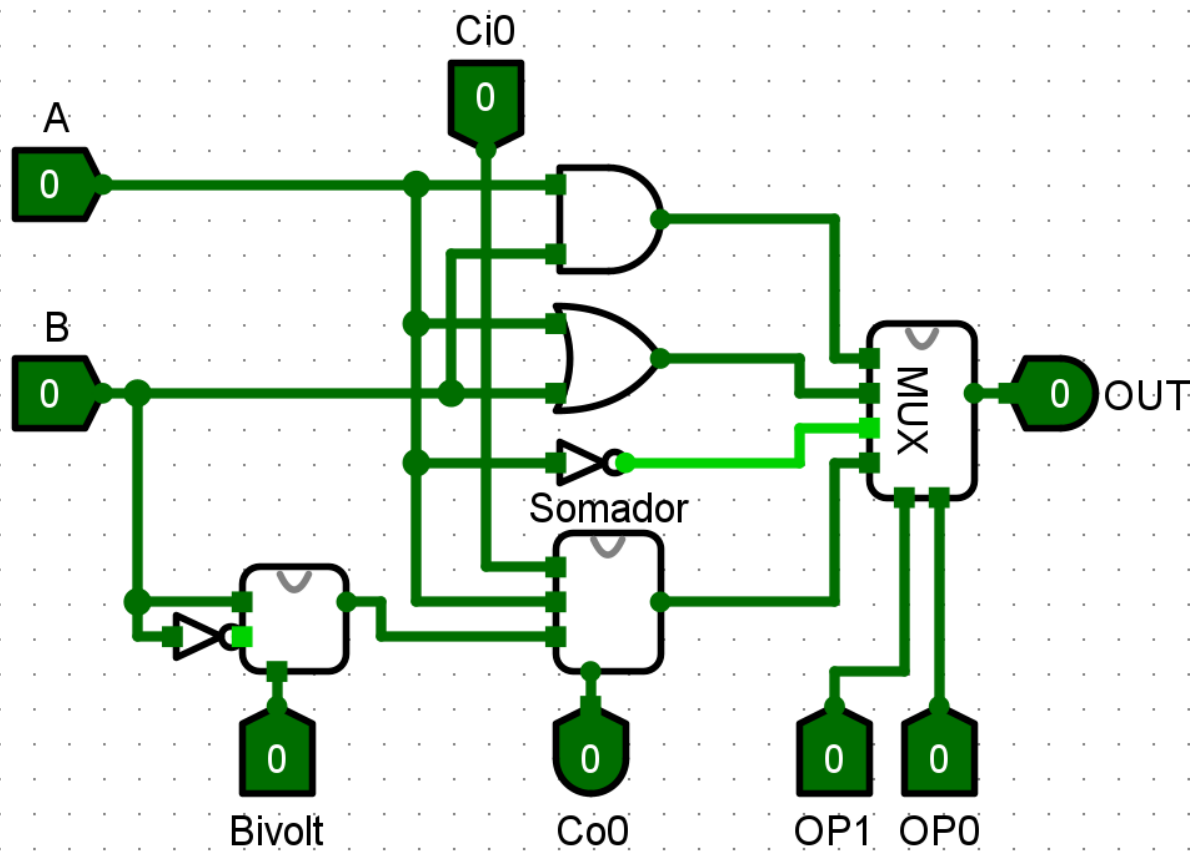
MUX 2 bits



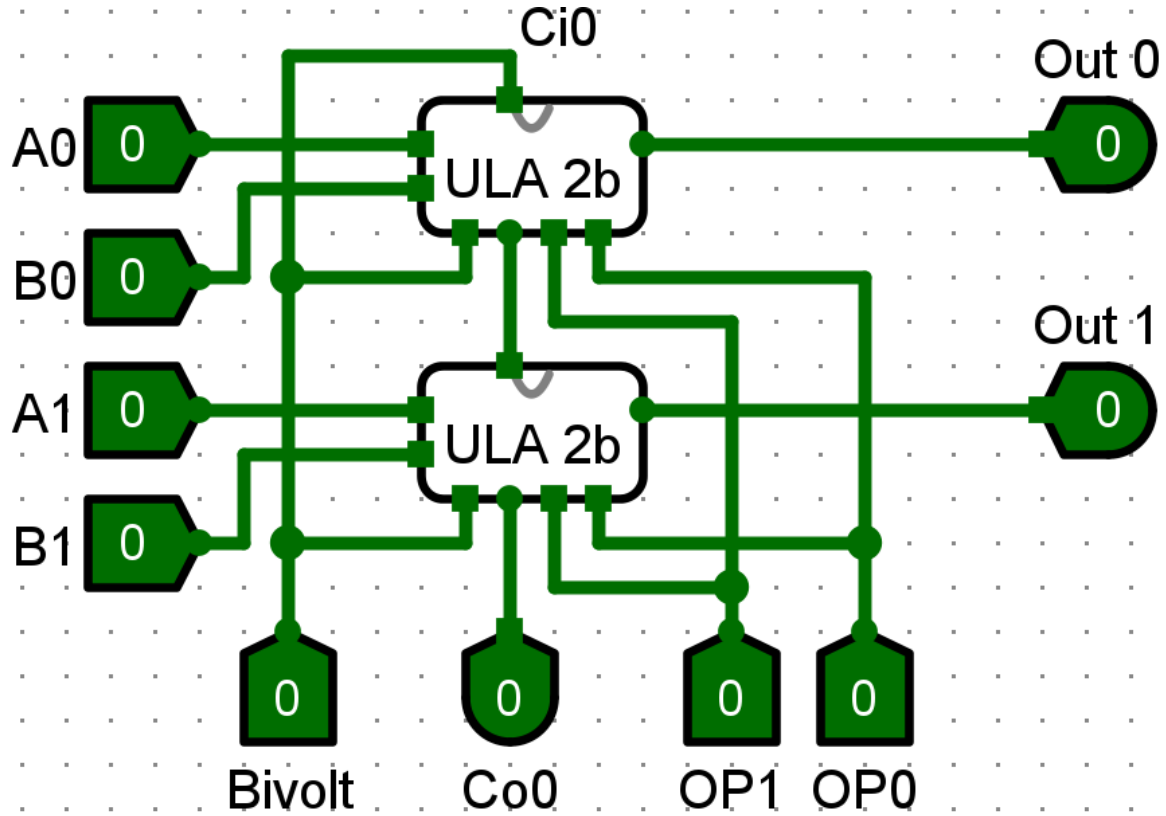
MUX 4 bits



ULA 2 bits



ULA 4 bits

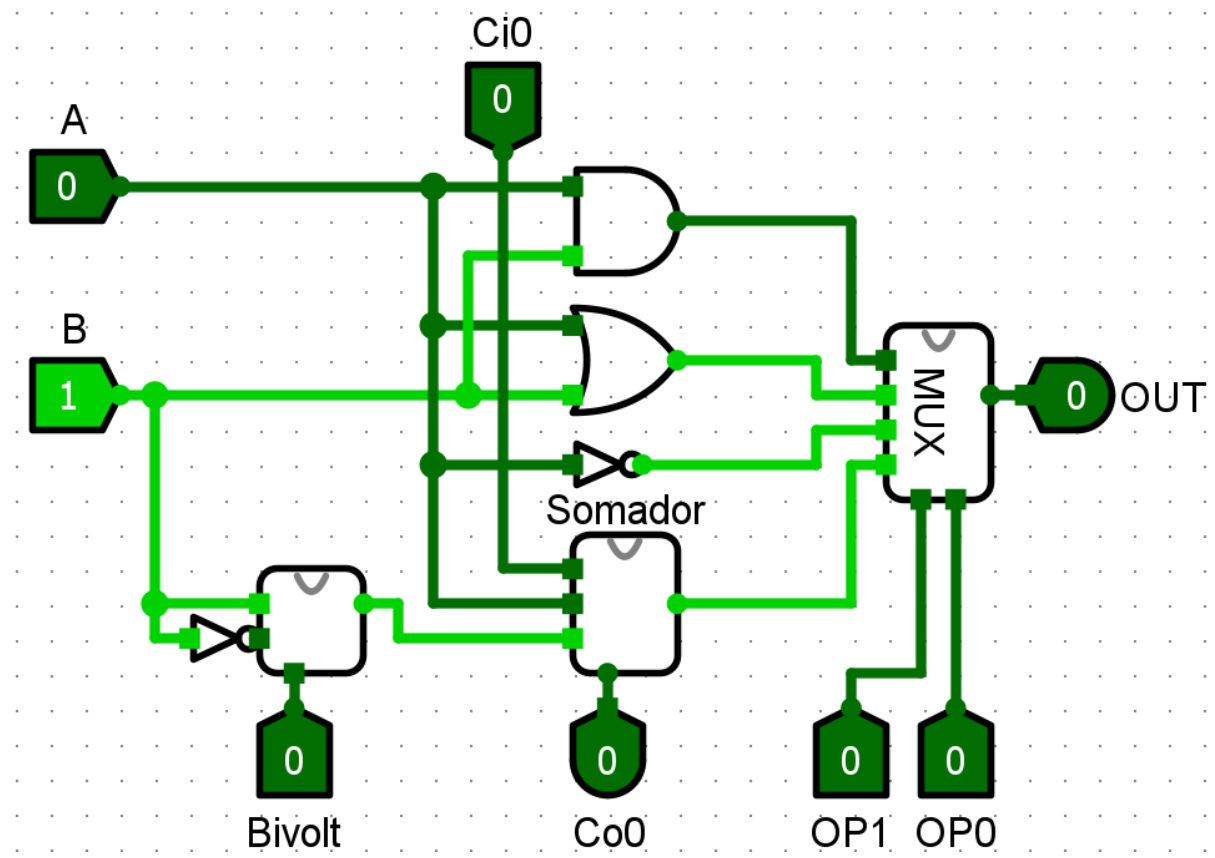


Testes logisim

A=0

B=1

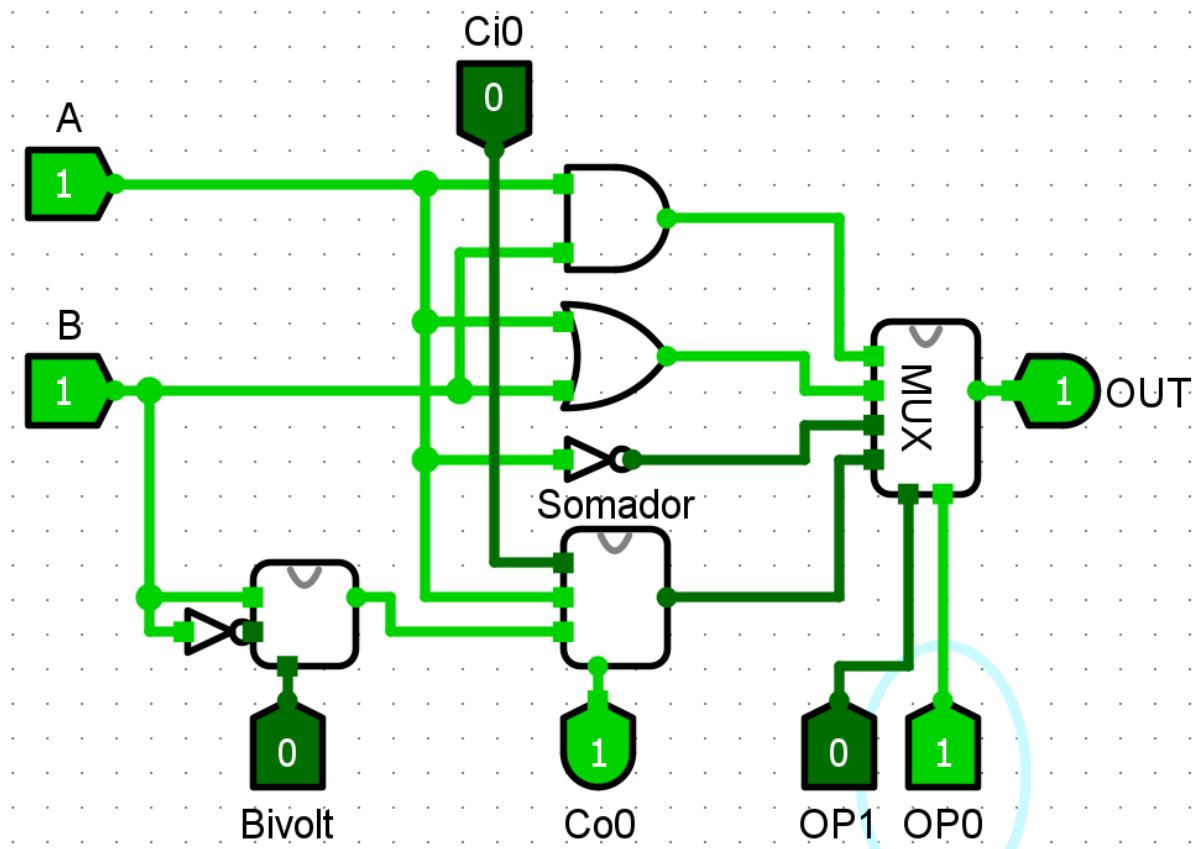
AND(A,B)



A=1

B=1

OR(A,B)

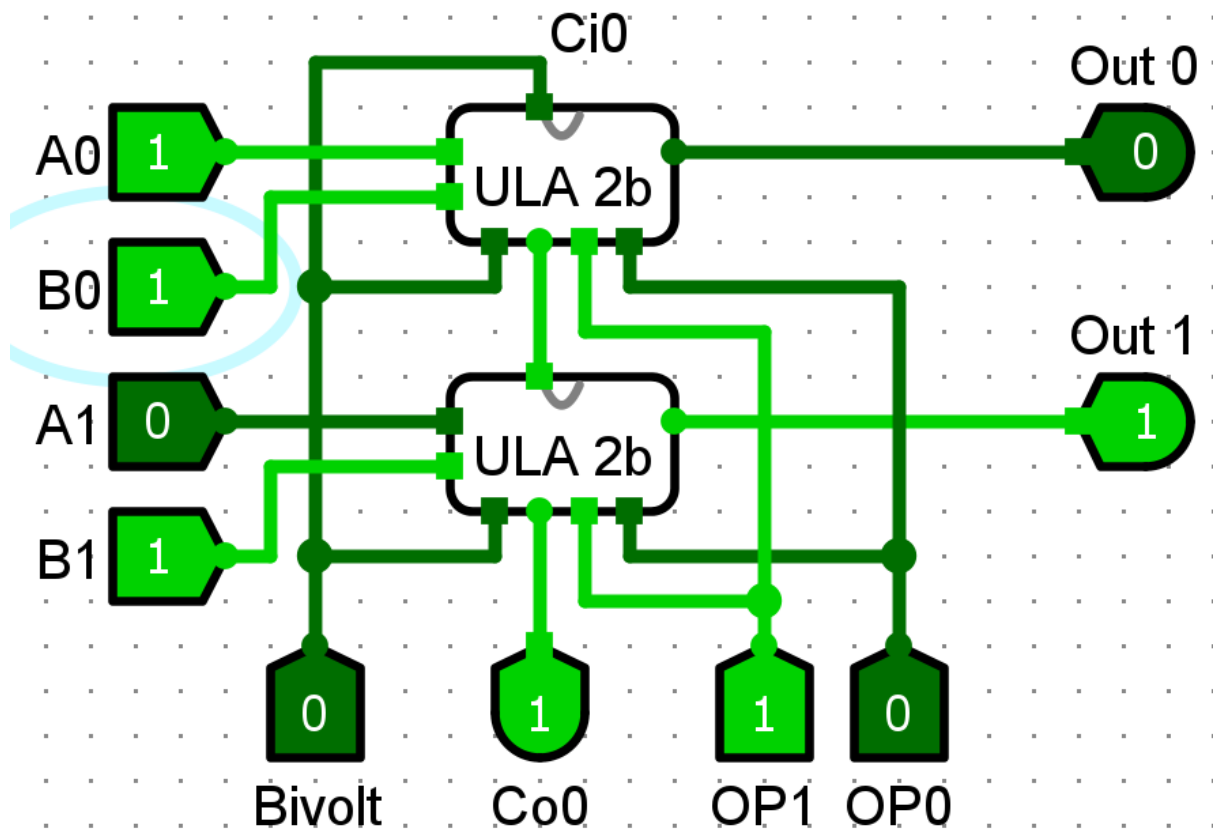


A0=1

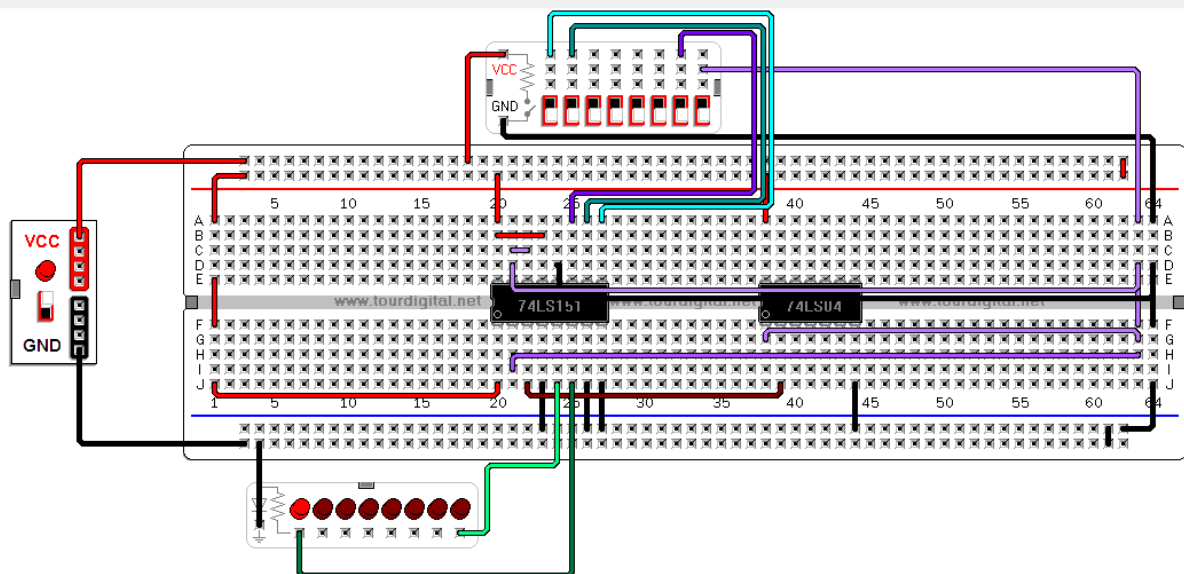
B0=1

B1=0

NOT_A(A0,A1)



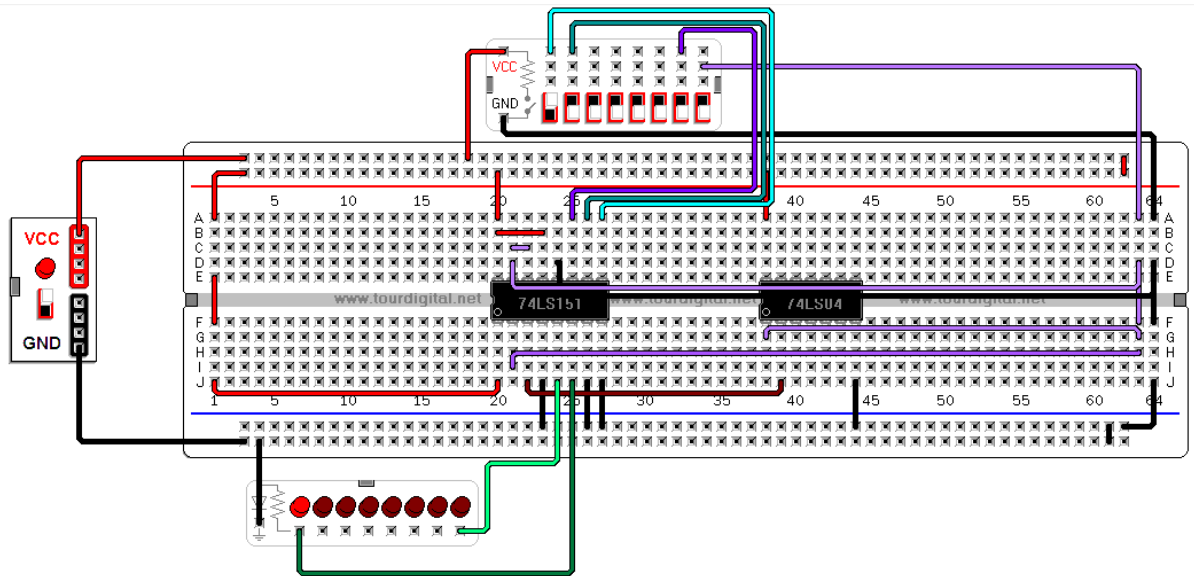
ULA 2 bits Circuit Board



A=1

B=0

AND(A,B)



A=0

NOT_A(A)

