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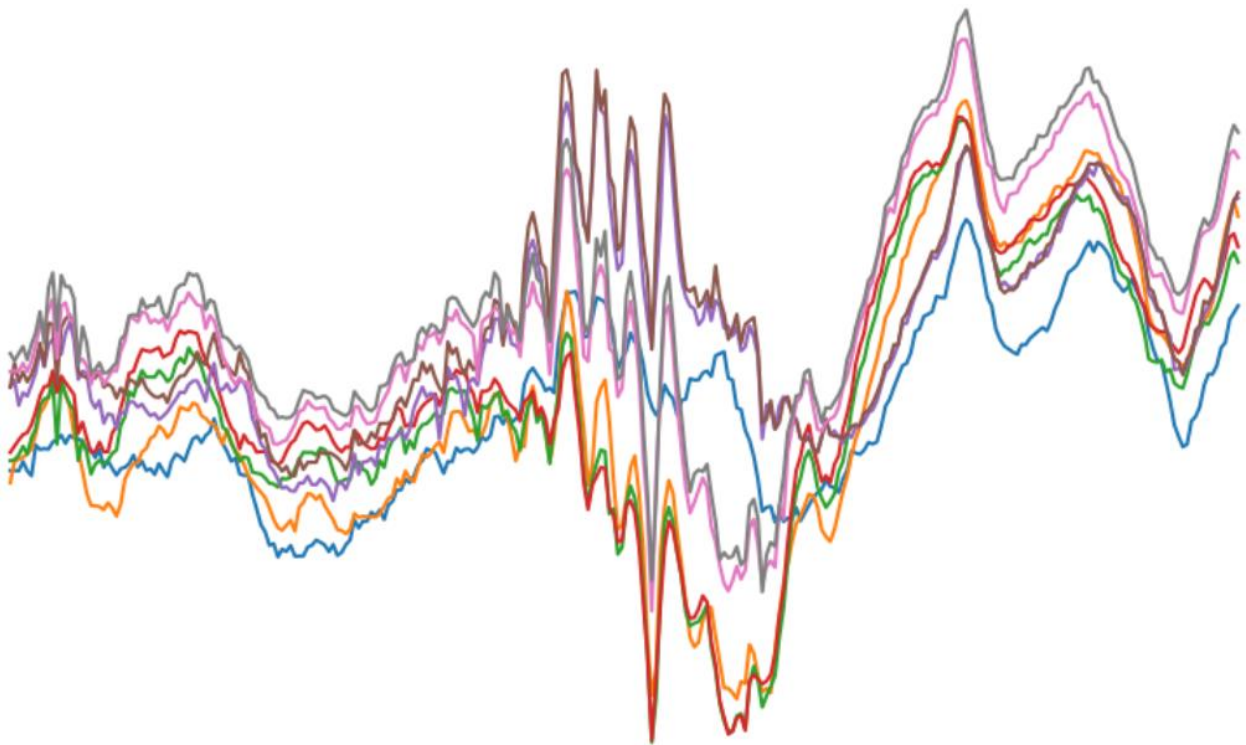**Facultad de Medicina y Ciencias de la Salud**

Trabajo de Fin de Grado

Leveraging Neuromorphic Computing for Low-Power Detection of High Frequency Oscillations in the Hippocampus

Bachelors of Bioengineering qualification

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Acknowledgements

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Abstract

With great advances...

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Introduction

Neuromorphic Computation

Neuromorphic computation (NC) could be described as a "*computing paradigm designed to mimic the structure and function of the human brain*". It involves the use of hardware and software systems that replicate the neural architecture and operational principles of biological neural networks.

The human brain is posed as the most intelligent and efficient machine. It is built by billions of neurons connected between them on an average of more than ten thousand synapses per neuron[1]. Its outstanding intelligence and efficient computation have inspired researchers to develop the working principles of AI, namely ANNs, where intelligence is achieved by small computing nodes forming a network capable to learn patterns. Following inspiration by nature, NC raised as another approach to recreate intelligence through a deeper emulation of the biological neural system [2]. At all ends, ANNs achieve intelligence as a simulation of a neural network allocating values in memory and performing operations between them, which is not very efficient and needs more energy to perform simple tasks. In contrast, human brain has physical nodes with real connections and weights which make it way more efficient than our digital technology in spatial and visual tasks (a 10 million factor approximately [3]). That said, the main advantage of NC against deep learning is that it aims to build the neural structure physically, to resemble more to brain's neural networks. That is, designing electronic neurons and synapses at a microscopic scale, which are connected as a circuit recreating a neural network. Essentially, NC aims to reinvent computing machines making them fundamentally different from current digital computers.

Strengths and weaknesses

Digital computers work with algorithms and calculations performed by arithmetic circuits[1]. Moreover, information is encoded in binary values by analogue-to-digital converters, working with zeros and ones. These values were first computed serialized (like a queue of values) controlled by timesteps set by an oscillating crystal inside the processing unit. Each timestep was able to perform a single operation. With the advances in the field, more parallel computation has been pursued and implemented. However, it supposes a hurdle and limits its capabilities. These limitations observed in the working nature of digital computation are overcomed in NC. Physical electronic nodes and synapses allow to process information in a decentralized manner, so the computation is much more parallelized. Moreover, a major part of the process happens by analogous electrical circuit operations which increase notably the speed compared with time step guided operations. Furthermore, as a consequence of parallel operation and improved efficiency in performing operations, much less energy is required to achieve the same computation (100 times less aprox. [4], [5]).

Of course, recreating the physical circuits to mimic brain neural network architecture is much more complicated than building a silico microprocessor unit. In addition, NC cannot produce so small processing units as in silico processors. Currently, transistors are in the nanometric scale whereas electronic neurons are in the range of micrometres, limiting their capability to form so complex processing systems as we see in silico computation, because they would need much more space.

Considering the abovementioned features of NC, it can be understood why this computation paradigm have not replaced yet the silico industry. Further steps of tailoring and nanoscale electronical neurons would be required for its widespread implementation in our society.

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[1] Arithmetic circuits are components in computers that perform mathematical operations, like addition or multiplication, using binary logic and electrical circuits. They're the building blocks for calculations in digital systems.

Applications of NC

Its broad potential due to parallel processing and low-latency operations make it suitable for tasks involving real-time analysis of streaming data from various sources, such as sensors, cameras , IoT devices... Its main strength could be assessed as instant decision making. This capability is often demanded in industries such as autonomous vehicles, predictive maintenance systems and healthcare devices.

In healthcare industry, there is a growing demand for low power, non-invasive quick treatments for handling biosignal analysis [6]. Current implantable devices which attempt to perform real time deep neural network (DNN) applications often end up needing cloud computing due to the required processing power. This constrains limit their applications in such sensitive fields due to risks associated with communication interferences and delays [7]. Unlikewise, hardware-based neuromorphic systems address
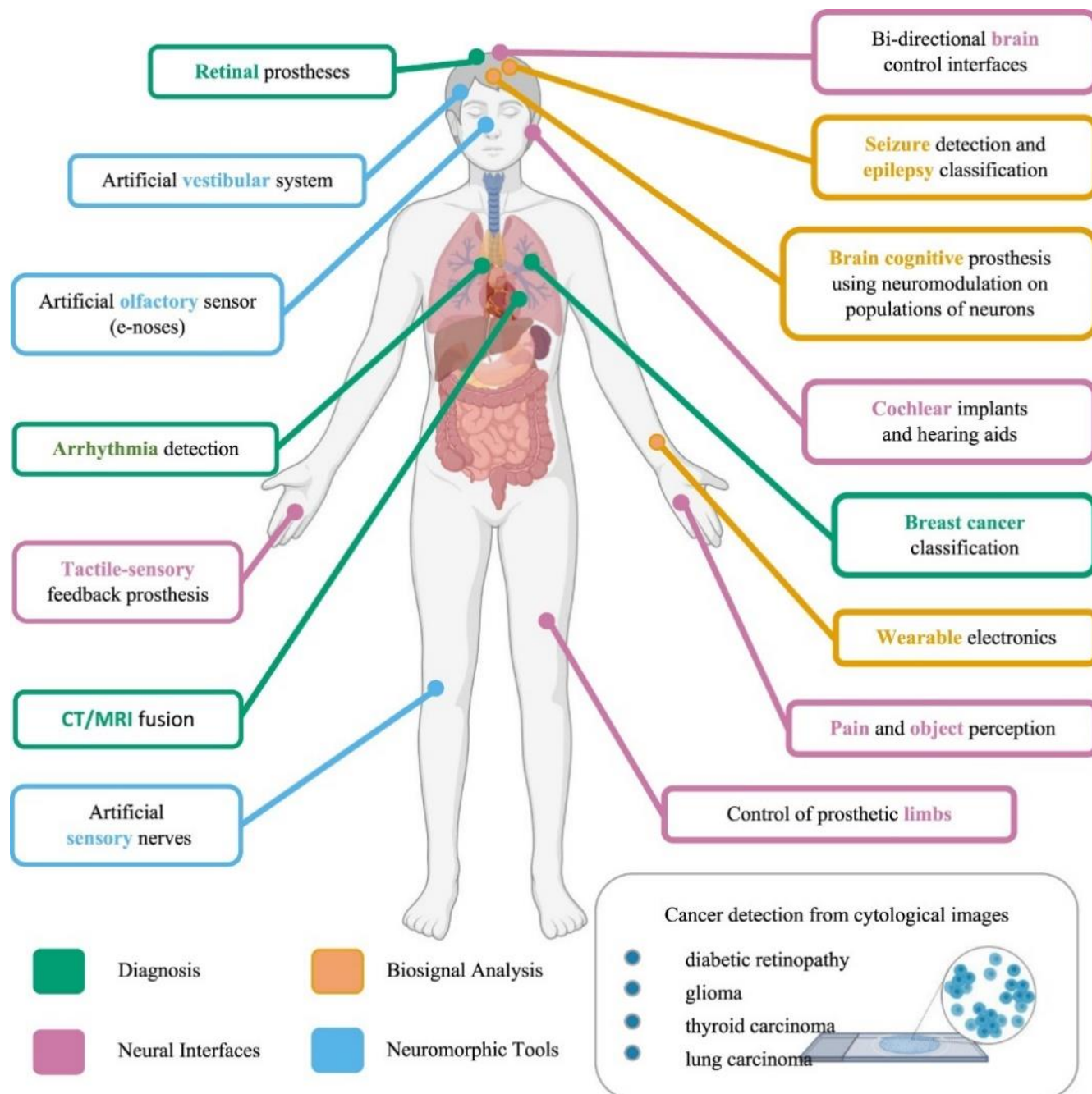

Figure1. Applications of neuromorphic devices in real-time analysis of biological signals. *Among the most relevant it can be found applications for: Deep Brain Stimulation closed-loop systems, cancer detection from biomarkers, and cardiac anomalies detectors, and imaging advances for high computation cost imaging techniques.*

these limitations by offering implantable devices capable to perform DNN computations locally in real time [7].

In signal detection applications, it is very usual to process the input signal before feeding the network for classification (for example filtering). This helps to overexpress the features wanted to be detected, which in consequence improve the accuracy of the model. A hurdle in the implementation of NC for real-time signal classification is this first process of signal filtering and preparation for setting the optimal input to the network. In digital computers is very easy because a complex code can be compiled to perform any modification to the signals.

However, in NC an equivalent electronic circuit must be developed and integrated in the system to feed a Spiking Neural Network (SNN) [2]. Fortunately, there is a wide knowledge and years of research in filtering circuits which are used by the community as preprocessing in hardware [7], [8]. These include bandpass filters and analogue-to-digital converters.

Hardware Neurons

In order to realize highly efficient neuromorphic computations that can be comparable to biological systems, bioinspired computing frameworks, involving biorealistic artificial synapses and neurons need to be developed. A circuit consisting of at least ten transistors is required to achieve the function of one biological synapse[1]. This makes the implementation of neuromorphic computation very difficult for large networks. Fortunately, another realm for electronic circuits have been tailored to mimic the electrical inputs and outputs of neurons. To achieve this, profound research in materials and physics has been done[1].

To recreate the brain environment, two electronic systems have been developed to define an electronic neuron (e-neuron). First, the cell body (e-cb), which computes the electrical inputs from the presynaptic neuron and regulates the state of its membrane potential (determining when to fire a spike[3]) and second, the neuron synapses (e-synapse), which transmit the electrical pulses generated in the e-cb. The electrical circuits defining the synapses also mimic how biological synapses retain synaptic strength[4], enabling the natural learning method behaviour of biological neurons to happen in hardware, namely Spike-Timing-Dependent Plasticity (STDP).

These two building blocks are sufficient to construct complex networks with high level of fidelity, in the way of processing information, to a biological neural network. E-cbs and e-synapses circuits can be configured together to mimic the architecture of neural networks that, up to date, could only be simulated. The level of resemblance to biological neurons obtained by the development of e-neurons, have made this computing paradigm much more energy efficient and fast than transistor-based computation[9], [10], [11]. Furthermore, previous e-neuron trials integrating many transistors suffered from high leakage power dissipation and difficulty in integration due to increased space needed.

Building the blocks

The key to overcome traditional methods mimicking neurons behaviour (transistor based) was the discovery of memristors. They were conceived in 1971 as a theoretical concept, but they were not applied to physical devices until 2008, by Hewlett-Packard (HP) lab [12]. The word memristor states for "memory" and "resistor". Basically, is a resistance with memory. So, the value of the resistance, therefore also of the conductance, depends on the last voltage applied to it. This feature makes them capable of working as non-volatile memory. In other computing frameworks, static random-access memory (SRAM) is used as non-volatile memory framework. With memristors, a resistance random-access memory (RRAM) can be achieved.

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[2] **A Spiking Neural Network (SNN)** is a type of artificial neural network that mimics the way biological neurons communicate. SNNs use discrete events called "spikes" to transmit information, making them capable of capturing temporal dynamics of neural activity, similar to the brain.

[3] **Spike** refers to an action potential, which is a rapid and temporary electrical signal that travels along the membrane of a neuron.

[4] **Synaptic strength** refers to the efficacy or efficiency of synaptic transmission between two neurons.

Working principle of memristors

Memristors are, indeed, very simple structures. They are composed of a two-terminal metal-insulator-metal (MIM) sandwich. The outer layers are conductive metals, and the inner layer is made of an insulating material. This insulating layer can change its resistance based on the history of voltage applied across the terminals, effectively "remembering" past electrical activity. When a voltage is applied, ions or charge carriers within the insulator move, altering its resistance [13]. Different insulating materials can be used to achieve memristive behaviour. TiO_2 memristors consist of a central insulating layer of titanium dioxide (TiO_2). The TiO_2 layer contains regions with oxygen vacancies, which can migrate under an electric field, altering the material's resistance. When a positive voltage is applied, these vacancies form conductive pathways, switching the device to a low resistance state (LRS). Reversing the voltage causes the vacancies to disperse, returning the device to a high resistance state (HRS) [12].

This ability to change and maintain the resistance in response to the voltage applied is very similar of how synapses work. Synaptic plasticity is the term used for actualizing the "synapse weights" within neurons in response to electrical activity. This behaviour is key for learning, because the output of a network can vary a lot when weights are changed. Modifying weights can imply that a post-synaptic neuron that was firing in response to a stimulus, do not fire anymore, because the inputs are no longer enough to achieve the membrane threshold. Therefore, introducing these components to the electrical circuits defining neurons has supposed a revolution in hardware based SNNs.

Before diving into the electrical circuits forming e-cbs and e-synapses, let's see the neuron models proposed in the literature.
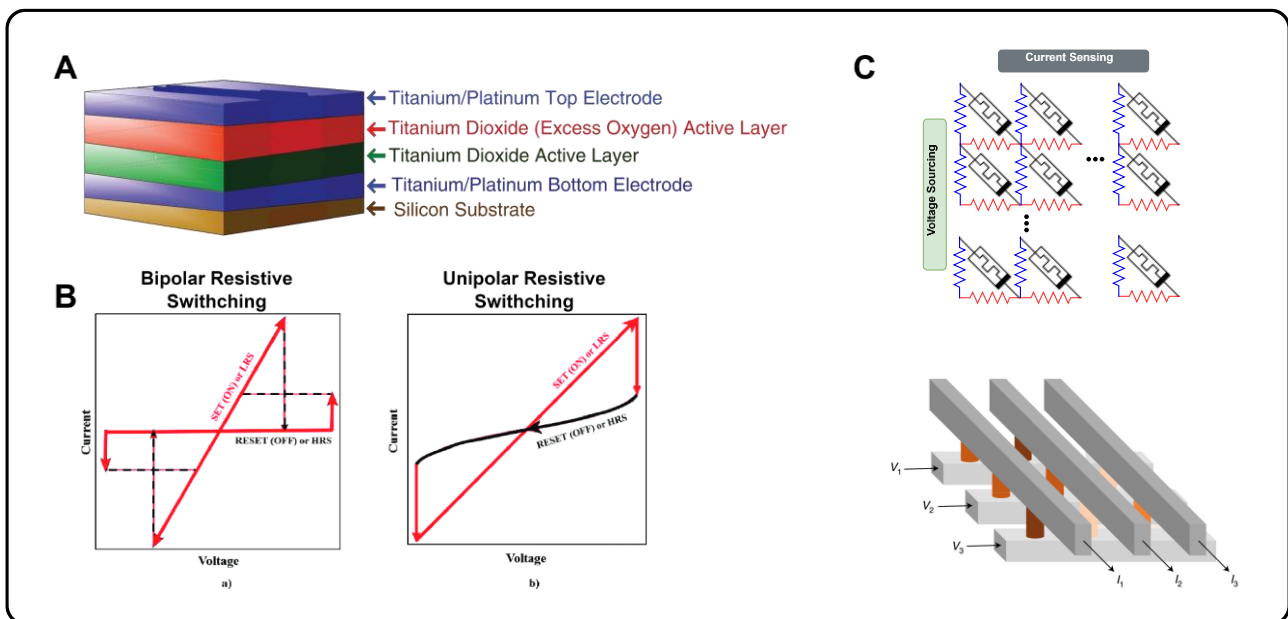
Figure 2. Memristors Overview. How memristors materials are distributed and their behaviours to electrical stimulus.

A) *Spatial distribution of memristors layers. The choice of silicon substrate can impact factors such as thermal conductivity, mechanical strength, and integration with other semiconductor technologies, top and bottom electrodes establish electrical contact with the memristor and enable flow of current. Finally, Dioxide layers undergo reversible changes in its resistance in response to applied electric fields or currents (Image recreated from [94])*

B) *Current-Voltage characteristic curves of memristors, showcasing their hysteresis behaviour.* **Image "a"** *shows a bipolar state transition from high resistant to low. When the voltage is reduced back to zero and further int the negative range, the memristor remains in the LRS until the negative voltage exceeds a certain threshold, causing it to switch back to HRS (marked as "RESET (OFF)"). This creates a hysteresis loop, indicating the memory effect of the device.* **Image "b",** *unlike the bipolar case, the reset process (switching back to HRS) happens at a higher voltage but of the same polarity as the set process. The device remains in the LRS until a higher voltage is applied, causing it to reset to HRS.*

C) Memristive cross-bar arrays in SNN and Neuromorphic Computing. *The convolution product needed in convolutional neural networks in image classification tasks is the most energy demanding process and it can be naturally performed in a cross-bar array on the physical level based on Ohm and Kirchoff laws [13], [95].*

Neuron models

Neurons are the main variable of SNNs. How the nodes of the network will communicate between them provide notable insights of the network behaviour. In the literature there are many neuron models reported and some of them have been widely used for different applications [14], [15].

Leaky Integrate-and-Fire (LIF)

Popular due its simplicity and efficiency. It captures the essence of neuronal behaviour by integrating incoming signals (synaptic inputs) over time. When the accumulated membrane potential reaches a specific threshold, the neuron fires a spike, and the potential resets, beginning the integration process. This model, with its straightforward threshold mechanism and leaky integration, has been foundational in many SNN applications, offering a balance between biological realism and computational manageability [15], [16].

Hodgkin-Huxley

Developed through Nobel Prize-winning research, this model describes how action potentials in neurons are initiated and propagated, based on detailed ionic currents through the neuronal membrane. The Hodgkin-Huxley equations capture the dynamics of voltage-gated ion channels, offering an unparalleled level of detail. Although computationally intensive, this model is useful if electrochemical mechanisms underlying neural activity need to be considered [1], [15].

Izhikevich

Aiming to bridge the gap between the simplicity of the LIF model and the complexity of the Hodgkin-Huxley model, the Izhikevich model was introduced. This model combines biological plausibility with computational efficiency, capable of replicating a wide variety of neuronal firing patterns observed in real neurons. By using fewer computational resources, the Izhikevich model captures complex spiking behaviors, making it a versatile tool for simulating large-scale neural networks without compromising on realism [15].

Adaptative Exponential Integrate-and-Fire (AdEx)

AdEx enhances the LIF framework by incorporating adaptation mechanisms. This model can replicate the adaptative behaviour of real neurons, such as frequency adaptation and spike-frequency adaptation. The AdEx model's ability to adjust its response based on prior activity provides a more refined simulation of neuronal behaviour, being able to capture the dynamic nature of neuronal adaptation observed in biological systems [15].

Electronic Circuitry of e-neurons

E-neurons, as the basic building blocks of SNNs, must be the smaller the better (to build complex networks in small devices), non-heating (to not damage cells if implanted in the body) and easy to produce (to minimize the costs). Of course, the circuit must behave similar to neurons. In the context of the LIF neuron model, as
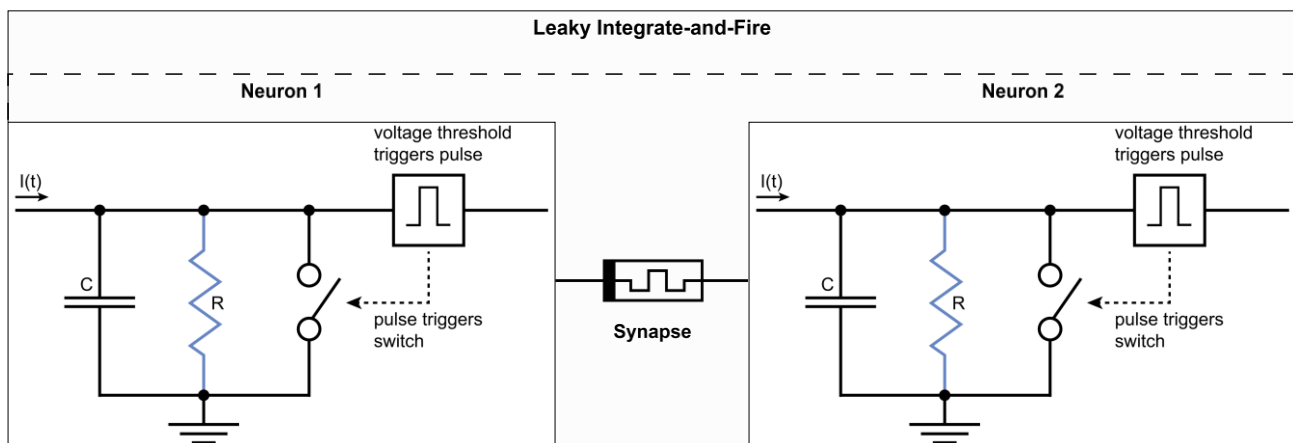
Figure 3. Two LIF neurons connected with a memristive synapse. *The capacitor accumulates voltage until it reaches a threshold, where the trigger makes the switch connect and send the current (spike) out of the circuit. The memristor connects the neurons and adjusts its resistance in response to history of voltage applied. The natural behaviour of the capacitor leaks voltage at a certain rate making the neuron "leaky". (LIF circuit reconstructed from [16])*

its name suggests (leaky integrate-and-fire), accumulates voltage from inputs, while it is leaking the membrane voltage over time at a certain rate. This makes that the neuron only reaches its firing threshold when inputs arrive within a certain interval of time. LIF models, traditionally, perform the leakage pattern with the capacitor [17]. However, with the arrival of memristors, the use of capacitors will probably get obsolete.

Memristors inherently exhibit both integration and leakage behaviours similar to biological neurons. They can integrate incoming signals by changing their resistance state in response to the input voltage and then gradually return to their baseline state, mimicking the natural decay of graded potentials in neurons [18]. This leaky behaviour is naturally achieved in memristors without additional circuitry. In terms of density and integration, memristors can be fabricated at a very small scale and integrated into high-density crossbar arrays, allowing for the creation of compact, large-scale neuromorphic systems. This high integration density is harder to achieve with capacitors, which generally require more space and additional components to emulate complex neuronal dynamics. Consequently, memristors offer a more efficient and scalable solution for advanced neuromorphic circuit design.
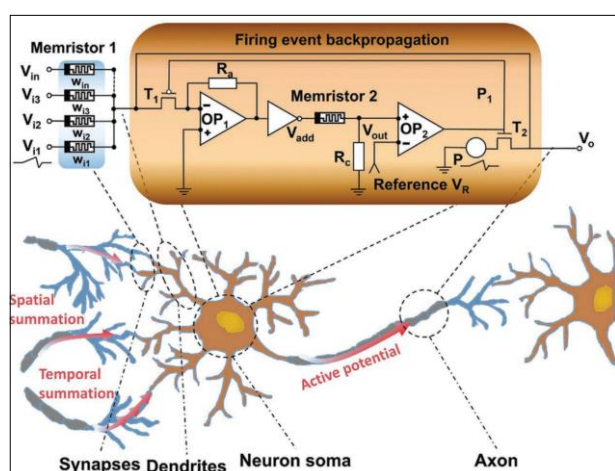

Figure 4. Newer LIF models using memristors for membrane voltage leakage. *Memristor 1 are synapses with other neurons. The circuit enables for backpropagation learning. Memristor 2 is the component regulating the accumulation and leakage of voltage. (Reproduced from* [1])

Spiking Neural Networks

Neural communication is well known to be mediated by spikes. The neuroscientific research

community have widely debated whether if neural communication primary relies on the shape of spikes or the spike rate. Initially, some theories suggested that the precise shape of an action potential might carry important information. However, contemporary research tends to emphasize the importance of the spike rate over the plain shape of individual spikes for neural encoding [19].

Rate-based coding theories propose that the frequency of spikes over a period is the primary way neurons encode and transmit information. This approach is supported by evidence showing that many neural functions, such as sensory processing and motor control, correlate strongly with changes in spike rate [19], [20]. In these theories, the spike rate reflects the amount of information transmitted and is crucial for understanding neural activity.

Additional theories highlight the significance of the precise timing and patterns of spikes, arguing that the exact timing of spikes, in relation to each other, can carry additional layers of information that might be missed if only the rate was considered [20], [21]. One notable example is the "chronotron (Florian R et al 2012)", which is a model of neurons that learn to fire at specific times [22]. This model demonstrates how precise timing of spikes can encode information. Overall, the consensus in modern neuroscience is that both spike rate and timing play important roles in neural communication. In contrast to older theories, shape of the spike seems to be irrelevant in the task.

Discovering that biological neurons process information with spike timing independently of the information of the spike led to the idea of event-driven computation. Spikes are the events and neurons act in response to them. When a stimulus is provided to the biological network through sensors (touch, vision, noise...), the neurons propagate the stimuli through the corresponding areas of the brain in the form of events (spikes). The rate and time patterns of events encode the information, ultimately leading to an output. This process can involve more or less neuronal complexes. Similarly, artificial SNNs have been developed to process information in

an event domain. An architecture of spiking neurons can be engineered to mimic the processing task that biological neurons would do in the brain. To achieve such task, neuron models have been proposed and tailored to behave as neurons (*see "Hardware Neurons" section)*.

SNN Learning Mechanisms

The training process of SNNs is still at the stage of development because of its novelty and lack of deployment in our society. There are several methods which enable the network to learn patterns, but there is a lot of improvement to be done. ANNs are able to achieve better performances than SNNs in many workloads [23].

Learning in neural networks implies changing the weight connections between nodes. In ANNs these nodes are responsible to modify the input signal throughout the layers to enhance "invisible" pattern features so that finally a simple operation can determine the correct label of the input. These modifications rely on "values as inputs". For example, an image as the input of a convolutional network, will experiment kernel operations on its pixel values that will change the image itself through the layers. However, in SNN, the inputs are not values, but spikes. Therefore, the network must be able to modify the strength of neuron synapses to learn time domain patterns within input rates or timings instead of input values[24], [25]. The network will have a response for each sample. That is, in the context of detecting an event, composed of 100 samples, each sample will modify the membrane potentials of neurons and the network will produce spikes (or not) at the output layer. The spikes of the event will therefore cause that the network fire at higher or lower frequency. Thus, setting a rate threshold for detection can be done [20], [26]. As it is showcased, the rationale behind learning is notably different within ANNs and SNNs.

Mechanisms to achieve a trained SNN, with correct weights set within their layers, have been engineered in the past decades.

Spike-Timing-Dependent Plasticity (STDP)

This unsupervised learning rule adjusts the synaptic weights based on the precise timing of spikes from pre- and post-synaptic neurons. If a presynaptic spike precedes a postsynaptic spike within a certain time window, the synaptic strength is increased (long-term potentiation, LTP). If the presynaptic spike follows the postsynaptic spike, the synaptic strength is decreased (long-term depression, LTD) [24], [26].

Surrogate Gradient (SG) Methods

Due to the non-differentiability of spike events, traditional gradient-based optimization is challenging for SNNs. Surrogate gradient methods approximate the gradient of the spike function, enabling the use of backpropagation [5]. This approach has been effective in training deep SNNs directly and handling temporal data efficiency [27].

ANN-to-SNN Conversion

Another approach involves converting a pretrained ANN into an SNN. This is done by replacing the activation functions of the ANN with spiking neuron models. While this method can quickly yield SNNs, it often relies on rate coding and might not fully exploit the temporal dynamics of SNNs [27], [28].

The training framework sounds quite straightforward until now. In fact, it is just required the code to recreate the mentioned behaviour, which we, as humans, are perfectly capable of. The problem comes when switching from software to hardware. That is, trespass the equivalent network to a physical network of electrical neurons.

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[5] **Backpropagation**, short for "backward propagation of errors", is an algorithm for supervised learning of artificial neural networks using gradient descent. Given an artificial neural network and an error function, the method calculates the gradient of the error function with respect to the neural network's weights.

Neuromorphic Frameworks

Working with such complex systems require a well-organized framework. Through the last decades several neuromorphic environments have been developed to carry out neuromorphic computation [28], [29].

A clear leader in Spiking Neural networks for neuromorphic computing is SpykeTorch. SpykeTorch is an extension of PyTorch oriented to build SNNs. Many further emerged frameworks are supported on SpykeTorch and focused more in deploying the network to hardware [29]. Within all the frameworks, LAVA by intel provide a versatile workspace to build custom SNNs with fully integrated neuron models. Furthermore, it is very fined tuned to compile the models and deploy them to their chip (loihi2) and other neuromorphic chips.

Lava Neuromorphic Computing

Lava[6] is an open-source software framework developed by Intel. It is designed for developing neuro-inspired applications and mapping them to neuromorphic hardware. Lava provides developers with tools to harness the principles of neural computation, enabling neuromorphic platforms to process, learn, and respond to real-world data efficiently and quickly compared to other computer architectures.

Lava has a versatile structure, allowing researchers to integrate a wide range of algorithms and build complex neuromorphic applications. It supports the definition of processes, such as individual neurons, neural networks, and interfaces to external devices, which can be encapsulated into modules and aggregated to form sophisticated systems. Communication between processes in Lava uses event-based message passing, facilitating the simulation of neural interactions.

Moreover, Lava allows applications to be run on conventional CPUs/GPUs and deployed to various neuromorphic chips, such as Intel's Loihi. This flexibility is enhanced by a low-level interface called Magma, which aids in compiling and executing processes across different backends. Lava aims to unite the neuromorphic computing community, providing a common framework to facilitate collaboration and innovation in the field.
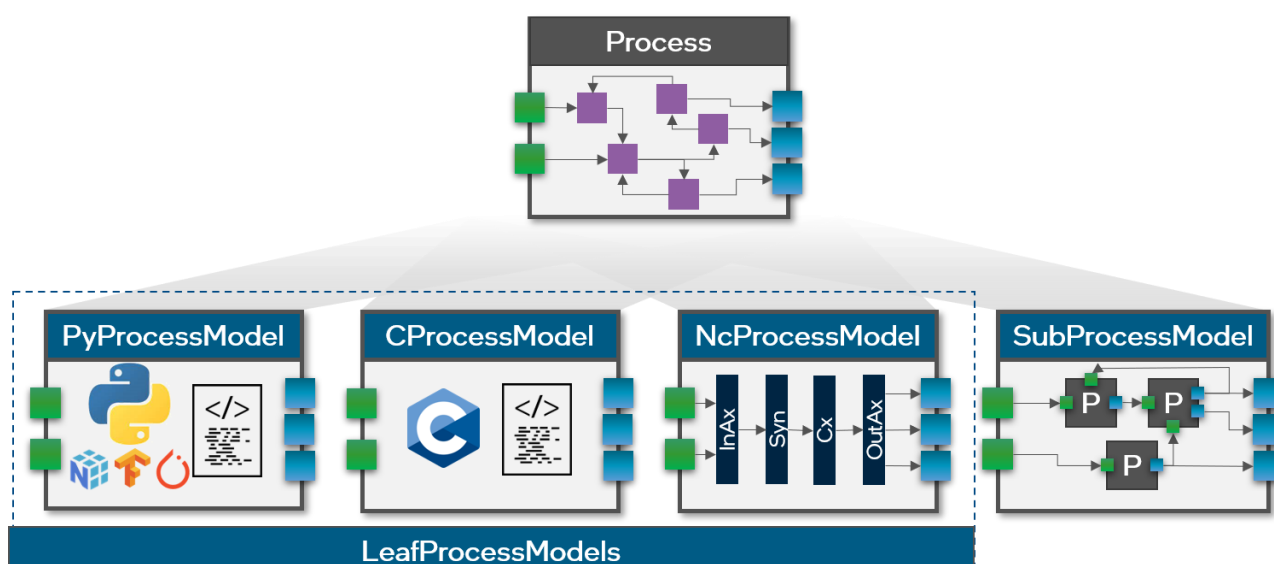

Figure 5. Versatility of Lava Neuromorphic Framework. *Lava supports the creation of custom processes which can connect between them setting "IN" and "OUT" ports. (green and blue) These processes are compiled and work by event driven computation. The running of these processes can be on neuromorphic hardware or in a simulated neuromorphic environment* [5].

Lava Workflow

Lava framework is divided in two branches:

Lava-nc is the main branch and it is used to compile and run lava processes, as well as migrate them to physical neuromorphic chips.

Lava-dl is an independent module used to create neuron models and networks. It also provides SNN training as well as functions to convert network to lava process, which can be compiled and mapped to neuromorphic hardware.

Slayer, standing for Spike Layer Error Reassignment in Time, is the tool used by lava-dl for training SNNs. It enhances a more effective learning by improving the network's ability to recognize patterns and make decisions, mimicking the way the brain processes information. By implementing a temporal credit assignment policy, SLAYER enables errors to be backpropagated through the network layers [5], addressing the challenge of non-differentiability in spike generation [7]. This allows for the adjustment of synaptic weights and axonal delays, ultimately leading to better performance in tasks like pattern recognition and decision-making, similar to biological neural networks. Slayer built-in tool from lava is built in top of SlayerPytorch, which is the slayer developed by PyTorch.

Loihi2

Loihi 2, Intel's second-generation neuromorphic chip, offers several significant improvements over its predecessor, Loihi. Key advancements include up to 10 times faster processing and up to 15 times greater resource density, which allows for the integration of up to 1 million neurons per chip[5]. This enhanced performance is achieved through Intel's latest process technology and asynchronous design methods, including the use of extreme ultraviolet (EUV) lithography. Loihi 2 also supports more complex, programmable neuron models and graded spike events, which improve the efficiency and capabilities of neuromorphic applications. Additionally, Loihi 2 provides better support for new learning algorithms, such as variations of

backpropagation, and more flexible input/output interfaces, which facilitate its use in a wider range of real-world applications [5], [30].

Loihi2 is fully supported by Lava framework, enabling seamless mapping of processes. Furthermore, intel labs have developed an api access to test neuromorphic applications in physical loihi2 chips through the cloud.

Neuronal Electrode Systems

To effectively interface with the brain and record neural activity, a good and reliable recording system is required. It is as important to have good quality signals as to being able to detect them. Since we know that the brain has electrical activity happening within its tissue, many trials have been done aiming to decipher their meaning. Currently, many recording systems are enabling neuroscientific community to learn many insights from neuronal signals [31], thus boosting the appearance of new technologies and advances. Electrodes implanted in the brain have created a renaissance in the study of normal and pathological brain function. These devices are being developed to treat a growing number of medical conditions, including Parkinson's disease, paralysis, Alzheimer's disease and depression [32]. These systems range from non-invasive to invasive techniques, each offering different levels of spatial resolution and specificity.

Novel technologies

Common and traditional methods such as electroencephalography (EEG) and magnetoencephalography (MEG) are widely used for their ability to monitor brain activity without the need for surgical intervention. EEG measures electrical activity through electrodes placed on the scalp, while MEG detects magnetic fields produced by neuronal activity. Both methods provide interesting insights into brain function but are limited by their relatively low spatial resolution.

Other newer more invasive techniques include Electrocorticography (ECoG), which involves placing electrodes on the surface of the

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[7] The challenge of **non-differentiability in spike generation** refers to the difficulty in calculating the precise changes needed to

improve the network's performance because the spiking function is not smooth and doesn't have a straightforward derivative.

brain, typically during neurosurgery. This approach offers better spatial resolution than EEG and MEG, allowing for more precise localization of neural activity. Some works have relied on this technique to assess epileptogenic tissue areas during brain interventions [8]. The more precise recorded signals provide with a higher signal-to-noise ration and the detection of biomarkers results to be more accurate. In epileptic patients, high frequency oscillations originating from epileptogenic tissue can be detected by filtering the signal in its frequency band and training a simple network to detect them[33].

More invasive methods involve implanting electrodes directly into brain tissue, providing the highest spatial resolution and the ability to record from individual neurons or small groups of neurons. These include:

Single-Unit Recording uses fine microelectrodes to record the activity of single neurons. It offers unparalleled detail but can only sample a limited number of neurons at a time. This type of electrodes can be found in patch clamp method, a powerful technique widely used in electrophysiology to study the ionic currents flowing through individual ion channels on the membranes of neurons.

Multi-Electrode Arrays (MEAs) consist of multiple electrodes arranged in a grid, allowing simultaneous recording from many neurons. This approach is valuable for studying neural networks and their dynamics [34]. MEAs can be valuable to study 2D distributed cell populations. Being able to place electrodes uniformly as a "matrix" through the cellular space can assess signals flowing through the network and population oscillations. Nevertheless, they may not be suitable for deep tissue recordings due to their limited capabilities of being embedded in a 3D cell complex [35]. MEAs are primarily applied in vitro. Where neurons are delivered in top of the electrodes to grow in top of them. Recordings are directly in contact with neuron membranes, leading to much less interferences and pure raw

signals [34]. Although, they have also been used in vivo [34], [36].

Shank [8] Invasive Electrodes, such as silicon probes, are a specific type of invasive electrode designed to penetrate brain tissue and record from multiple sites along their length (**see Figure 6**). These electrodes can sample from different layers of the brain, providing detailed information about neural activity in various depths. Shank electrodes have been instrumental in advancing our understanding of complex neural circuits and their role in behaviour and cognition. Their appearance was driven by the need of recording deep areas of the brain with high resolution [37].

In applications where the goal is to study deep brain complexes, invasive technologies show better outcomes compared to superficial ones [32]. In neurodegenerative diseases (NDs) where structures such as the hippocampus and temporal lobe are object of study, shank electrodes, despite their invasive damage, are worth the resolution they provide at the local site [38].

Key advances in recent systems

In top of improvements in recording methodologies, advances in electrode materials and physics and logics have also been done. Softer, less invasive, and more broadly distributed approaches outperform older technologies [37], [39], [40]. It has been well reported that traditional metal and silicon probes often cause an undesirable immune response, being reflected in local neural loss and glial reactivity [41], [42]. Improved electrodes influenced by underlying concerns reveal the well known "Michigan" style arrays. These devices consist mainly on silicon with iridium or platinum recording sites embedded in the silicon shank[43], [44]. Their main advantage is the possible different configurations easily achieved with them [32].

Another key point in neural recording is the electronical advancements. These made possible to overcome a limitation which was the low capability of multiplexing [9]. Previously, this low

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[8] A **shank** is typically a slender, elongated structure that houses multiple electrode sites along its length, allowing for the recording of electrical activity from different depths within the brain or other neural tissues.

[9] **Multiplexing** refers to combining multiple signals from various electrodes into a single data stream for transmission or processing.

capability limited the number of simultaneous recordings that could be obtained from neural electrode systems [31]. With modern electronic advancements, it is now possible to combine and transmit multiple signals from different electrodes through a single channel [31]. This has allowed researchers to record neural activity from many more sites simultaneously, greatly enhancing the ability to study complex neural networks and brain functions in greater detail [37], [40], [45].

Prospects for learning and memory in NDs

Recording oscillatory behaviours, such as ripples in the hippocampus, in local parts of the brain is a thoroughly studied realm and has let to the understanding of cognitive processes (see *"Memory Consolidation section")*. However, there are still many gaps and unclear processes that limit the development of more efficient treatments for NDs. NeuroGrid electrodes were able to determine anatomical locations of ripple-generating cortical areas [46]. Furthermore, Neuropixel electrodes, a kind of deep implanted shank electrodes, achieved to record simultaneous activity of multiple cortical areas (with the outstanding number of > 2000 neurons per site) and deeper brain structures[47], [48]. Further investigation works to deeper understand the transfer of memory traces and consolidation of memory could be achieved by combining these two recording systems simultaneously, because information of subcortical and cortical areas would be obtained at the same time, thus being able to relate patterns within deeper and surface observing points [45].

Current stage of research in NDs

At the current stage of scientific research, Alzheimer's Disease (AD), the most common form of ND, representing the 60% to 80% of all cases [49], has no efficient treatment. This is mainly due to the late manifestation of its symptoms. When they first start appearing, many parts of the brain are already damaged without the possibility to recover [50]. Similarly, Temporal Lobe Epilepsy (TLE) show the first symptoms years after the onset of the disease.

NDs are becoming a major health and economic issue due to the aging and lifestyle of the society. Currently, over 50 million people worldwide suffer from a ND. This number is expected to triple in 2050 if no effective preventive measures are found[49].

Throughout the years, the research community has tried to approach and counteract the downsides of NDs by many different techniques, leading to greatest advances and improvements in the lifestyle of patients.

Drug Treatments

Current treatments for AD are symptomatic-based rather than curative, trying to limit the progression of cognitive, behavioural and psychological symptoms of dementia. There are two main families of drugs approved on the market: Anticholinesterase inhibitors and anti-glutaminergics. Both are drugs that when enter the central nervous system (CNS) provoke a desired effect. Anticholinesterase inhibitors are molecules designed to increase acetylcholine levels in the brain and antiglutaminergics regulate the high levels of glutamate observed in persons with AD, which impair learning and memorization [51].

Although these treatments are not curative, they improve the quality of life of the patient by enabling them to maintain independence. Unfortunately, these drugs have a modest effect compared to their potential because of the difficulty in reaching the CNS. The drug present in the bloodstream targeting neurons must trespass many hurdles. The endothelial cells in the walls of the vessels of the CNS are joined tighter together, sealing possible gaps where drugs may go through. Furthermore, their efflux transporters that pump out undesired molecules also minimizes the effect of the drug.
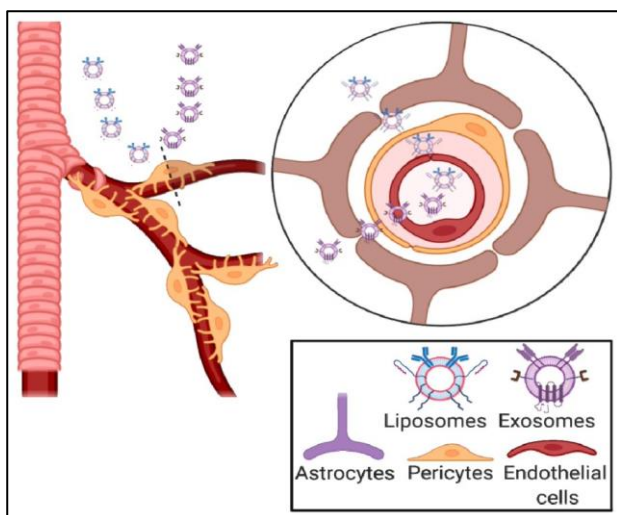
Figure 6. *Schematic representation of BBB complex interaction with inorganic and polymeric nanoparticles* [51]. *Liposomes or exosomes must cross endothelial cells, as well as pericytes to finally reach the CNS.*

To overcome all these obstacles from the blood to the CNS, known as blood brain barrier (BBB), researchers have developed encapsulation methods such as lipids and exosomes [52], [53]. Moreover, other delivery routes were explored to overcome traditional ones. Intranasal delivery (IN) administration provides an alternative to intravenous administration [54]. It is non-invasive, painless, and easy to administer without a medical specialist. Furthermore, the IN route bypasses the BBB, enhancing drug bioavailability by avoiding first-pass metabolism and intestinal degradation[55].

With all the proposed techniques significant advances with improving results were obtained. However, any of them was able to revert the progression of the disease nor neutralize its primary cause. Furthermore, they were not able to restore the damage. As a result, dealing with NDs implied long-term treatments without foreseeable total cure.

Drug therapies are thought to be more effective in the early asymptomatic stage before the process of neurodegeneration occurs [51]. *Cummings et al. (2018)* claim the need for better diagnosis in the early stages of AD using additional biomarkers to improve their prospects [56].

Electrical Treatments

Since achieving fair effectiveness of drug therapy in the CNS has been a major challenge, other therapy methods have been explored. Patients with Parkinson (PD), AD and TLE have shown good response to determined electrical stimulus in specific regions of the brain [57].

There are several types of electrical stimuli applied to brain tissue [58]. Their main goal is to induce some controlled neuronal activity that will improve the condition of the disease. It was first applied in the mid 20s. Olds J. et al (1954) observed positive reinforcement in rats by electrical stimulation on septal area [59], Mcintyre et al (1969) reported changes in rats behaviour as a result of daily brain electrical simulation [58]. This opened a new realm in the context of therapies for neurological diseases. Since then, many electrical stimulations methods have been explored.

Brain Stimulation Systems

Nowadays, there are some methods that have gained higher relevance and are more often used.

Deep brain stimulation (DBS) has been approved for the treatment of Parkinson's disease, essential tremor, and dystonia. Research is ongoing to explore its potential in other neurodegenerative disorders such as AD and Huntington's disease[49], [57], [60].

Transcranial Magnetic Stimulation (TMS) is non-invasive and uses magnetic fields to induce electrical currents in targeted brain regions. It is primarily used as a treatment for depression but has also shown promise in conditions such as Parkinson's disease and Alzheimer's disease [61].

Transcranial Direct Current Stimulation (tDCS) delivers a low-intensity direct current through electrodes placed on the scalp, modulating the excitability of cortical neurons. It is a non-invasive technique that has been investigated for various neurodegenerative diseases, including Parkinson's disease, Alzheimer's disease, and multiple sclerosis [40].

DBS consists of electrodes implanted in deep areas of the brain. These electrodes emit regular electrical impulses to modulate neuronal activity. It is primarily used as a treatment for chronic neurological disorders such as PD, AD,

obsessive-compulsive disorder (OCD), dystonia, essential tremor, epilepsy, and depression [62].

The working principle of DBS is not fully understood. However, there are some neuronal behaviours strongly linked to DBS. The main hypothesis is that high-frequency stimulation of DBS helps to normalize dysfunctional neuronal firing patterns in the brain[63]. It may disrupt pathological oscillations, synchronize neuronal activity, and induce plastic changes in neural circuits, ultimately leading to therapeutic effects. *Castro D et al (2024)*, proposed a closed-loop system to detect abnormal high frequency oscillations in the hippocampus and perform local electrical stimulation to restore normal firing activity [64].

Dysfunctional neuronal firing patterns are one target for DBS regarding PD and epilepsy [57]. The high frequency oscillations of the electrodes end up exhausting the synapse neurotransmitters of nearby neurons and block the pathway to subsequent action potentials of the pathological network. This disrupts excessive oscillatory synchronization leading to normal brain function [64], [65].

Depending on the frequency of stimulation and other parameters, different outcomes can be achieved on the target tissue. It has also been reported increased neuroplasticity resulting from DBS [60], [62], [66]. This neuroplasticity enhanced a notable recovery of damaged tissue on
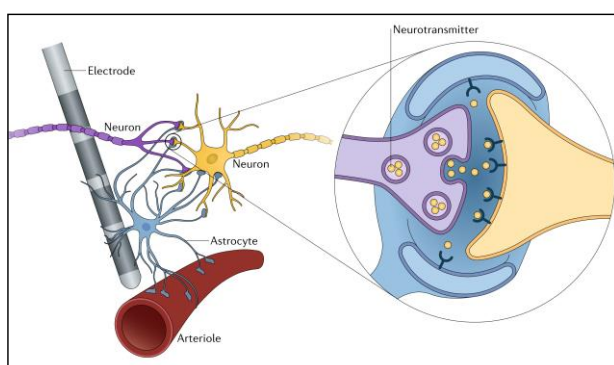

Figure 7. Overview of DBS on neuroplasticity. *Neurotransmitters (inset) are released in response to stimulation, leading to calcium waves and subsequent release of gliotransmitters. This release influences synaptic plasticity, leading to arteriole dilation and increased regional blood flow ultimately leading to tissue regeneration (Figure from [62]).*

neurodegenerative diseases, where most of the times the affected areas cannot achieve tissue recovery.

Overall, DBS and other types of electrical stimulation devices have opened another realm for approaching NDs. This realm probably boosted the field of neuronal signal processing. The bound line between stimulating and measuring is very thin. If a cable can be introduced precisely to deliver controlled current same can be achieved with an electrode to measure. So, with the development of DBS, local invasive electrodes have also gone through a process of tailoring [45]. The combination of measuring and stimulating provides another degree of freedom and control in the context of electrical stimulation as a therapy for NDs, where stimulations can be intelligently delivered in response to known biomarkers [64], [67], [68]. Some of these biomarkers are localized in specific parts of the brain, namely the hippocampus [69]. The hippocampus has shown to play an important role in ND. Oscillation patterns within its networks show to mediate important tasks related to memory [70]. Studies in the literature involve numerous recordings and analysis of signals within its neural populations[71].

The hippocampus

The hippocampus is one of the most thoroughly investigated parts of the brain. As a complex brain structure, it is found embedded into the temporal lo be and is involved in many processes of learning and memory. Since the famous report of the case study H.M.[10], who lost the ability to acquire new memories after the removal of the hippocampus in a desperate approach to suppress invalidating epileptic seizures, it has been posed at the centre of research in memory consolidation [72]. The intense research raised to the discovery of several subregions, with a complex interplay between them (firstly defined as trysinaptic loop), where input information from sensory systems was processed following a specific path (see

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[10] **H.M.,** whose full name was Henry Molaison, was a patient who became one of the most famous cases in the history of neuroscience. In 1953, when he was 27 years old, Molaison underwent

experimental brain surgery to alleviate severe epileptic seizures. The surgery, performed by Dr. William Scoville, involved the removal of large portions of his medial temporal lobes, including most of his hippocampus [72].

"*Hippocampal Subregions*"), thus providing the brain with a spatiotemporal framework where memories could be stored and consolidated.

Hippocampal subregions

The hippocampus is divided into two main complexes: Entorhinal Cortex (EC) and Dentate Gyrus (DG). EC provides the major cortical input source to the hippocampus. As information flows through it, different subregions (CA1, CA2, CA3, CA4) are distinguished [73], [74], [75] (see **Figure 8**). It is said to mediate neural communication between neurons from hippocampus and neocortex. It primarily targets Dentate Girus (DG), which then targets Cornu Ammonis 3 (CA3), finally leading to CA1, which will project back to neocortical neurons, through EC, to complete the loop [73], [75]. Apparently, the hippocampus seems predictable, each part doing a defined job. However, it has been found that the complex is not so stratified into separate regions. Simultaneous activity, parallel processing and widespread connectivity has been revealed in further studies [70], [73], [74], [75].
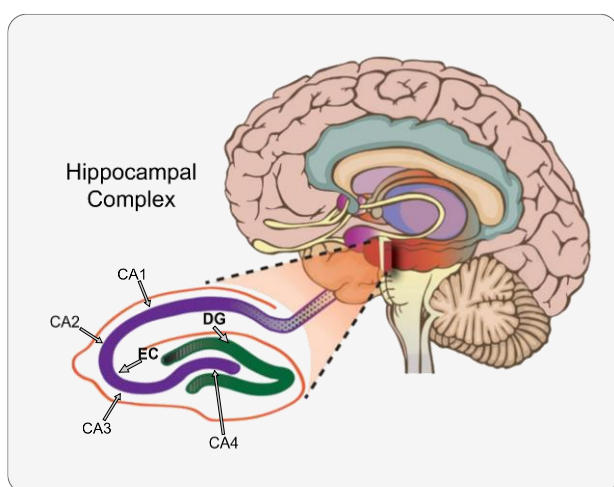

Figure 8. *Transversal axis sliced view of the hippocampus showcases its subregions and their spatial distribution. Entorinal Cortex (EC) and Dentrite Gyrus (DG) are sandwiched between them creating the collectively known "hippocampal formation".*

Memory consolidation

These processes happening within different subregions of the hippocampus are believed to somehow encode the lived experiences in a way that later the sense of it can be recreated in our

brain with conscious awareness (known as declarative memory). When exposed to an experience, the learned material remains vulnerable to interference for a period of time before consolidating in other cortical areas or getting replaced by new ones [70]. In the literature it has been reported two types of declarative memory [70]:

Episodic memories are those related to events that happened in a specific place and time throughout the day. They are easily forgotten. For example "this morning I closed the door when leaving".

Semantic memories are stored as general knowledge about the world. For example "if you touch the fire you get burnt". Also referred to as "long-term memory".

Many researchers believe that the hippocampus is specifically important for forming new episodic memories, whereas other parts of the temporal lobe and neocortex are more critical for semantic memories [75]. Therefore, the process of memory consolidation, where an experience is interiorized in our brain to contribute to our knowledge, relies on the ability of the hippocampus (as the first step of the process) to retain episodic memories. Episodic memories will follow further steps of consolidation if they are relevant or not [70] (repetition of an event usually makes it more relevant), ultimately becoming more independent of the hippocampus (semantic memory). In support to these, it has been observed that patients with hippocampal damage could remember events that happened years before but could not remember what they ate for breakfast [75].

The process of how memories gradually get independent from the hippocampus is not fully comprised. It is widely believed that the memory storage framework used by the mammalian brain is based on the synaptic weights and connections that neurons have between them[76], [77]. Connections and weights encoding for episodic memories are built during events [78]. Some oscillation and firing patterns originating from neuronal ensembles[11] are thought to be involved in

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[11] A neuronal **ensemble** refers to a group of neurons that function collectively to perform specific tasks or processes.

the process of transferring those connections and weights to the neocortex [70], [79], where they will be available for a long period of time. Then, the hippocampus will leave those connections free to new incoming events.

Sharp Wave Ripples (SWR)

SWRs are distinctive patterns of neural activity observed in the hippocampus. These patterns consist of high-frequency oscillations (ripples) superimposed on sharp-wave complexes, which are characterized by brief, high-amplitude deflections in the local field potential (LFP)[12]. They represent the most synchronous population pattern in the mammalian brain [69]. In the ~100 ms time window of a hippocampal SWR, 10-20% of the total neural population in the rat hippocampus discharge simultaneously in the CA3-CA1 subregions [74].
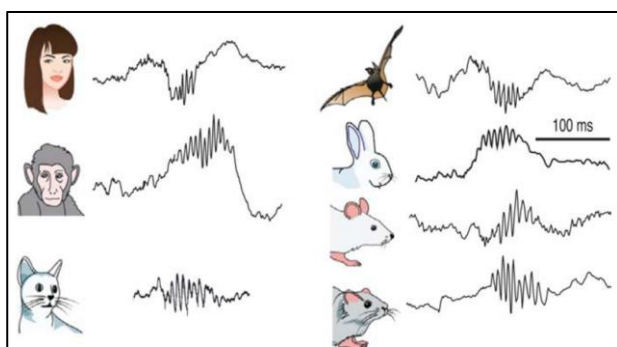

Figure 9. SWRs: the most synchronous oscillating pattern in the mammalian brain. *SWR recorded from different mammal hippocampus. They all have a similar pattern: 3-9 high amplitude waves in a frequency ranging from 100-250 Hz (Figure from* [69] *)*

They arise from neuronal ensembles in the hippocampus. Their excitatory output stimulates a wide area of the cortex and several subcortical nuclei. SWRs occur during "off-line" states of the brain, associated with consummatory behaviours[13] and non-REM sleep, and are influenced by numerous neurotransmitters and neuromodulators. Numerous studies unveil their relevance in the process of episodic memory encoding [69], [80], [81]. The memory traces are encoded via

weak synaptic potentiation[14] in the CA3 network induced by theta oscillations during consummatory behaviours. Then, synapses strengthened during the process contribute to the generation of SWRs [75], which target, through entorhinal cortex, neocortical regions. Therefore, SWRs stablish a bidirectional communication between hippocampus and neocortex, being able to gradually transfer memories outside the hippocampus and playing a pivotal role in the process of knowledge and memory consolidation.

SWRs related terms and definitions [69]

Sharp waves are characterized by brief, high-amplitude deflections in the LFP recorded in the hippocampus. They typically have a frequency range of around 0.1 to 4 Hz. These sharp waves represent synchronous depolarization of populations of neurons, often associated with the reactivation of neuronal ensembles involved in memory consolidation.

Ripples refer to high-frequency oscillations superimposed on the sharp waves. They are fast oscillations in the frequency range of approximately 100 to 250 Hz. Ripples are thought to reflect synchronized activity within local neuronal circuits, particularly involving the coordinated firing of interneuron[15] populations.

Fast Ripples are ripples occurring at higher frequencies, typically above 250 Hz. They are often observed in pathological conditions such as epilepsy and are believed to be related to abnormal neural firing patterns associated with seizure generation.
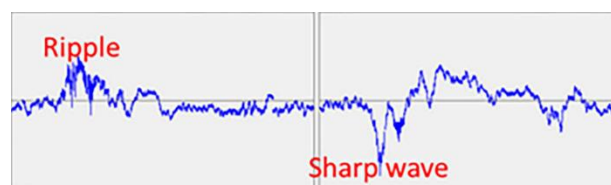

Figure 10. *Ripples refer to high amplitude oscillatory activity in frequencies ranging from 100-250 Hz. Sharp Waves show a deeper low frequency wave sharply decreasing at the beggining.*

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[12] **LFPs** reflects the synchronized activity of a group of neurons in the vicinity of the recording electrode.

[13] **Consummatory behaviours** are actions or activities that fulfil a biological or psychological need, typically associated with the satisfaction of a primary drive or motivation. (i.e. Eating, resting ...)

[14] **Weak synaptic potentiation** typically involves a moderate increase in the efficiency of neurotransmission at the synapse,

leading to a relatively modest enhancement in the postsynaptic neuron's excitability or responsiveness.

[15] **Interneurons** also known as association neurons, are a type of neuron that serves as a mediator or connector within the nervous system.

Pathological SWRs

In rat brains, SWRs duration range from 30 to 150ms, and its amplitude should never exceed 3mV [69]. Alteration of the physiological mechanisms supporting SWRs leads to a pathological signal morphology, which is a marker of epileptogenic tissue and can be observed in Schizophrenia and AD. In addition, dysfunctional SWRs could be an important cue for early-stage detection of AD, as these signals show different morphological features in the affected host and start appearing near the start of the disease, when there are no symptoms and no damage is yet done to brain tissue. It is true that genetic and environmental risk factors can be used to predict future AD, however, the confidence of these predictions and temporal prediction for the appearance of symptoms remains poor. Alternatively, these factors could be combined with an analysis of SWRs to detect the disease and treat it at an early stage.

Being these signals a promising biomarker for future neurodegenerative diseases affecting memory and cognition, it is of valuable interest to learn to correctly assess and detect them.

Overview

The human brain, with its intricate web of neurons and synapses, remains one of the most complex and enigmatic systems in nature. Understanding its fundamental workings holds the key to unraveling mysteries such as cognition and memory formation, which represent one of the main downsides of Neurodegenerative Disorders. Among the brain's many regions, the hippocampus stands out as a focal point for memory encoding and retrieval, spatial navigation, and emotional processing [70], [74].

Within the hippocampus, high-frequency oscillations (HFOs), such as sharp wave ripples (SWRs) have emerged as crucial neural events implicated in various cognitive functions and dysfunctions [70]. These oscillations, often ranging from 100 to 500 Hz, play a pivotal role in information processing and communication within neural circuits. Detecting and deciphering these HFOs provide invaluable insights into the underlying mechanisms of learning, memory, and pathologies such as epilepsy and Altheimer's Disease (AD) [69], [80], [81].

However, capturing and analysing HFOs pose significant challenges, particularly concerning power consumption and computational efficiency [8]. Traditional computing approaches struggle to cope with the complexity and real-time demands of neural data processing, especially when targeting low-power applications [82]. These applications, among others, include closed-loop systems [83], which typically involve the integration of sensing and stimulation components that can monitor neural activity in real-time and deliver therapeutic interventions accordingly, therefore becoming independent from outer machines and more comfortable for the patient. In Parkinson's Disease (PD) closed loop deep brain stimulation (DBS) systems are being developed willing to monitor neural activity and adjust stimulation parameters in response to variations in neural activity [64], [67], [68]. Of course, these systems require minimum energy consumption to minimize the number of recharging interventions.

Neuromorphic computing (NC), a groundbreaking approach inspired by the brain's own architecture and functionality, emulate the parallelism, plasticity, and energy efficiency of biological brains, offering promising opportunities for real-time and low-power neural data analysis [11], [84]. By harnessing the principles of SNNs and event-driven computation[16], neuromorphic platforms hold immense promise for revolutionizing the way we study and understand brain dynamics [9], [10].

Artificial intelligence has supposed a technological explosion in the last ten years. Current AI are performing better than humans, and the development possibilities it offers are uncountable. The relentless improvement of algorithms has been accompanied by a notable increase in energy consumption, which at first was

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[16] **Event-driven computation** is a computational paradigm where the execution of tasks or processes is triggered by events rather than being based on a fixed, predetermined schedule. In this approach, tasks are initiated in response to specific occurrences or stimuli, referred to as events.

overlooked. However, its widespread implementation is starting to raise concerns about sustainability and environmental impact[85]. Efforts underway to develop energy-efficient AI algorithms and hardware, are one of the causes pushing the advances of NC. Other pushing force of this research branch includes advancements in brain computer interfaces due to its further resemblance in the way of processing information [10] (which is more similar to neurons). Furthermore, these platforms are facilitating breakthroughs in understanding the complex dynamics of neural systems. By simulating the behaviour of biological neurons and synapses, fundamental principles of brain function, such as learning and memory can be studied[10][86].

In light of all benefits offered by NC among other computation systems, this work is commited to implement it in a neuroengineering framework. SNNs and neuromorphic computing have their origins many years ago, but it was near 2000 when it got accelerated due to the explosion of semiconductors industry and due to notable contributions from researchers such as Eugene Izhikevich, who introduced a revolutionary concept of neuron model in 2003. Up to date, NC have a solid background and implementation in diverse low-power engineering tasks, such as image classification systems [11]. Namely embedded cameras in cars, street traffic, security systems...[14]. Nonetheless, very few works about closed loop systems for brain applications have been reported [87]. Enabling low power and faster computation on implanted systems in the brain holds immense promise for revolutionizing health care, human-machine interaction, and seamless integration of advanced technologies within the human brain [6], [83].

This final degree project aims to explore the use of neuromorphic computing for the detection of Sharp Wave Ripples (SWRs), which is a kind of HFO, in the hippocampus. By leveraging state-of-the-art neuromorphic hardware and algorithms, a competent "*SNN-based*" signal detector can be built and trained. Cutting edge neuromorphic hardware device may be used to run the model instead of using a neuromorphic simulator. Different neuron models and neuromorphic computation paradigms from the literature [15] and network architectures will be explored aiming to find the better performant appropriate combination. Furthermore, the frameworks used in these areas will be learnt from online documentation. Naturally, state-of-the-art knowledge from different areas involved in the project will be learnt and embraced.

Lastly, once achieved data preparation and training of the model, the performance and computation time will be compared with state-of-the-art ANN models [88] and SNN models [8], [33] designed to detect same or similar oscillation patterns.

Neuromorphic computing, utilizing SNNs and state-of-the-art neuromorphic hardware, will achieve higher, or similar performance and efficiency in detecting Sharp Wave Ripples (SWRs) recorded from the rat hippocampus compared to traditional Artificial Neural Networks (ANNs) and existing SNN models.

The hypothesis is based on the premise that neuromorphic hardware and algorithms, which mimic the neural mechanisms of the brain, can offer improved performance and reduced computation time compared to conventional ANNs and current SNN models. This premise aligns with the goal of exploring the potential advantages of neuromorphic computing in detecting specific neural oscillation patterns, aiming to demonstrate its efficacy and efficiency in this domain.

Materials

Dataset

The dataset was recorded by Neural Circuits Lab from CSIC, Instituto Cajal, Madrid guided by Liset M. de la prida[17]. μ-LED optoelectrodes (32 channels, 4 shanks of 8-channels) where used in head-fixed awake transgenic Thy1-GCaMP7 mice[18] to record local field potentials (LFPs) at 30 KHz in the dorsal hippocampus [88]. Recordings were done several days before the implantation to let them habituate.

The dataset used for this study consisted on two recording sessions in different rats "Amigo2_1_hippo" and " Som_2_hippo". Each session contained ripples and SWR events tagged altogether as ground truth ripples. The times when ripples occurred were tagged by an expert in a postprocess phase. The expert would tag the start and the end of the event. This is because ripples have a range of durations and can include to 3-9 ripple oscillations. Therefore, it would not be correct to set a fixed window for all of them. In other studies, the events are tagged by setting an only marker in the centre of the ripple [71]. However, more information can be extracted by tagging the precise full window.

Each session had a binary raw file with the recorded LFPs, and an info file containing all the tagged events.
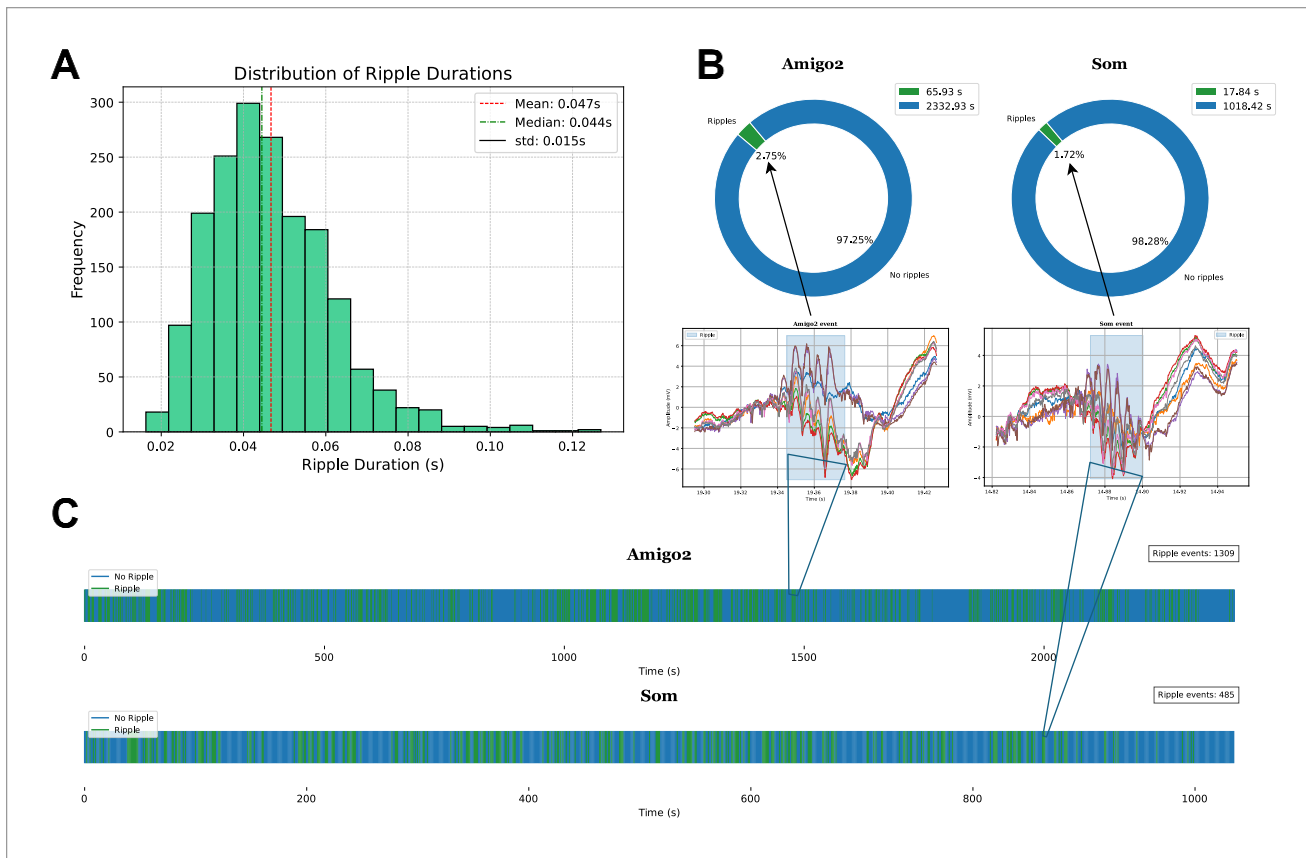

Figure 11. Overview of the two recording sessions: Ripple events, durations, and features. *Both datasets have very similar events. Amigo2 represents a major part of the data. Both sessions contain altogether 1794 ripple events.* ***A)*** *Normal distribution of the duration of the hand-tagged ripples. Most of them are in the range of 30-60 ms, however there is a non-small part of the population with longer durations ranging from 80-120 ms.* ***B)*** *Ripple events represent a ~2% of the recording and are* ***C)*** *uniformly distributed through it. Each vertical green line represent a second of the recording which contained at least one event.*

Methods

Implementing SNNs in biosignals is very recent and has low online documentation. Developing a SNN from scratch can be a challenging task [89].

At first, the recent publication of a machine learning toolbox for detecting ripples from liset M. de la prida on a similar dataset [90] pushed me to copy their approach. In their work, they were detecting the ripples with different CNNs[19] with different window sizes. Seeing a good performance achieved, the most logical way to proceed was to build an equivalent SNN. Reading the literature, it can be seen that SNN can be built from a previous ANN by copying the layer shapes and trespassing the weights[28]. However, since the nature of SNNs are fundamentally different from ANNs, the networks obtained from the conversion are not as good as the original ANN. Furthermore, a SNN may need less nodes to learn and better performance can be obtained if trained from scratch. Overall, some trials were done to reconvert the models from the liset publication with two different frameworks: Nengo (https://github.com/nengo) and SNN Toolbox (https://github.com/NeuromorphicProcessorProject/snn_toolbox). As the trials were unsuccessful, the approach was discarded.

Then it was decided to build and train the network from scratch with lava-nc. Lava, as a neuromorphic framework developed by intel, is mainly developed for widespread engineering applications. Furthermore, their documentation is not very complete. The most similar example found consisted of a n-mnist detector (see on https://github.com/lava-nc/lava-dl tutorials). There, they would go through the whole process of building and training the network. N-mnist dataset consists of binary arrays of similar shape than the ripples. Therefore, the approach was supported by the workflow observed in that example.

The learning module used was based on SpikeRate. Learning from spike rate means using the rate of the output neurons to make

predictions. Given a window of spikes (representing an input), the number of times the output neuron fired in response to that input is used to assess the prediction.

Signal visualization

Visualizing the signals is the first step to get familiarized. The data, however, is not always user friendly. When dealing with large amounts of data, it is sometimes convenient to store the data optimizing the space. In this study, the data was compressed to consume the less space possible. Python programming language was used to read and visualize the data.

Reading the data

The data provided, as mentioned in *"Materials"* section, was structured containing the signal data in a binary raw file, and the event timings in a separate info file. The binary file consisted of a one dimensional array containing the merged values of all channels from the whole recording time. The recording contained 43 channels, from which only 32 were recording data. The remaining 32 channels were divided in 4 sections. Each section was a shank containing 8 electrodes in different depths. Only one shank was placed at the region of interest (hippocampus) for each recording. Hence, the corresponding electrode values of the shank (specified in info file) needed to be extracted from the one-dimensional array. To do so, the concatenated channel values from each recording sample were assessed to isolate them from the other values. In order to simplify the process, a python module was programmed to easily load and visualize the data (https://github.com/MarcosOriolPago/liset_tk). Helpful cues for understanding how to load this kind of data were found in [88].

The data was normalized and downsampled. For the normalization it was used a custom z-score normalization (defined in python module *"load_data.py"* file). Once the data was normalized, it was downsampled to 4000 Hz. Original sampling frequency (SF) was 30000 Hz, but since the model does not need that much detail of the signal, and having higher SF means more input

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[19] **C**onvolutional **N**eural **N**etwork

signals for a time window event, it makes no sense to use the original SF. In the machine learning toolbox paper from liset, they downsampled the signals to 1250 Hz. However, the quality of the signals with that SF was very poor for the SNN.

The downsampling method consisted on taking the middle elements within the recording so that they would have the set values each second.
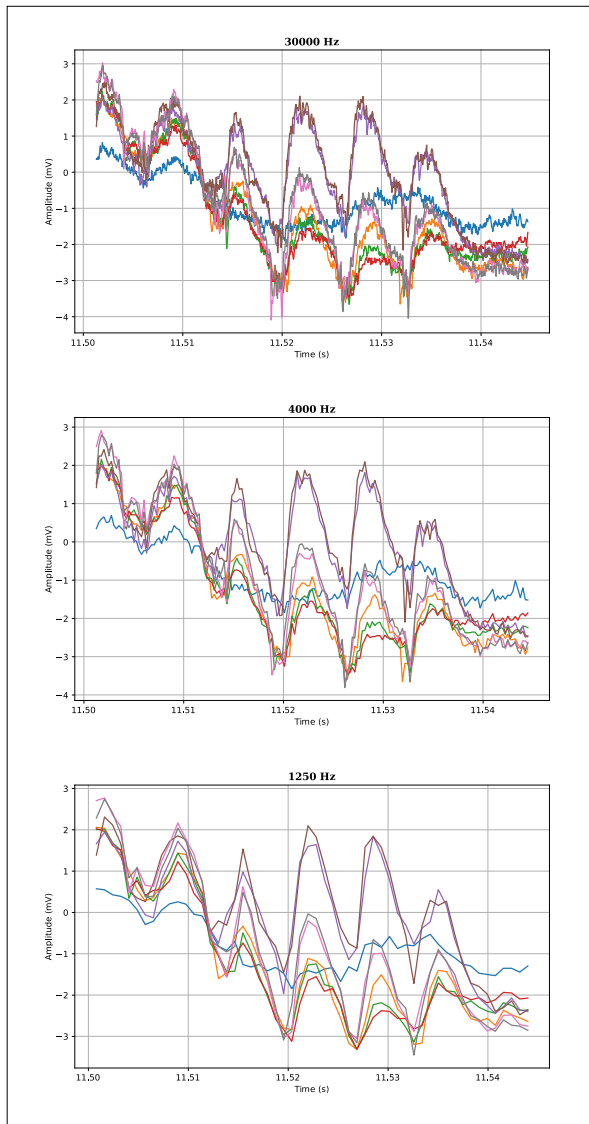

Figure 12. Sampling frequency choice. *The image shows the same signal for three different sampling frequencies (ripple event before filtering). Using 30000 Hz embraces too much unnecessary noise. 1250 Hz may work, because the shape, at its main, is conserved. However, in order to make sure it gets all the oscillations with a decent resolution, the fs can be increased to 4000 Hz, which seems reasonable.*

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[20] **N-dataset** refers to neuromorphic dataset. That is a binary dataset for spike supported training.

Data preparation

For achieving a ripples detector, the network should be able to distinguish between a ripple event and a non-ripple event. If the model would only be trained with ripple data but would never see non-ripple events it may not be able to differentiate between them. In this approach, a n-dataset[20] with the tagged ripples and another one with random non-ripple events was created from the recording sessions. Available code for the extraction of events and creation of n-dataset can be found in https://github.com/MarcosOriolPago/extract_Nripples.

SNNs work with spikes, which means, that the data must be reconverted into a spikes format to fit the network. Spikes, as pulses sent by neurons, are theoretically equal within them. The communication is based on the rate and specific times they are sent. To mimic a spike, the default value is set to 1, that is: 0 not spike, 1 spike. Following that rationale, signals were reconverted to a binary format.

Training dataset

There are many possible ways to create a representation of the data with spikes [8], [91]. In the context of detecting high frequency oscillations, which differ in shape from other neural patterns, the spikes may provide information about the shape. *Burelo K. et al.* applied an interesting conversion method which was based on UP and DN spikes. That is, if the processed signal crossed a threshold, a spike would be assigned to UP (positive) or DN (negative)[8]. That would provide information of the oscillatory frequency and the amplitude. Inspired by his work, this approach tried to add another degree of information to the spike signal. Instead of binarizing to UP and DN, the signal was discretized into a number "n" of values in the y axis. The number "n" could be
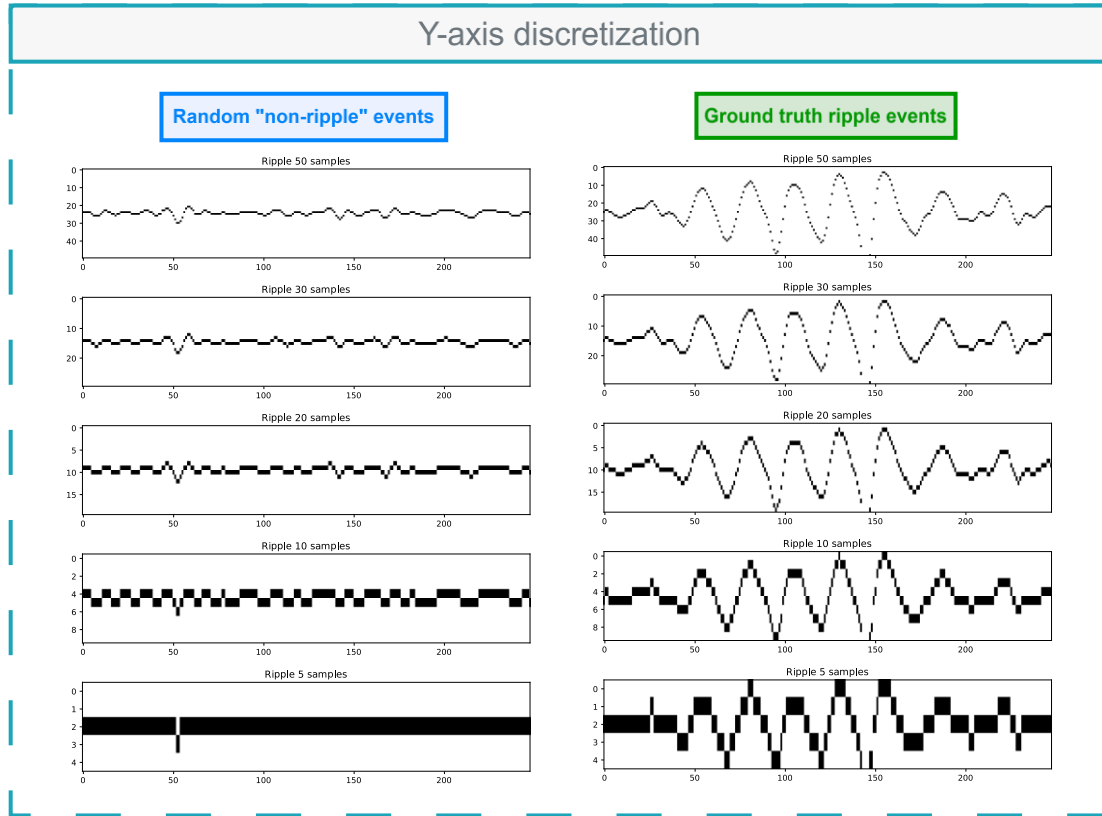
Figure13. Same signal discretized with different y values. *Ripple and non-ripple events need to be discretized to train the model. Each dot represents a spike that will make the corresponding input neuron from the network fire. The less values used, the more the neurons from each value will fire, and may learn faster. However, the cost of low level of discretization is the loss of resolution. The shape changes notably in lower values.*

fine-tuned to see how many levels would be necessary to assess the shape of the signal.

Extracting the events

The training dataset needed to be a dataset with two labels: "Ripple", "Non-ripple". Each label would contain events (of a fixed time window) representing the label. The tagged events only included ripple events, but not times with non-ripple activity. Therefore, they were set as random timings where for sure there was not a ripple nearby. On those timings, the half window was added to the right and the other half to the left, leaving a non-ripple event matching the restricted time window. For the ripple events, same method was applied from the middle of it. Next, the extracted events from the recording were bandpass filtered in the ripple band (100-250 Hz) and then converted to spikes (***Figure8***).

Defining the window

As the network needed a fixed window as an input for spike rate predictions and training, all the training data must have the same shape. As it

can be observed in ***Figure6-a*** the tagged ripples from the recordings had very different durations. Determining a fixed window for all of them was an important decision that would significantly affect the performance of the model. If the window is too short, large ripples may not be detected and otherwise, small ripples may not be detected. In order to set a window that would get the most of all the ripples, the following equation was used.

$$t\_w = \mu(ripples\_D) + \sigma(ripples\_D)$$

Where t_w is the fixed time window for creating the custom n-dataset and "*ripples_D*" is a one-dimensional array containing the durations of all the ripples tagged from both recordings. That is, the mean summed by the standard deviation of the ripple's durations. Once the time window value is set, the number of samples to take from the recording for extracting ripple and non-ripple events, would depend on the sampling frequency of the data.

Defining the filtering band

Ripples propagating through the hippocampus can show mild variations in its oscillation frequencies [69]. This explains the variability in bandpass filtering among the literature [46], [79], [92], [93]. Where they go from 80Hz to 150 Hz in the low pass, and from 200 Hz to 300 Hz in the high pass. In this work the filtering bandpass was set to 100-250 Hz as in [93]. In other works involving high frequency oscillations to detect epileptogenic brain areas, they filtered the LFPs in the 250-500 Hz bandpass. ***Figure10*** shows how different filtering values modify the LFPs of this study.

Conversion to Spikes

Once the filtered data, the last step of the process was spike conversion. The approach used was based on maintaining the shape of the signal but in a binarized way. That is, augmenting the dimension of the signal from 1D to 2D. The 2D signal had same length as the 1D signal and a custom height. Each value had a corresponding "1" in the height position corresponding to the amplitude of the value (y-axis discretization). Following that principle, the 2D signal would be an array of shape (heigh, length) where it would be seen the shape of the signal as in an image.

Figure 14. Finding the optimal filtering bandpass. *Clear Ripple, middle clear ripple and non-ripple events are filtered within three bandpass filters. Middle clear ripples are some events found in the tagged dataset which do not show clear ripple activity. The three ranges make a detectable difference between ripples and non-ripples, but not between middle-clear ripples and non-ripples. The most differential and consistent bandpass is the one seen in the literature (100 – 250 Hz).*

The equation for determining the position of the y axis was defined as follows:

$$y\_val = (y\_s - 1) - \left\lfloor \frac{x}{cutoff} \cdot \frac{y\_s}{2} + \frac{y\_s}{2} \right\rfloor$$

Where y_s is the number of "*y values*", x is the value of the 1D signal, and cutoff is the cutoff amplitude[21], defined with the function:

$$cutoff = \mu(ripples\_A) + \sigma(ripples\_A)$$

Where $ripples\_A$ is a one-dimensional array containing the maximum amplitudes of all the ripples tagged from both recordings.

Building the network

Once familiarized with the data, some approaches were explored to know which one could fit better. As first part of the approach, *"Principles of computational modelling in neuroscience"* [15] gave good insights of which kind of neurons
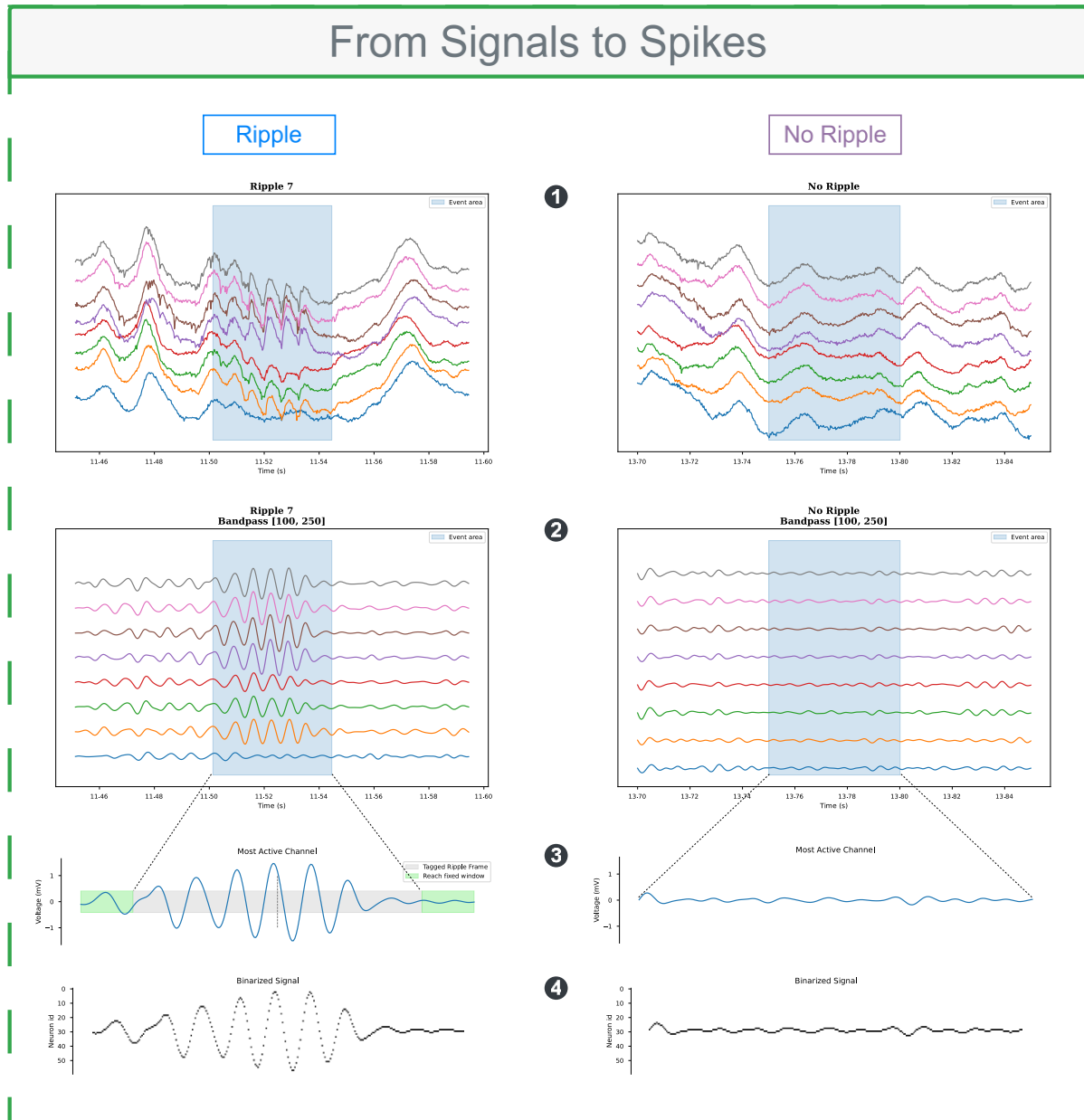

Figure 15. Workflow to build the n-dataset. 1) Events are located in the recording, 2) A Butterworth bandpass filter is applied in the range of 100 to 250 Hz, which is the oscillation speed of ripples, 3) highest amplitude channel is selected from the shank and the signal is adjusted in length to fit the previously set window size (in the case of no ripple, as they are not previously tagged, the window length is directly set), 4) the extracted signal is converted to spikes.

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[21] **Cutoff Amplitude** is the maximum value allowed for the y-axis discretization. Any value greater that the cutoff will be set to max neuron id (if positive) or min neuron id (if negative).

should be used for the detector. LIF neurons were the most appropriate due to their balance between biorealism and light computation. For applied NC not involving neural simulation they are totally fine. Other neuron models such as izhikevich neurons have similar simplicity in computation but show less behaviour mimicry.

Next, the network had to be defined and built. Following the workflow from n-mnist example in lava-dl github repository, the network was built and trained with ripples n-dataset. However, very bad performance was achieved with the architecture of n-mnist classifier. Next, other architectures were tested to get a glance of which architecture could perform better. The architectures ranged from 128 – 1024, and from 2 – 3 dense layers. Each architecture was run for n-datasets with different *"y_samples"* (4, 10, 20, 40,

60). With the obtained results, it was determined that the most consistent learning outcomes were given by 256_128 architecture. That is, the input layer, then a dense 256 LIF neuron layer, followed by a 128 LIF dense layer and finally, the output layer, which is 2 neurons referring to ripple and no-ripple. Trained models and code for the training can be found at https://github.com/Marco-sOriolPago/trainSNNripples.

The worflow for training a network used by lava consists in training the Spiking Neural Network with pytorch, and then converting it to a format compatible with neuromorphic hardware. Lava has builtin functions to convert the pytorch SNN network into a format that can be compiled into a lava process and deployed to neuromorphic hardware, namely, hdf5. Pytorch network (.pt) can be run on cpu as any other network. Hdf5
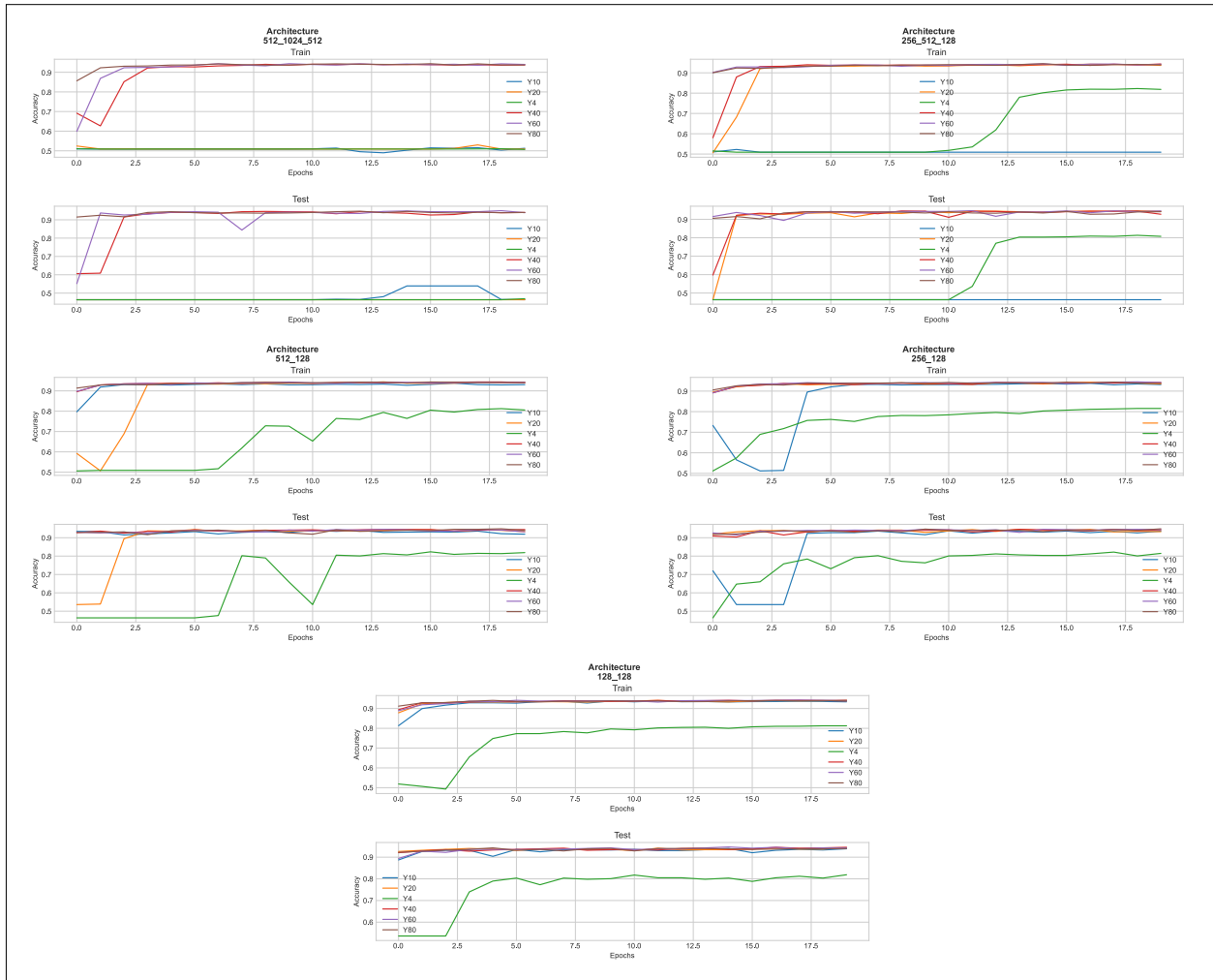
Figure 2. In search of the optimal architecture. *Five trials were tested on n-datasets with different "y values" (4, 10, 20, 30, 40, 60, 80). Based on the results obtained, models with 3 dense layers had more difficulty for learning the patterns, and needed more epochs to reach high accuracy values. Lighter models, with 2 layers seemed to learn the patterns faster and needed less resolution for the y-discretization, that is, needed less neurons as input to detect the ripples. The most consistent with higher accuracy values was given by the model with two layers: 256_128.*

The ULC barcelona logo

network (.net), naturally cannot be run on cpu, because neuromorphic hardware is fundamentally different than traditional silico hardware.

Model evaluation

The performance determined in the training and testing was given by the pytorch SNN model. To test on neuromorphic hardware it would be required to use the hdf5 network and run it either in physical loihi2 (where a system should be developed in which the device would have as input the data through a physical port), or in a neuromorphic simulator (such as the lava loihi simulator). Due to time limitations, the SNN model was not able to be run on hardware, and the accuracy was assessed by the equivalent pytorch SNN.

Results

Architecture 256_128 — Train / Test

Architecture 256_128 — Train / Test
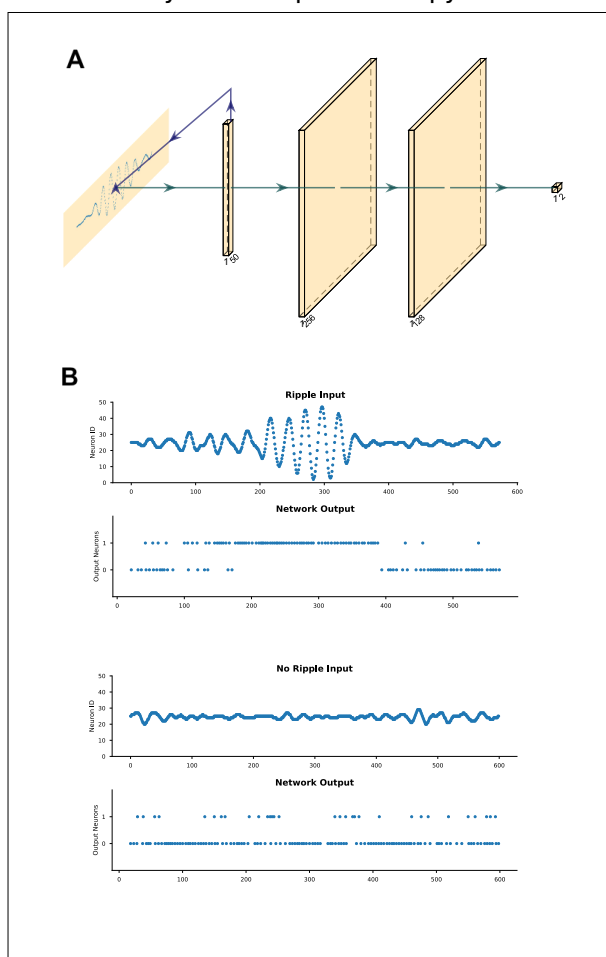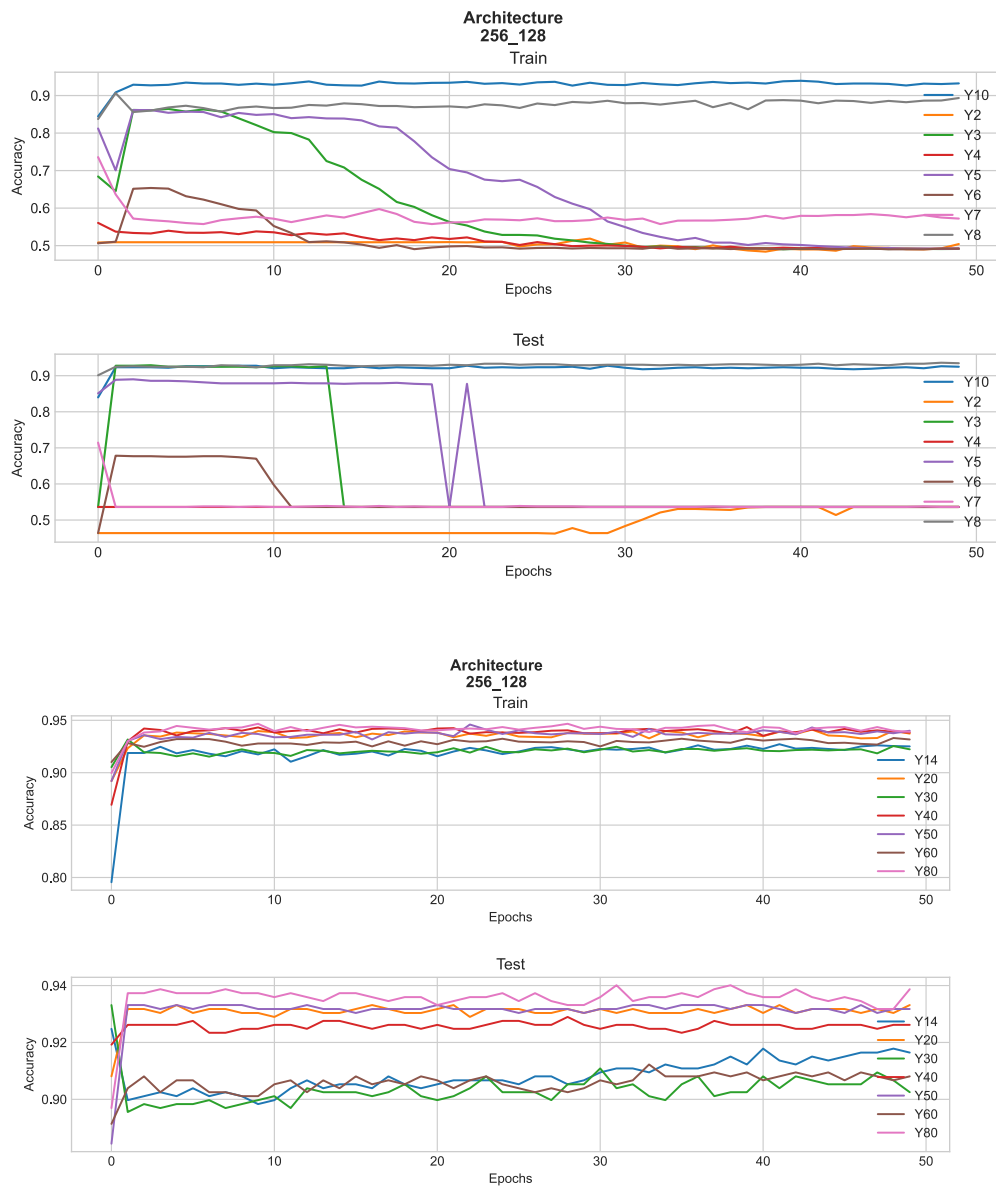
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