# 1. Description

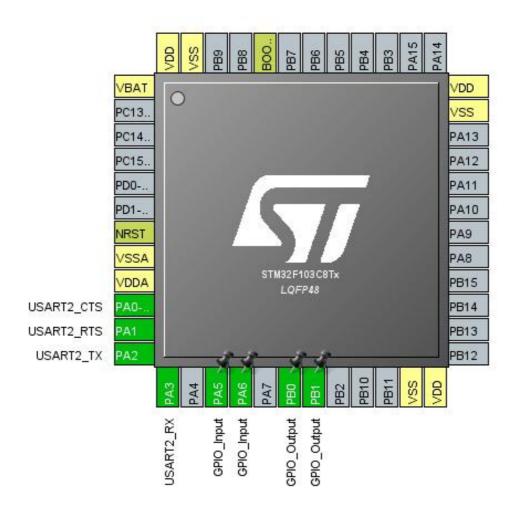
## 1.1. Project

Project Name	gsmCommunication
Board Name	custom
Generated with:	STM32CubeMX 5.0.0
Date	04/20/2019

### 1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C8Tx
MCU Package	LQFP48
MCU Pin number	48

## 2. Pinout Configuration

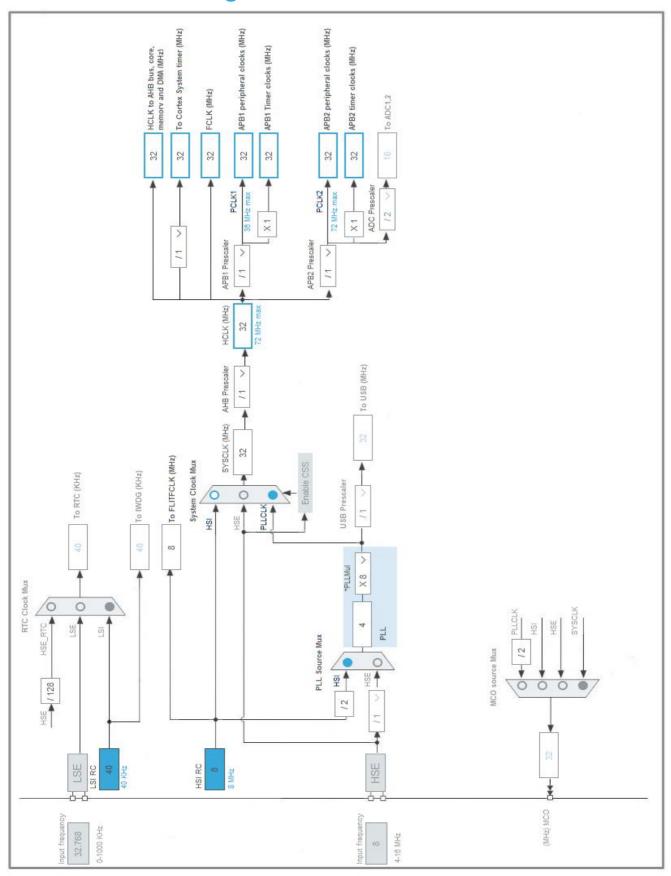


# 3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0-WKUP	I/O	USART2_CTS	
11	PA1	I/O	USART2_RTS	
12	PA2	I/O	USART2_TX	
13	PA3	I/O	USART2_RX	
15	PA5 *	I/O	GPIO_Input	
16	PA6 *	I/O	GPIO_Input	
18	PB0 *	I/O	GPIO_Output	
19	PB1 *	I/O	GPIO_Output	
23	VSS	Power		
24	VDD	Power		
35	VSS	Power		
36	VDD	Power		
44	воото	Boot		
47	VSS	Power		
48	VDD	Power		

<sup>\*</sup> The pin is affected with an I/O function

# 4. Clock Tree Configuration



# 5. Software Project

## 5.1. Project Settings

Name	Value		
Project Name	gsmCommunication		
Project Folder	D:\Embedded_System\Projets\gsmCommunication\SW\gsmCommunication		
Toolchain / IDE	MDK-ARM V5		
Firmware Package Name and Version	STM32Cube FW_F1 V1.7.0		

## 5.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	

# 6. Power Consumption Calculator report

#### 6.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
мси	STM32F103C8Tx
Datasheet	13587_Rev17

#### 6.2. Parameter Selection

Temperature	25
Vdd	3.3

# 7. IPs and Middleware Configuration 7.1. SYS

**Debug: No Debug** 

Timebase Source: SysTick

#### **7.2. USART2**

**Mode: Asynchronous** 

Hardware Flow Control (RS232): CTS/RTS

7.2.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction Receive and Transmit

Over Sampling 16 Samples

#### 7.3. FREERTOS

mode: Enabled

#### 7.3.1. Config parameters:

Versions:

FreeRTOS version 9.0.0
CMSIS-RTOS version 1.02

Kernel settings:

USE\_PREEMPTION Enabled

CPU\_CLOCK\_HZ SystemCoreClock

TICK\_RATE\_HZ 1000

MAX\_PRIORITIES 7

MINIMAL\_STACK\_SIZE 128

MAX\_TASK\_NAME\_LEN 16

USE\_16\_BIT\_TICKS Disabled

IDLE\_SHOULD\_YIELD Enabled

USE\_MUTEXES Enabled

USE\_RECURSIVE\_MUTEXES Disabled

USE\_COUNTING\_SEMAPHORES

QUEUE\_REGISTRY\_SIZE

USE\_APPLICATION\_TASK\_TAG

ENABLE\_BACKWARD\_COMPATIBILITY

USE\_PORT\_OPTIMISED\_TASK\_SELECTION

USE\_TICKLESS\_IDLE

USE\_TASK\_NOTIFICATIONS

Disabled

Enabled

#### Memory management settings:

Memory AllocationDynamicTOTAL\_HEAP\_SIZE3072Memory Management schemeheap\_4

#### **Hook function related definitions:**

USE\_IDLE\_HOOK Disabled
USE\_TICK\_HOOK Disabled
USE\_MALLOC\_FAILED\_HOOK Disabled
USE\_DAEMON\_TASK\_STARTUP\_HOOK Disabled
CHECK\_FOR\_STACK\_OVERFLOW Disabled

#### Run time and task stats gathering related definitions:

GENERATE\_RUN\_TIME\_STATS Disabled
USE\_TRACE\_FACILITY Disabled
USE\_STATS\_FORMATTING\_FUNCTIONS Disabled

#### Co-routine related definitions:

USE\_CO\_ROUTINES Disabled MAX\_CO\_ROUTINE\_PRIORITIES 2

#### Software timer definitions:

USE\_TIMERS Disabled

#### Interrupt nesting behaviour configuration:

LIBRARY\_LOWEST\_INTERRUPT\_PRIORITY 15
LIBRARY\_MAX\_SYSCALL\_INTERRUPT\_PRIORITY 5

#### 7.3.2. Include parameters:

#### Include definitions:

vTaskPrioritySet Enabled uxTaskPriorityGet Enabled vTaskDelete Enabled vTaskCleanUpResources Disabled vTaskSuspend Enabled vTaskDelayUntil Disabled Enabled vTaskDelay Enabled xTaskGetSchedulerState

xTaskResumeFromISR	Enabled
xQueueGetMutexHolder	Disabled
xSemaphoreGetMutexHolder	Disabled
pcTaskGetTaskName	Disabled
uxTaskGetStackHighWaterMark	Disabled
xTaskGetCurrentTaskHandle	Disabled
eTaskGetState	Disabled
xEventGroupSetBitFromISR	Disabled
xTimerPendFunctionCall	Disabled
xTaskAbortDelay	Disabled
xTaskGetHandle	Disabled

#### \* User modified value

# 8. System Configuration

## 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull	Max	User Label
				down	Speed	
USART2	PA0-WKUP	USART2_CTS	Input mode	No pull-up and no pull-down	n/a	
	PA1	USART2_RTS	Alternate Function Push Pull	n/a	High *	
	PA2	USART2_TX	Alternate Function Push Pull	n/a	High *	
	PA3	USART2_RX	Input mode	No pull-up and no pull-down	n/a	
GPIO	PA5	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	
	PA6	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	

## 8.2. DMA configuration

nothing configured in DMA service

## 8.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Prefetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0		
System tick timer	true	15	0	
PVD interrupt through EXTI line 16	unused			
Flash global interrupt	unused			
RCC global interrupt	unused			
USART2 global interrupt	unused			

<sup>\*</sup> User modified value

# 9. Software Pack Report