

# CS1026 – Lab #6 - 16324334

The truth table and ASM diagram for a JK Flip flop. And also a Negatively edge triggered D Flip Flop

Clock	J	K	Q	Q'
0	0	0	Q	Q'
0	0	1	0	1
0	1	0	1	0
0	1	1	Q'	Q
1	0	0	Q	Q'
1	0	1	Q	Q'
1	1	0	Q	Q'
1	1	1	Q	Q'

