

# SYSFLT: MCWDT fault and reset generation

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- › **Description:**
  - This example demonstrates the generation of faults using MCWDT and subsequent reset.
  
- › **Target Device:**
  - Traveo-II CYT2Bx devices
  
- › **CPU Board:**
  - CYTVII-B-E-176-CPU BOARD REV.C (REV\_C)

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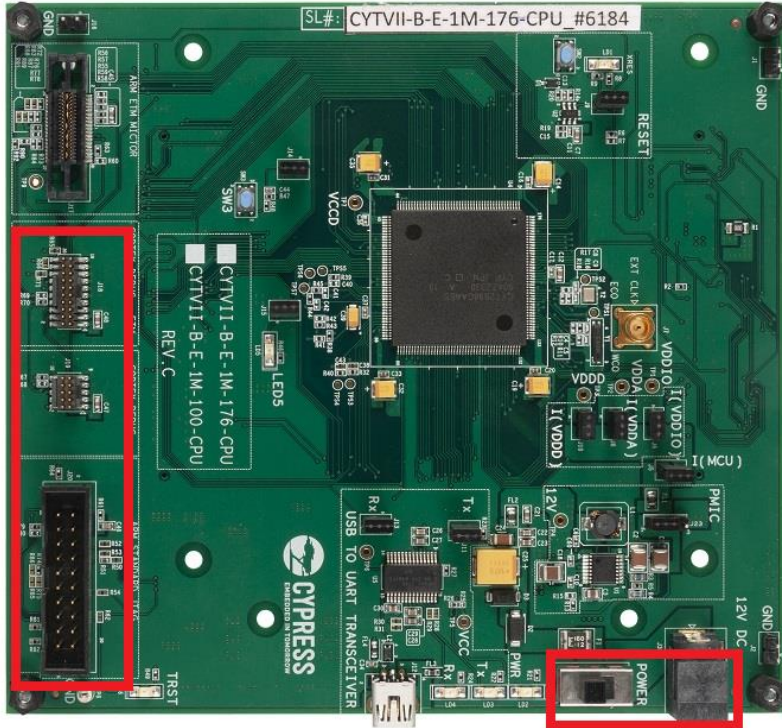
### › **Dependency:**

- Any one example needs to be copied to the “src” based on the interested core

### › **Expectation:**

- Run the program
- Put a break point in the fault ISR and observe for the status and see whether which sub-counter and lower/upper limit caused the fault
- By default check for the reset too, if not interested in checking resets enable the last part of the code inside the infinite while loop

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## > Legend:

- Red block for power, debug and USB (Mandatory)
- Yellow block for the example specific connections