SYSPM – Sleep – Wake-up from TCPWM

› **Description**:
  - This example demonstrates how to enter Sleep mode and exit it through a TCPWM interrupt

› **Target Device**:
  - Traveo-II CYT2Bx devices

› **CPU Board**:
  - CYTVII-B-E-176-CPU BOARD REV.C (REV_C)
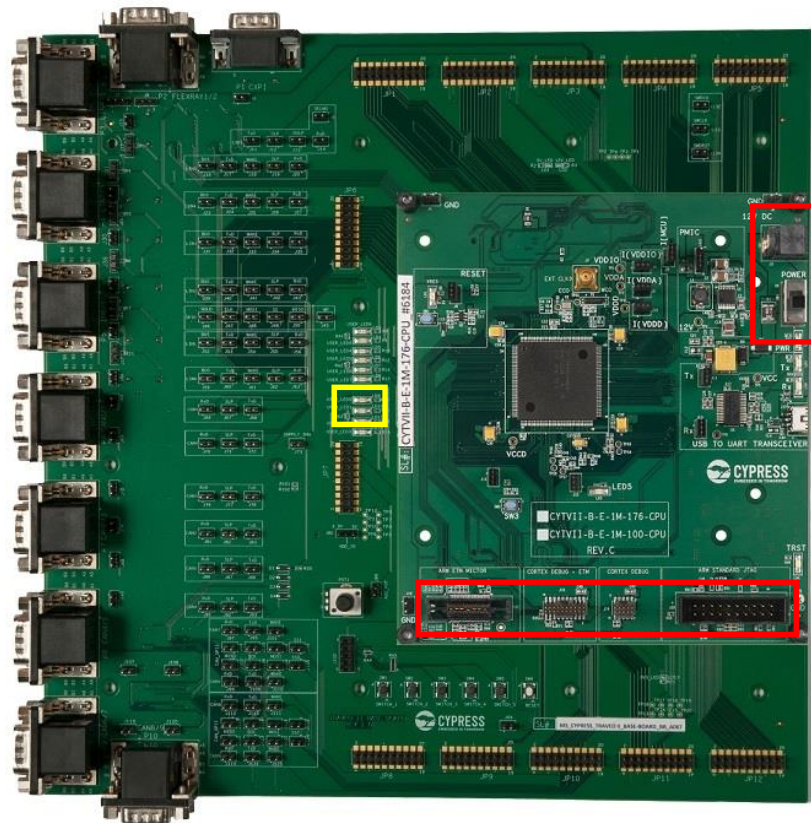
› **Dependency:**
  – None

› **Expectation**:
  – CM4 configures the TCPWM and then enters Sleep mode
  – Every 1 second the CPU will wake up
  – It toggles one LED on the base board in the TCPWM IRQ handler
  – It toggles another LED on the base board for half a second
  – Then sleep mode is entered again

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Legend:
- Red block for power and debug (Mandatory)
- Yellow block for the example specific connections