SCB EZI2C: Software mode

› **Description**:
   - This example demonstrates SCB-EZI2C in SW Slave Mode, with PC/Aardvark in master Mode.

› **Target Device**:
   - Traveo-II CYT2BLx devices

› **Hardware**:
   - CYTVII-B-E-176-CPU BOARD REV.C (REV_C)
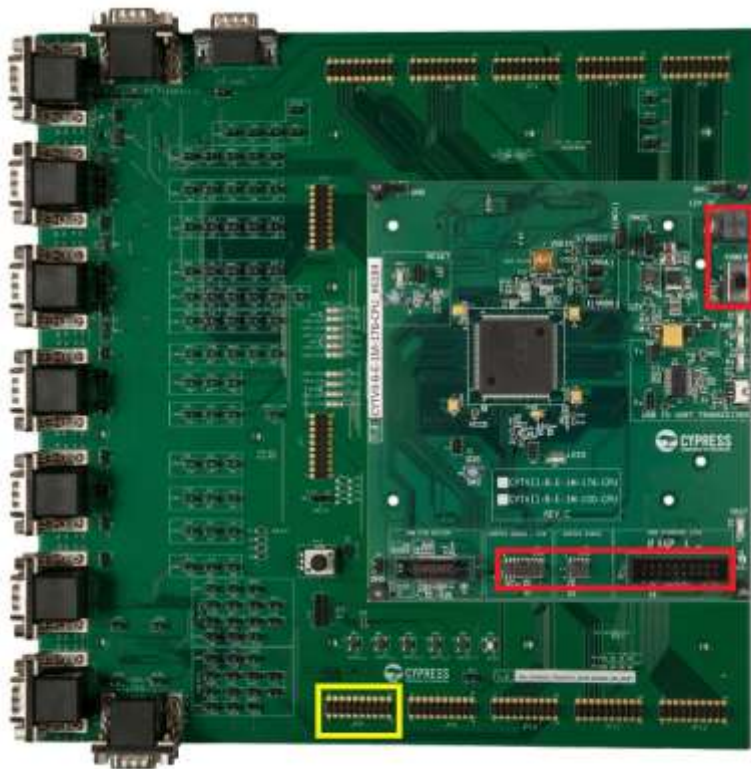   - TVII Base Board (BB)

› **Dependency:**
  – Any example could be copied to the main "src" based on the interested core
  – Two slave addresses are supported for the MCU, 0x08 and 0x09
  – Aardvark SCL/0 - P1_0_SCB0_I2C_SCL (JP8/4 on BB)
  – Aardvark SDA/2 - P1_1_SCB0_I2C_SDA (JP8/3 on BB)
  – Aardvark GND/1 to any GND on the CPU/BB

› **Expectation**:
  – MCU is set in Slave Mode always in EZI2C, Aardvark shall be set in Master mode and click start.
  – PC/Aardvark sends data to MCU. Aardvark will show the sent data and received data.
  – Any SCB channel can be used and along with respective channel interrupts.

› **Legend**:

– Red block for power, debug and USB (Mandatory)

– Yellow block for the example specific connections