

SCB SPI: Onboard SPI EEPROM

› **Description:**

- This example demonstrates SCB-SPI in Master Mode to read and write on board SPI EEPROM.

› **Target Device:**

- Traveo-II CYT2B6x devices

› **Hardware:**

- CYTVII-B-E-1M-100-CPU REV 1.0
- TVII Base Board (BB)

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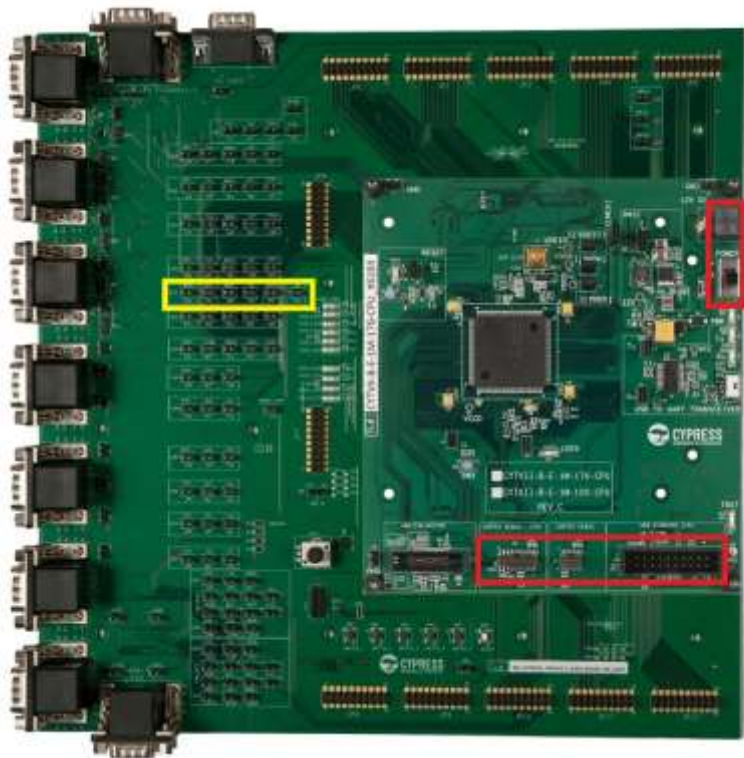
› Dependency:

- Baseboard connected to the CPU board
- MOSI – JP2/10 on BB
- MISO – JP2/9 on BB
- SCLK – JP2/12 on BB
- SS – JP2/13 on BB
- Connect these to the J47/1 J48/1, J49/1, J50/1 on BB

› Expectation:

- First part of the example writes byte by byte (255 bytes) to the SPI EEP with values 0 to 255, reads back. Observe the read buffer after the completion “readData”.
- Second part write in pages of size 32 bytes. Observe the read buffer after the completion “readData2”.

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› Legend:

- Red block for power, debug and USB (Mandatory)
- Yellow block for the example specific connections