

## TCPWM: PWM\_DT

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### › **Description:**

- This example is used to demonstrate PWM dead Time Test.
  1. PWM Port outputs the 200Hz with right side dead time 250 [us] and left side dead time 500 [us].

### › **Target Device:**

- Traveo-II CYT2Bx devices

### › **CPU Board:**

- CYTVII-B-E-176-CPU BOARD REV.C (REV\_C)

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### › **Dependency:**

- Check the following port output:

Port 6.0: BB\_LIN3\_RX

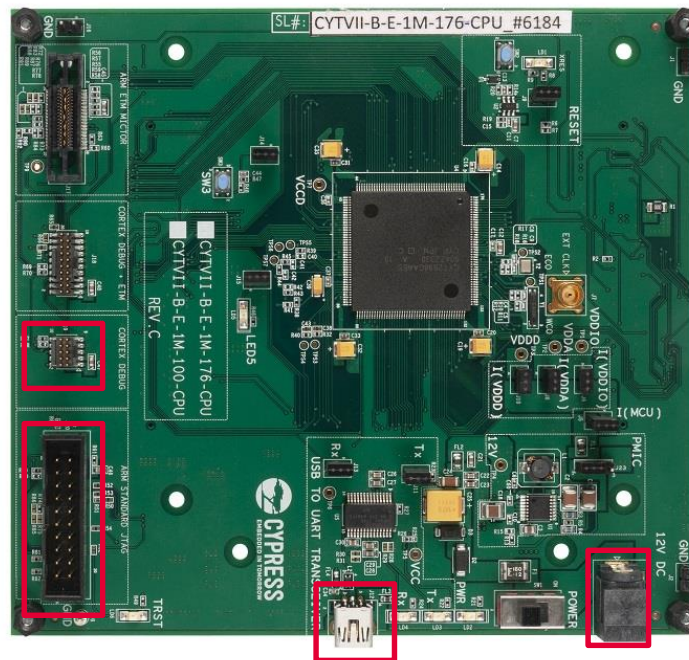
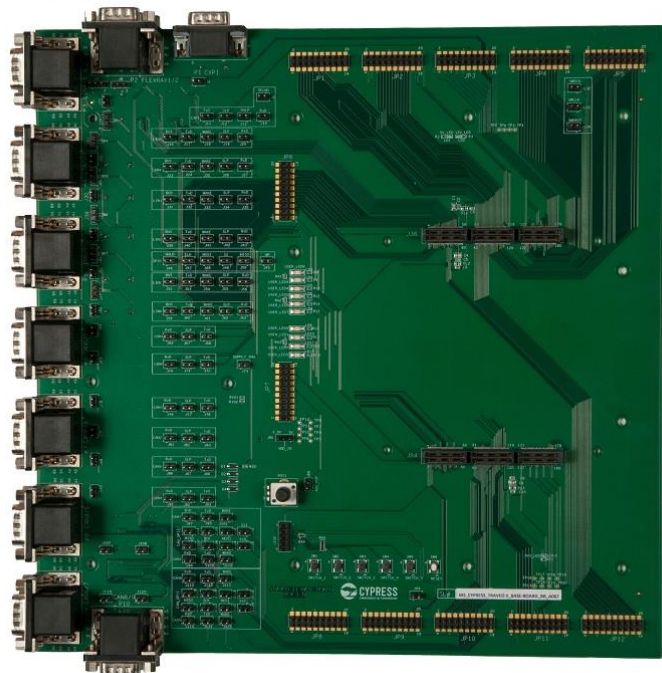
Port 6.1: BB\_LIN3\_TX

### › **Expectation:**

- Check the port 6.0 and Port 6.1:

It will outputs 200Hz with right side dead time 250 [us] and left side dead time 500 [us].

# Trigger MUX: DMA-UART



## Legend:

- Red block for power, debug and USB (Mandatory)