DMA: MDMA Interrupt



Description:

- This example demonstrates MDMA interrupt test.
- Destination buffer address is changed alternately via receiving the complete interrupt which indicates Mem to Mem transfer is executed.

Target Device:

Traveo-II CYT2BLx devices

> CPU Board:

CYTVII-B-E-1M-176-CPU Rev. C Board

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Dependency:

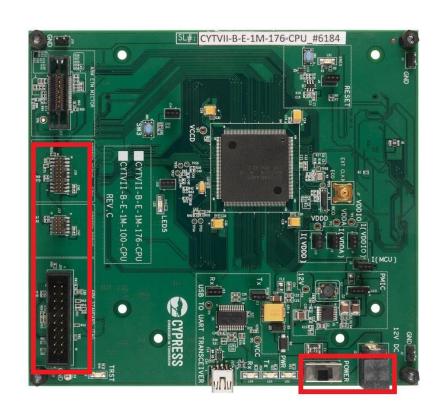
Any example could be copied to the main "src" based on the interested core

> Expectation:

 After running the example, user should be able to see the 'au8SrcBuffer' contents in the destination buffers 'au8DestBuffer0' and 'au8DestBuffer1' in the watch window of the IDE.

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> Legend:

Red block for power, debug and USB (Mandatory)