

SYSFLT : MPU fault

- › **Description:**
 - This example demonstrates the generation of faults using MCWDT and subsequent reset.

- › **Target Device:**
 - Traveo-II CYT2Bx devices

- › **CPU Board:**
 - CYTVII-B-E-176-CPU BOARD REV.C (REV_C)

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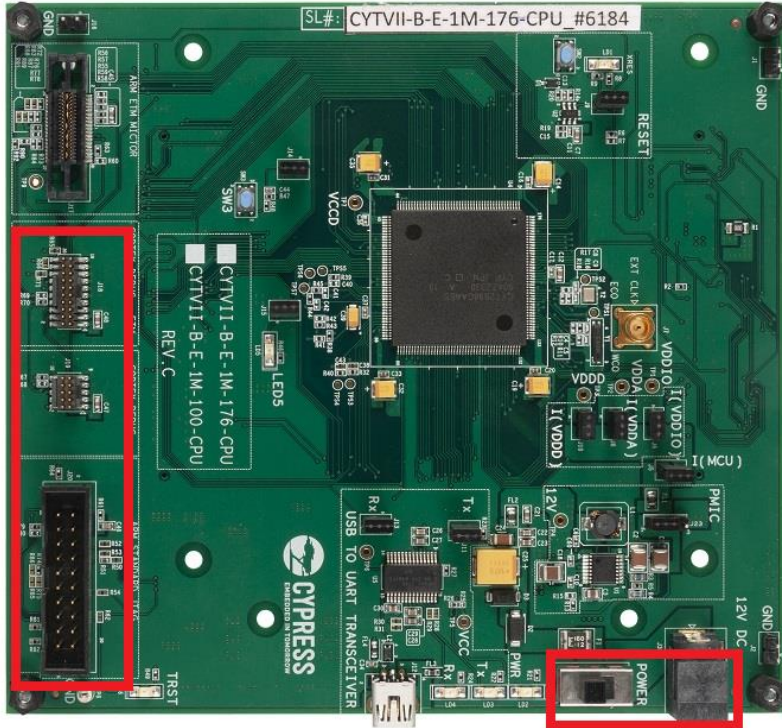
› **Dependency:**

- Both the examples need to be copied to the “src”

› **Expectation:**

- cm0plus example generates MPU fault and cm4 captures the fault
- Put a breakpoint inside the fault ISR of cm4 example
- Run both the cores
- Observe for the fault data to understand which SRAM address caused fault to trigger

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> Legend:

- Red block for power, debug and USB (Mandatory)
- Yellow block for the example specific connections