

TCPWM: PWM_PR

- › **Description:**
 - This example is used to demonstrate generation of random pulse.

- › **Target Device:**
 - Traveo-II CYT2Bx devices

- › **CPU Board:**
 - CYTVII-B-E-176-CPU BOARD REV.C (REV_C)

TCPWM: PWM_PR

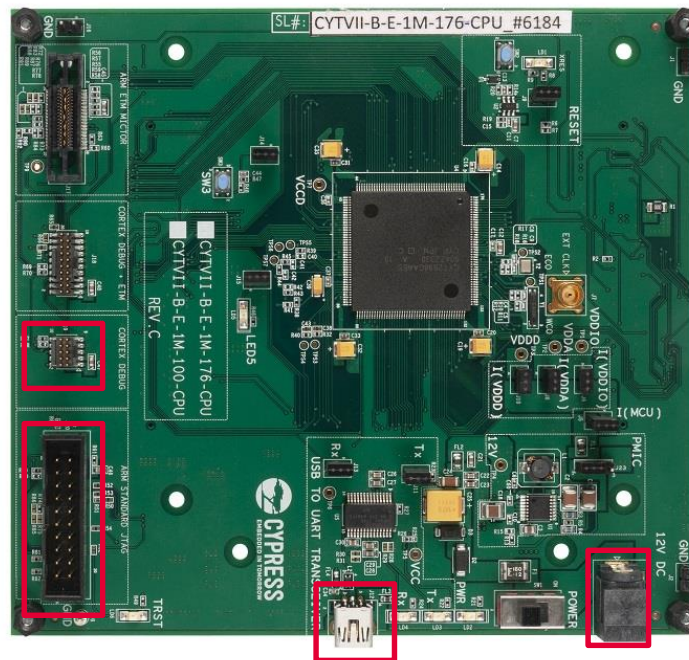
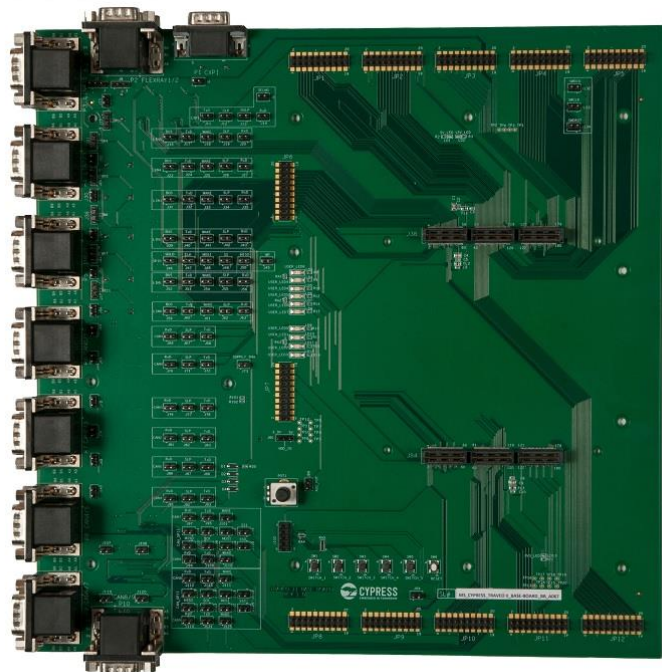
› **Dependency:**

- Check the Port 6.1
Port 6.1: BB_LIN3_TX

› **Expectation:**

- Check each PWM port, it will output random pulse.

Trigger MUX: DMA-UART



Legend:

- Red block for power, debug and USB (Mandatory)