SCB EZI2C: Hardware mode



Description:

 This example demonstrates SCB-EZI2C in HW Slave Mode, with PC/Aardvark in master Mode.

Target Device:

Traveo-II CYT2B6x devices

Hardware:

- CYTVII-B-E-1M-100-CPU REV 1.0
- TVII Base Board (BB)

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Dependency:

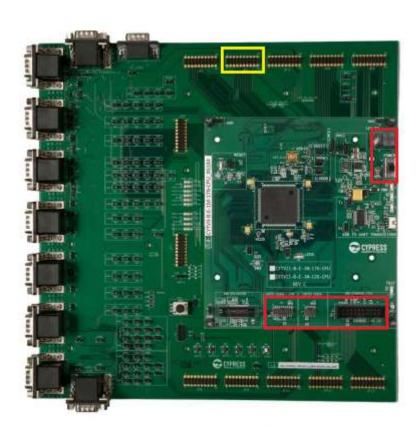
- Any example could be copied to the main "src" based on the interested core
- Slave address of the MCU is 0x08
- Aardvark SCL/0 JP2/12 on BB
- Aardvark SDA/2 JP2/10 on BB
- Aardvark GND/1 to any GND on the CPU/BB

Expectation:

- MCU is set in Slave Mode always in EZI2C, Aardvark shall be set in Master mode and click start.
- PC/Aardvark sends data to MCU. Aardvark will show the sent data and received data.
- Only SCB0 is supported and no interrupt handling.

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Legend:

- Red block for power, debug and USB (Mandatory)
- Yellow block for the example specific connections