#### SYSRTC



### Description:

- This example demonstrates the RTC setup and alarm handling

### Target Device:

Traveo-II CYT2Bx devices

#### > CPU Board:

CYTVII-B-E-176-CPU BOARD REV.C (REV\_C)

### **SYSRTC**



# Dependency:

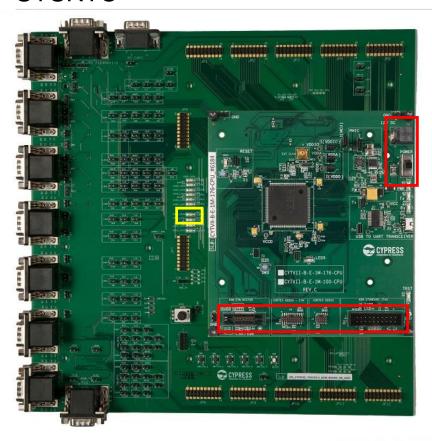
None

# Expectation:

LED on the base board is toggled every minute (via the RTC alarm IRQ handler)

### **SYSRTC**





# Legend:

- Red block for power and debug (Mandatory)
- Yellow block for the example specific connections