SYSINT: Software Interrupt

› **Description**:
  – This example demonstrates software interrupt
    1. Use software interrupt 0 handler
    2. Use software interrupt 7 handler

› **Target Device**:
  – Traveo-II CYT2Bx devices

› **CPU Board**:
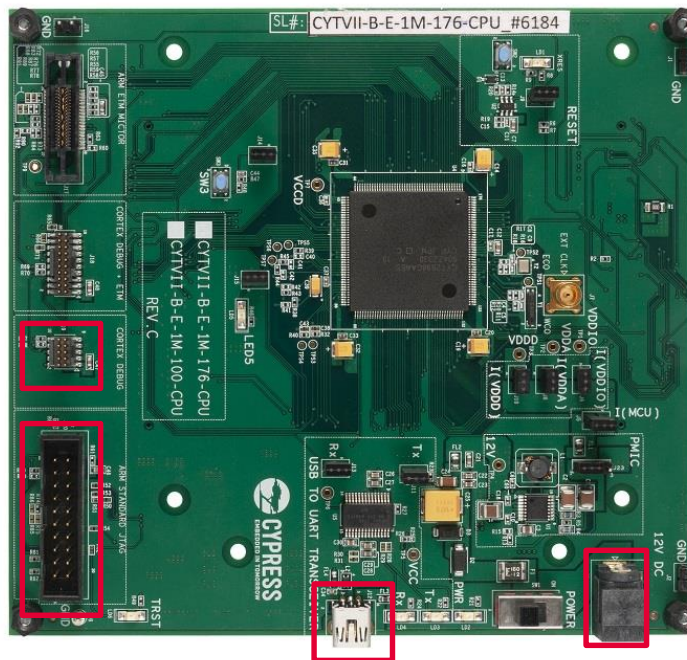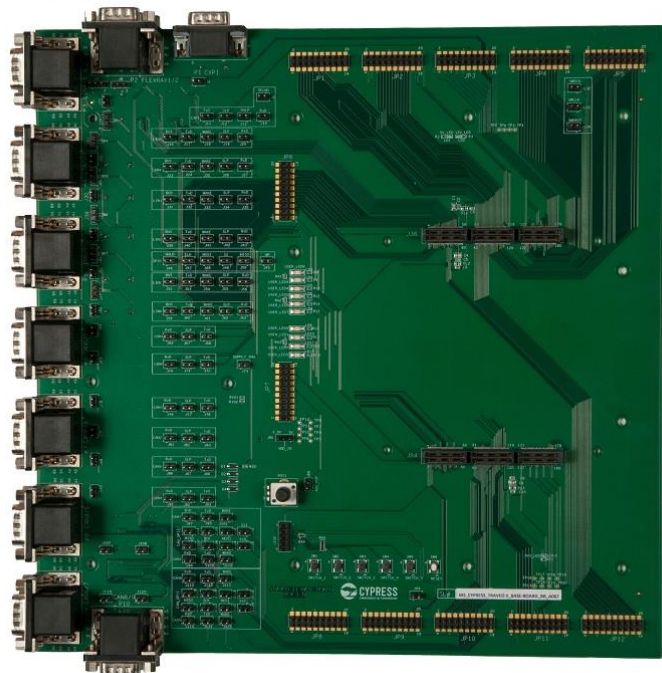 – CYTVII-B-E-1M-176-CPU

› **Dependency:**
   – None

› **Expectation**:
   – If software interrupt 0 handler occurred, then go to interrupt 0 handler.
   – If software interrupt 7 handler occurred, then go to interrupt 7 handler.

Trigger MUX: DMA-UART

› **Legend**:
  – <mark>Red block</mark> for power, debug and USB (Mandatory)