

## CXPI: Polling – Master – PWM Mode

### › **Description:**

- This example demonstrates CXPI transmission in Polling Trigger method.
- TVII acts as a master node of CXPI network.

### › **Target Device:**

- Traveo-II CYT2B9x devices

### › **CPU Board:**

- CYTVII-B-E-1M-176-CPU Rev. C Board
- CYTVII-B-E-BB Rev.A Board

### › **Dependency:**

- CYTVII-B-E-1M-176-CPU board should be connected on CYTVII-B-E-BB board.
- Open J60, J62, J63 on the base board. Short J58 on the base board.
- Connect following pins on the base board
  - (RX) JP5.9 to J63.2, (TX) JP5.7 to J60.2, (EN) JP7.17 to J62.1
- Connect LIN0 connector (P5 upper one) on the base board and CXPI analyzer if available.

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### › Expectation:

- Scheduled frames: TVII send PIDs according to below schedule

No.	PTYPE	ID	PID transmitter	Response transmitter	Frame length (byte)	Frame interval
1	no	0A	TVII	CXPI analyzer	8	100ms
2	no	4A	TVII	TVII	8	100ms
3	no	0F	TVII	CXPI analyzer	16	100ms
4	no	4F	TVII	TVII	16	100ms
5	yes	6D	CXPI analyzer	CXPI analyzer	8	100ms
6	yes	7D	CXPI analyzer	TVII	8	100ms

TVII copy the response and send back to tester

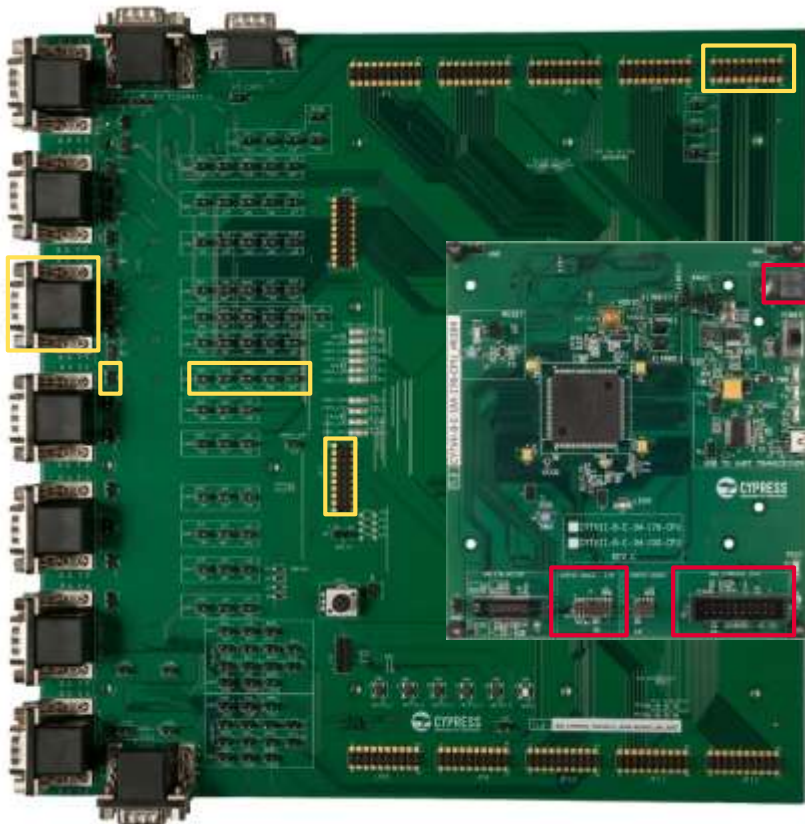
TVII copy the response and send back to tester

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- CXPI analyzer :

- If you have PX-10 CXPI analyzer, you can use `cspi_test.mps` for it.

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### > Legend:

- Red block for power, debug and USB (Mandatory)
- Yellow block for the example specific connections