› **Description**:
  − This example demonstrates usage of SCB-SPI to wake up the MCU.

› **Target Device**:
  − Traveo-II CYT2B6x devices

› **Hardware**:
  − CYTVII-B-E-1M-100-CPU REV 1.0
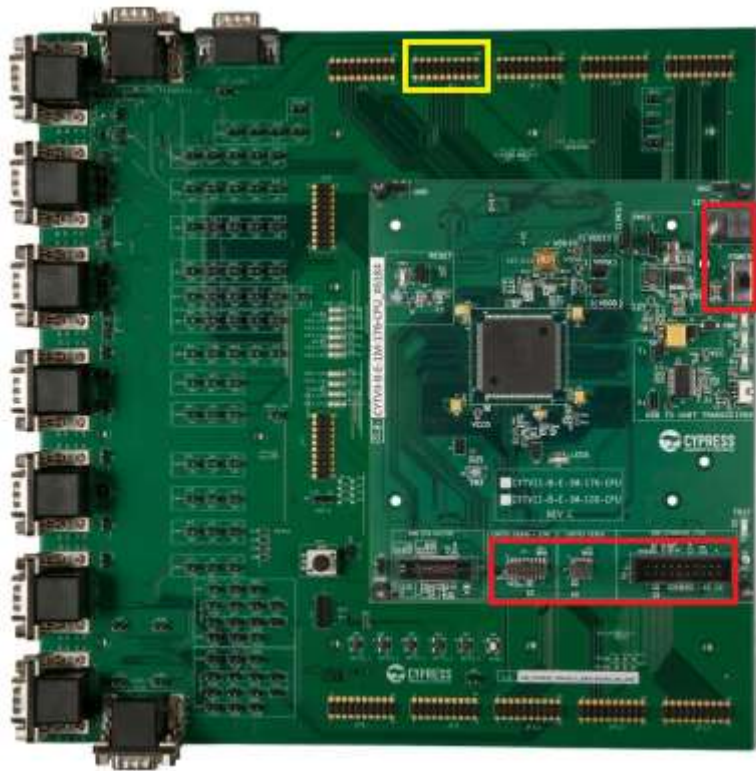 − TVII Base Board (BB)

› **Dependency:**
  – Both the examples to be copied to the main "src"
  – Aardvark to be configured as a Master
  – Aardvark MOSI/8 – JP2/10 on BB
  – Aardvark MISO/5 – JP2/9 on BB
  – Aardvark SCLK/7 – JP2/12 on BB
  – Aardvark SS/9 – JP2/13 on BB
  – Aardvark GND/1 to any GND on the CPU/BB board

› **Expectation**:
  – MCU is set in Slave Mode, Aardvark shall be set in Master mode and click start.
  – Both cores will be in DeepSleep mode, send a message from PC/Aardvark to wakeup the MCU.

SCB SPI: DeepSleep Wakeup

› **Legend**:

- Red block for power, debug and USB (Mandatory)
- Yellow block for the example specific connections