

CXPI: Polling – Master – NRZ Mode

› **Description:**

- This example demonstrates CXPI transmission in Polling Trigger method.
- TVII acts as a master node of CXPI network.

› **Target Device:**

- Traveo-II CYT2B9x devices

› **CPU Board:**

- CYTVII-B-E-1M-176-CPU Rev. C Board
- CYTVII-B-E-BB Rev.A Board

› **Dependency:**

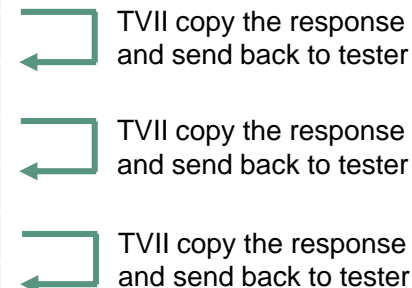
- CYTVII-B-E-1M-176-CPU board should be connected on CYTVII-B-E-BB board.
- Open J11, J12, J14 on the base board. Short J6, J9, J13 on the base board.
- Connect following pints on the base board
 - (RX) JP5.9 to J14.1, (TX) JP5.7 to J11.1, (CLK) JP5.6 to J12.2
- Connect CXPI connector (P1) on the base board and CXPI analyzer if available.

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› Expectation:

- Scheduled frames: TVII send PIDs according to below schedule

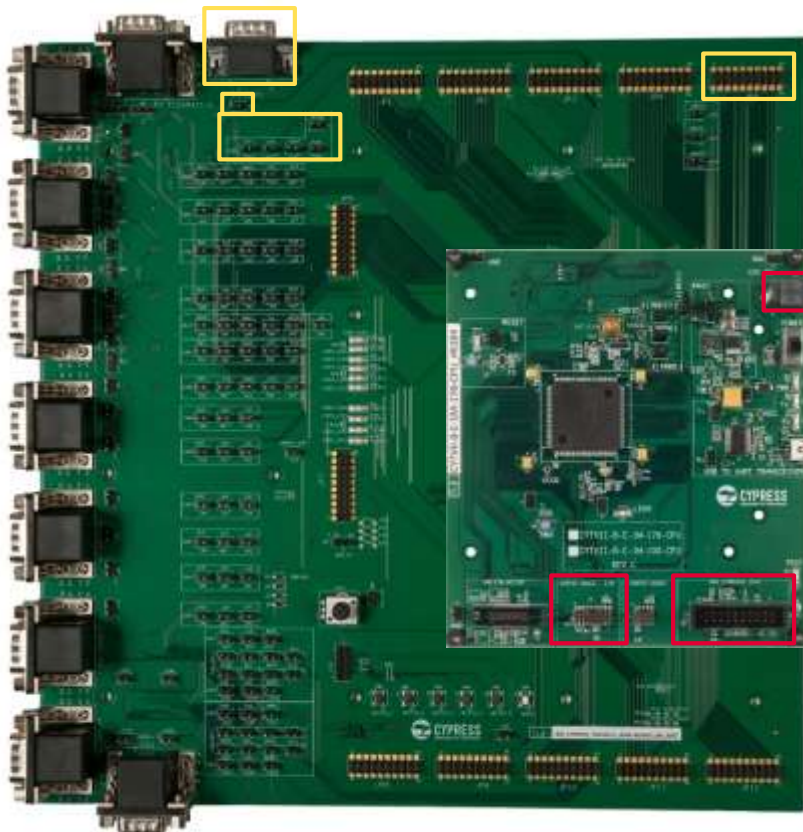
No.	PTYPE	ID	PID transmitter	Response transmitter	Frame length (byte)	Frame interval
1	no	0A	TVII	CXPI analyzer	8	100ms
2	no	4A	TVII	TVII	8	100ms
3	no	0F	TVII	CXPI analyzer	16	100ms
4	no	4F	TVII	TVII	16	100ms
5	yes	6D	CXPI analyzer	CXPI analyzer	8	100ms
6	yes	7D	CXPI analyzer	TVII	8	100ms



- CXPI analyzer :

- If you have PX-10 CXPI analyzer, you can use cxi_test.mps for it.

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> Legend:

- Red block for power, debug and USB (Mandatory)
- Yellow block for the example specific connections