› **Description**:
  - This example demonstrates LIN master configuration that will communicate with the LIN slave device.

› **Target Device**:
  - Traveo-II CYT2BLx devices

› **CPU Board**:
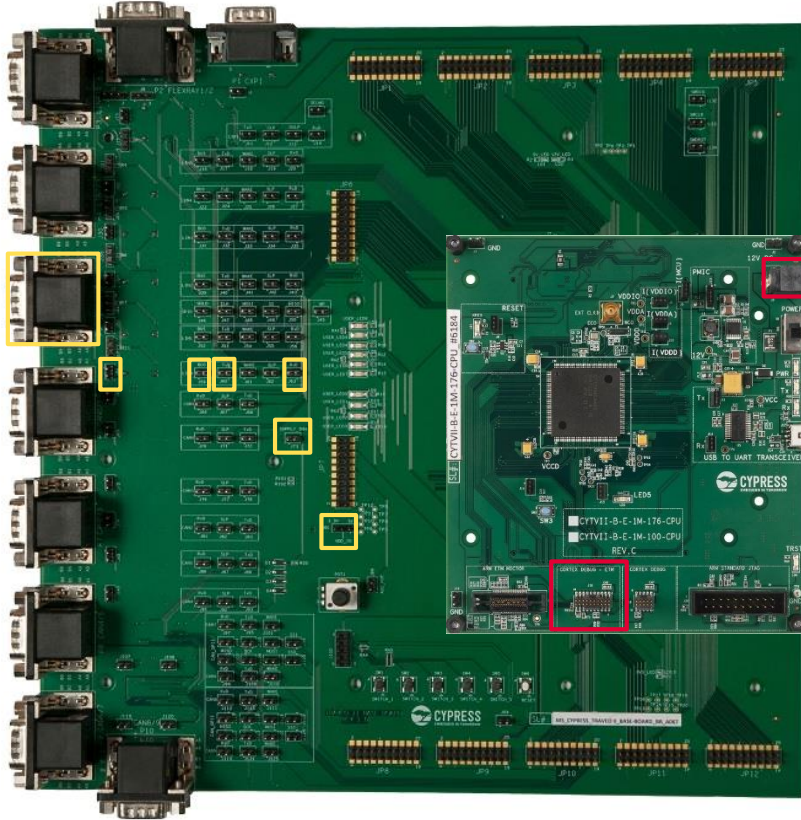 - CYTVII-B-E-1M-176-CPU Rev. C Board
  - CYTVII-B-E-BB Rev.A Board

› **Dependency:**
  – Jumper J80 to be short on position 1-2 on Base Board.
  – CYTVII-B-E-1M-176-CPU board should be connected on CYTVII-B-E-BB board.
  – Jumper J58, J59, J60, J63 and J73 should be closed on base board.
  – PLIN pro or CANoe should be installed on the system.

› **Expectation**:
  – Connect LIN0 connector (P5 upper one) on the base board and CANoe or other analyzer if available.
  – Import "LinConfig_ForMasterTest.cfg" and monitor using trace, statistics window.
  – Master will send "TX_RESPONSE", "RX_RESPONSE" and "TX_HEADER" to the slave device.
  – Master will respond to the "TX_HEADER", "TX_HEADER_TX_RESPONSE" and "TX_HEADER_RX_RESPONSE" from the slave device.

LIN: Master

› **Legend**:

– <mark>Red block</mark> for power, debug and USB (Mandatory)
– <mark>Yellow block</mark> for the example specific connections