SYSPM – Sleep – Wake-up from UART

› **Description**:
   – This example demonstrates how to enter Sleep mode and exit it through an UART interrupt

› **Target Device**:
   – Traveo-II CYT2Bx devices

› **CPU Board**:
   – CYTVII-B-E-176-CPU BOARD REV.C (REV_C)

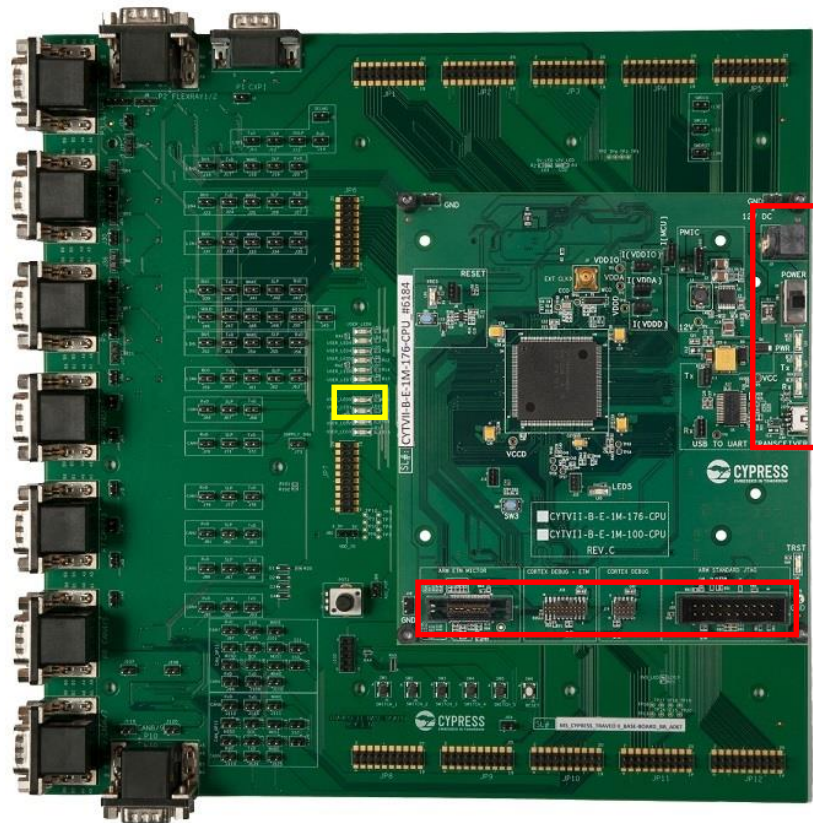# SYSPM – Sleep – Wake-up from UART

› **Dependency:**
  – Serial terminal (115200, 8N1)

› **Expectation**:
  – CM4 configures the UART and enters Sleep mode
  – User will then need to send characters via the serial terminal, the CPU will wake-up from UART RX interrupt and echo the received character through UART and go back to sleep
  – Additionally, one LED on the base board will be toggled on every wake-up

Legend:
- Red block for power, debug and USB (Mandatory)
- Yellow block for the example specific connections