# MPU

- **Description**:
  - This example setup ARM core MPU. Six regions will be set – “Background”, “Code flash”, “Work flash”, “SRAM”, “Peripheral register”, and “Arm system register” regions.
  - First, it sets the MPU so that the core can read from peripheral register region. And it tries reading which supposed to succeed.
  - Next, it sets the MPU so that the core “cannot” read from the peripheral register region. And it tries reading. It is supposed to cause hard-fault (bus-fault).

- **Target Device**:
  - Traveo-II CYT2Bx devices

- **CPU Board**:
  - CYTVII-B-E-1M-176-CPU Rev. C Board

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- **Dependency:**
  - None

- **Expectation**:
  - The CPU will set up ARM core MPU. And it will go to hard-fault handler by reading restricted memory intentionally.

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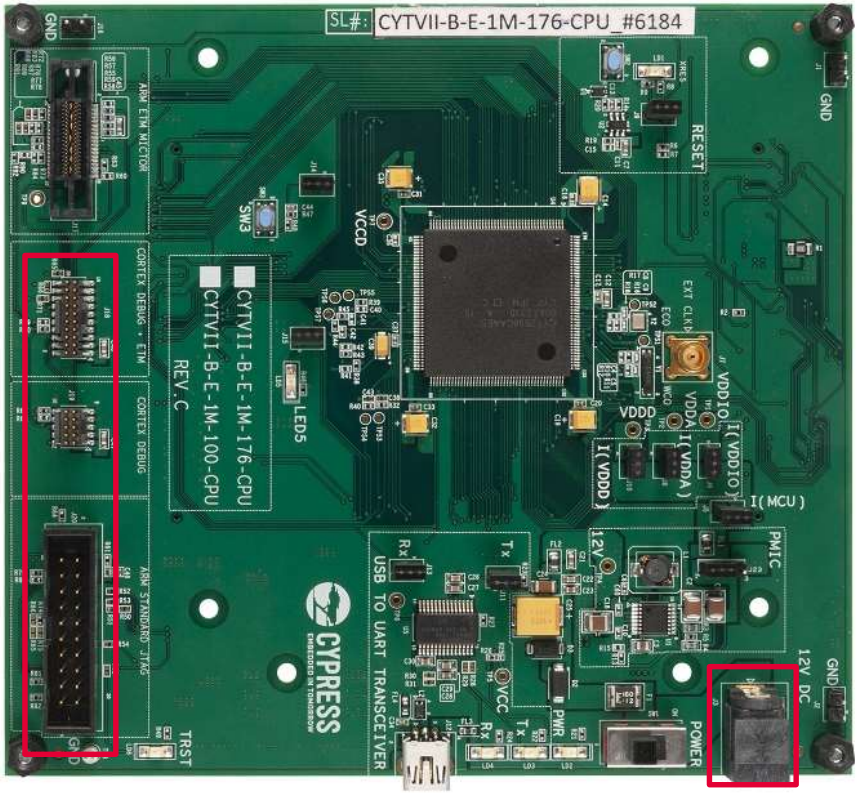


**Legend**:

- Red block for power, debug (Mandatory)