› **Description**:
  – This example is used to demonstrate Timer Test for TCPWM.
    1. Check the LED port, it will be blink at the period of 1[s].

› **Target Device**:
  – Traveo-II CYT2Bx devices

› **CPU Board**:
  – CYTVII-B-E-176-CPU BOARD REV.C (REV_C)
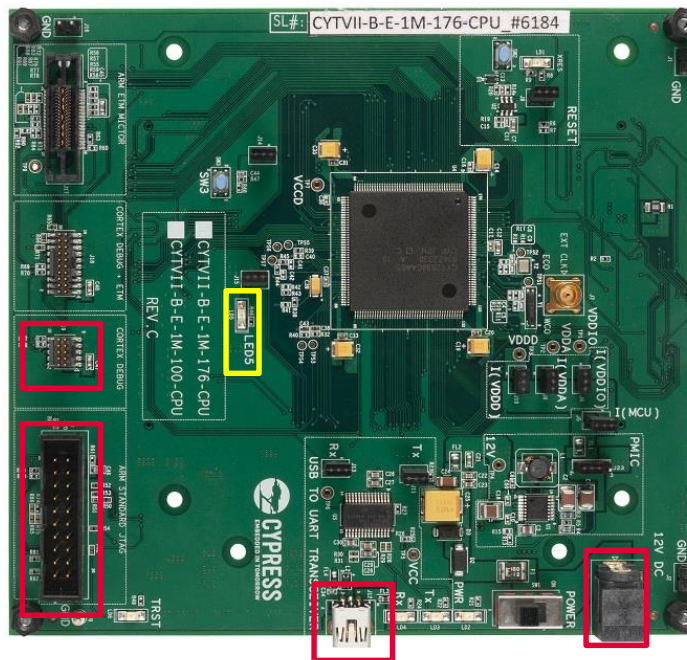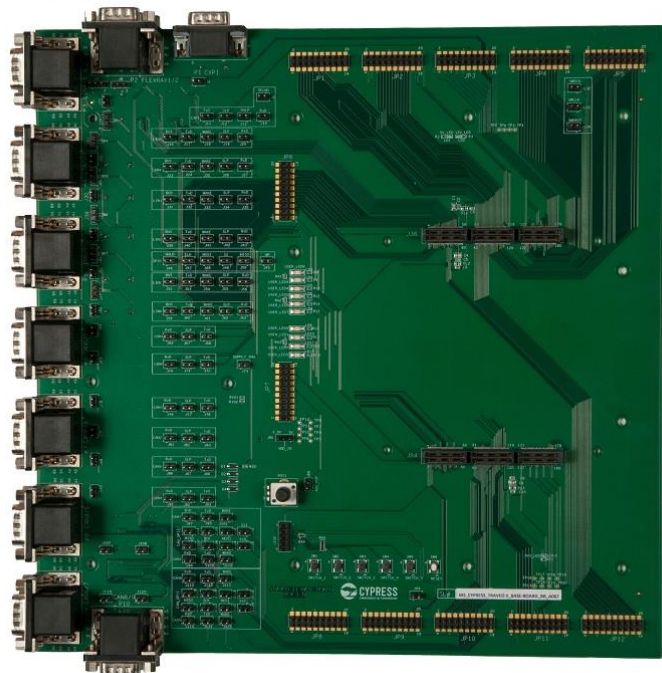
› **Dependency:**
  – Check the LED port:
      Port 8.2 – LED5 on CPU board.

› **Expectation**:
  – The LED port on CPU board is blinking at the period of 1 [s].

Trigger MUX: DMA-UART

› **Legend**:
  – Red block for power, debug and USB (Mandatory)
  – Yellow block for LED port.