› **Description**:
  – This example demonstrates the WDT warning interrupt functionality. As soon as the timer value overshoots the warn limit, interrupt is generated.

› **Target Device**:
  – Traveo-II CYT2Bx devices

› **CPU Board**:
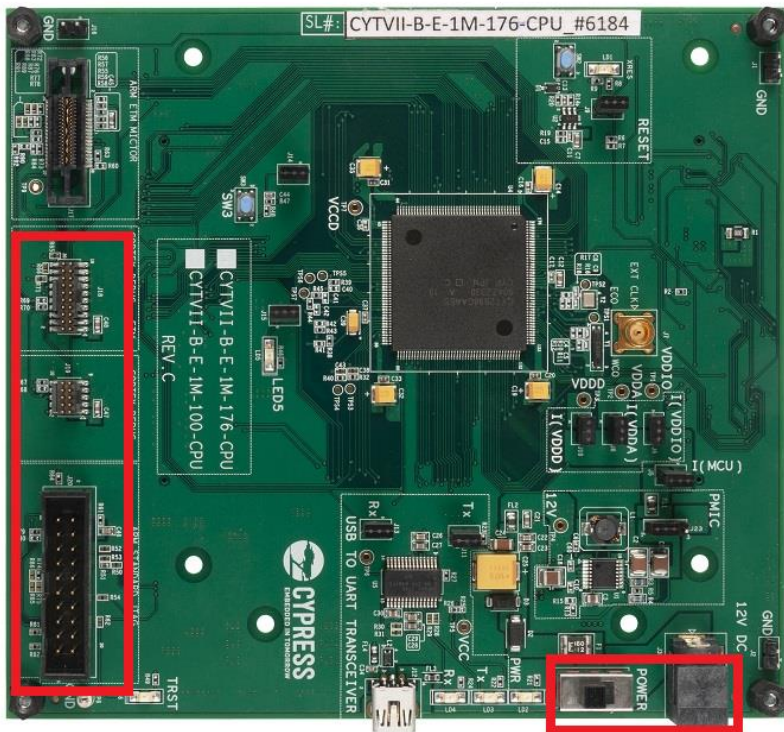 – CYTVII-B-E-176-CPU BOARD REV.C (REV_C)

› **Dependency:**
  – Any example could be copied to the main "src" based on the interested core

› **Expectation**:
  – Put a break point in the ISR "Wdt_Warn_IntrISR".
  – Run the code and observe for the warning interrupt occurrence.
  – LED gets inverted after the occurrence of the interrupt.

SYSWDT: Warning Interrupt

› **Legend**:

 – Red block for power, debug and USB (Mandatory)

 – Yellow block for the example specific connections