

SRAM: ECC – CM0

› **Description:**

- This example demonstrates the test on SRAM with ECC injection. Example has the feature of reporting of correctable and non-correctable ECC error.

› **Target Device:**

- Traveo-II CYT2Bx devices

› **CPU Board:**

- CYTVII-B-E-1M-176-CPU Rev. C Board

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› **Dependency:**

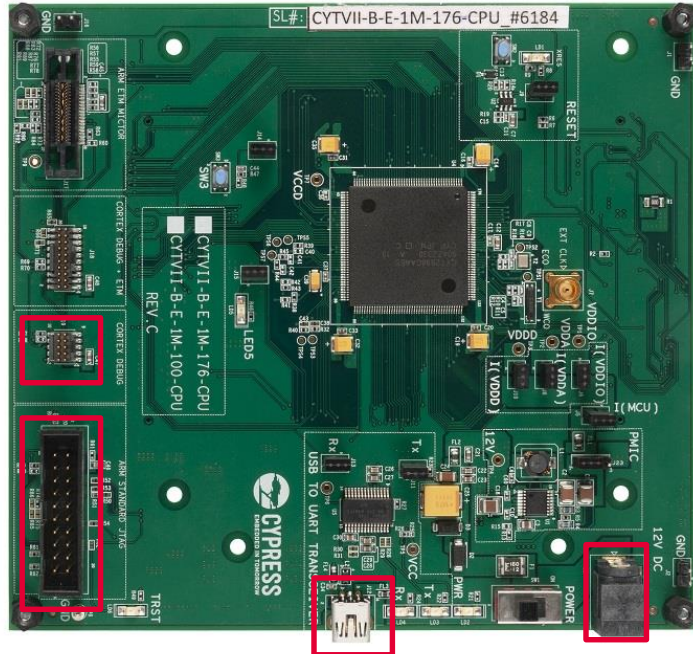
- Copy and paste main_cm0plus.c into main “src” folder
- Use only empty area from SRAM region.

› **Expectation:**

- Any error in SRAM ECC will generate the fault report.
- The value written in SRAM is checked on reading
 - With one-bit parity error (correctable ECC error)
 - With two-bit parity error (non-correctable ECC error)

Operation is okay when fault data will match with error and successful when LED starts blinking.

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> Legend:

- Red block for power, debug and USB (Mandatory)