SYSRESET

› **Description**:
  – This example demonstrates various reset types:
    1. Watchdog (WDT) reset
    2. Fault Structure reset
    3. Reset triggered by software
    4. Multi-Counter Watchdog (MCWDT) reset

› **Target Device**:
  – Traveo-II CYT2Bx devices

› **CPU Board**:
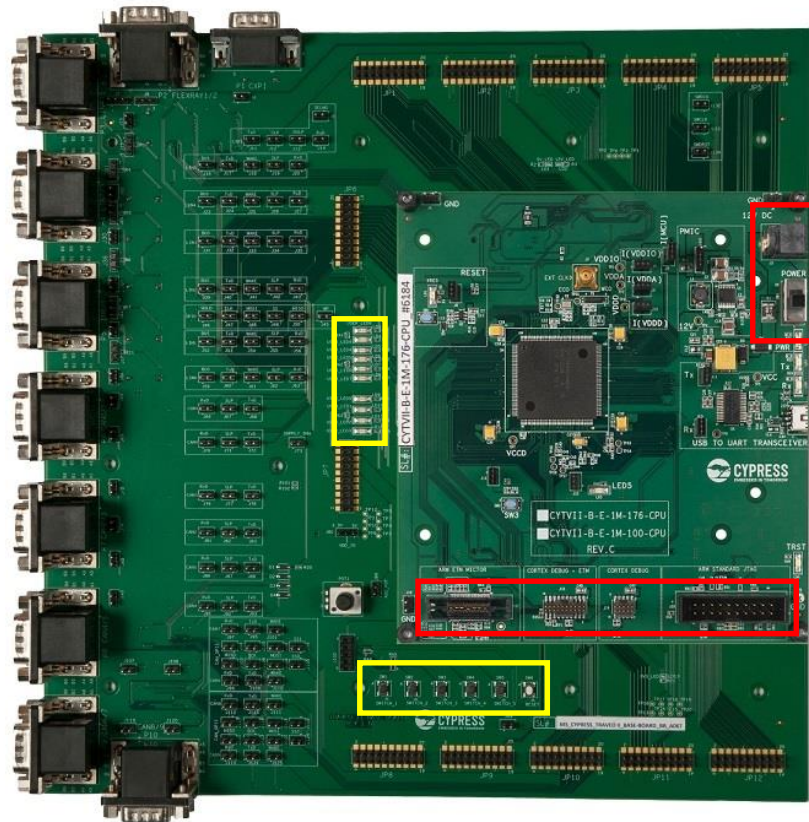 – CYTVII-B-E-176-CPU BOARD REV.C (REV_C)

SYSRESET

› **Dependency:**
  - All J102 jumpers (base board) are closed to ensure that buttons are connected
  - Disconnect debugger after programming and power-cycle the board

› **Expectation**:
  - Last reset cause is indicated through LEDs on the base board
  - Various reset types can be provoked by pressing buttons on the base board

SYSRESET

Legend:
- Red block for power and debug (Mandatory)
- Yellow block for the example specific connections