› **Description**:
  – This example is used to demonstrate Counter test of PWM
    1. If short Capture port and PWM line the interrupt will be occur.
    2. Get the Counter value.

› **Target Device**:
  – Traveo-II CYT2Bx devices

› **CPU Board**:
  – CYTVII-B-E-176-CPU BOARD REV.C (REV_C)

› **Dependency:**
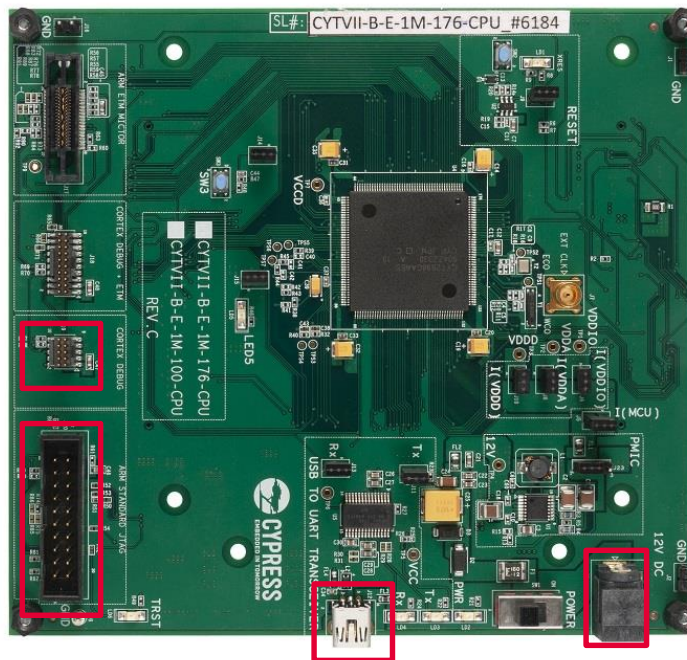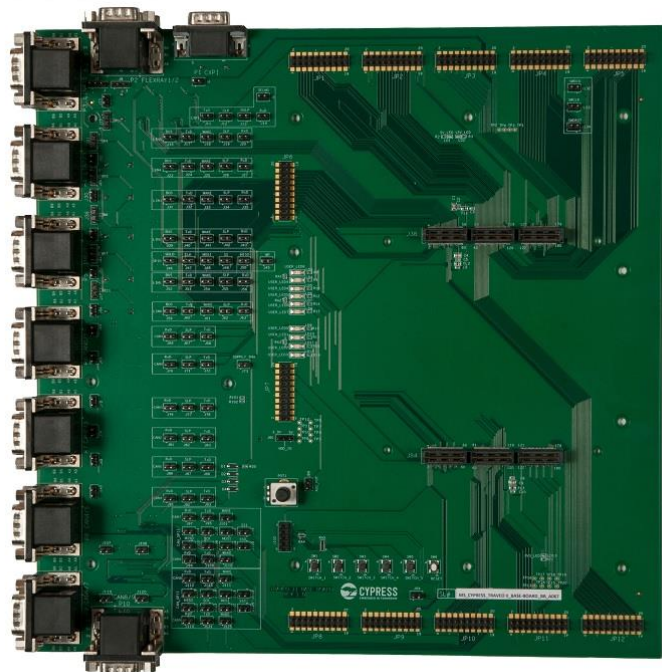 – Please short Port 6.1 and Port 6.3 on Base Board.
      Port 6.1 :BB_LIN3_TX
      Port 6.3: BB_CAN8_RXD

› **Expectation**:
  – Short Port6.1 and Port6.3, then Interrupt will be occur. Then get the counter value.

Trigger MUX: DMA-UART

› **Legend**:
  – Red block for power, debug and USB (Mandatory)