## TCPWM: Capture



#### Description:

- This example is used to demonstrate Capture test of PWM
  - 1. If short Capture port and PWM line the interrupt will be occur.
  - 2. Get the Compare value and calculate the value difference.

### Target Device:

Traveo-II CYT2Bx devices

#### CPU Board:

CYTVII-B-E-176-CPU BOARD REV.C (REV\_C)

## TCPWM: Capture



#### Dependency:

Please short Port 6.1 and Port 6.3.

Port 6.1 :BB\_LIN3\_TX

Port 6.3: BB\_CAN8\_RXD

#### Expectation:

 Short Port6.1 and Port6.3, then Interrupt will be occur. Then get the compare value and calculate the value difference.

# Trigger MUX: DMA-UART







## Legend:

Red block for power, debug and USB (Mandatory)