SCB SPI: Low Level Driver Slave

› **Description**:
  – This example demonstrates SCB-SPI in Slave Mode (Low level), MCU is set as slave and PC/Aardvark set in master Mode.

› **Target Device**:
  – Traveo-II CYT2B6x devices

› **Hardware**:
  – CYTVII-B-E-1M-100-CPU REV 1.0
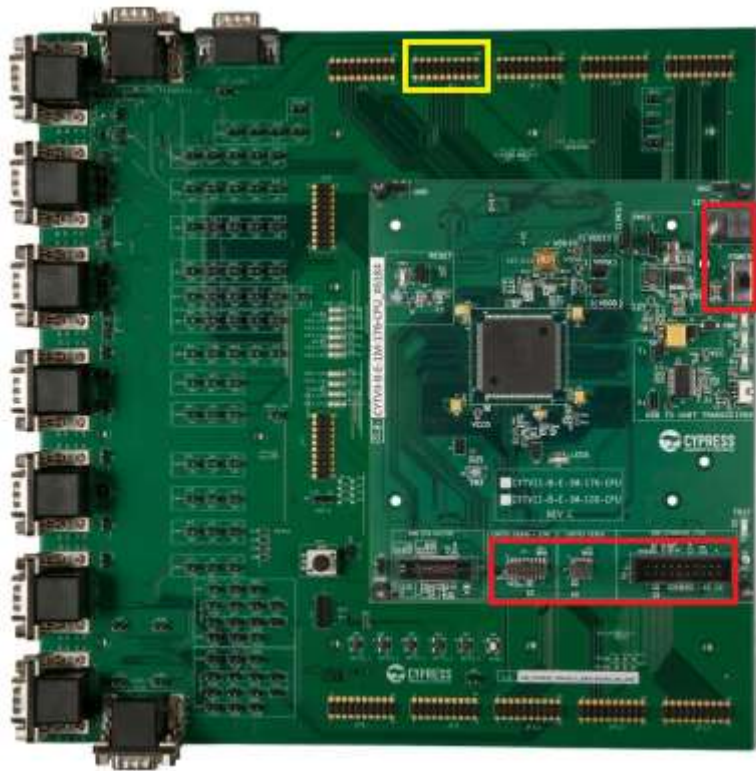 – TVII Base Board (BB)

› **Dependency:**
 – Any example could be copied to the main "src" based on the interested core
 – Aardvark to be configured as a master
 – Aardvark MOSI/8 – JP2/10 on BB
 – Aardvark MISO/5 – JP2/9 on BB
 – Aardvark SCLK/7 – JP2/12 on BB
 – Aardvark SS/9 – JP2/13 on BB
 – Aardvark GND/1 to any GND on the CPU/BB board

› **Expectation**:
 – MCU is set in Slave Mode, Aardvark shall be set in master mode and click start.
 – PC/Aardvark sends data to the MCU and receive back from MCU. Aardvark will show the sent and received data.

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› **Legend**:

– Red block for power, debug and USB (Mandatory)

– Yellow block for the example specific connections