SVC

- **Description**:
  - This example explains the usage of SVC (Supervisor call). The CPU core will attempt to access a restricted register for which a core in privileged mode can access.
    1. Sets the MPU to enable the cores in privileged mode only to access registers which control a GPIO leading to a LED on the CPU board
    2. Core gets itself into user mode
    3. Blinks the LED by SVC
    4. Gets itself into privileged mode by SVC
    5. Tries to blink the LED by directly accessing the registers
    6. Gets into user mode again
    7. Tries blinking the LED by directly accessing the registers, and results in hard-fault

- **Target Device**:
  - Traveo-II CYT2Bx devices

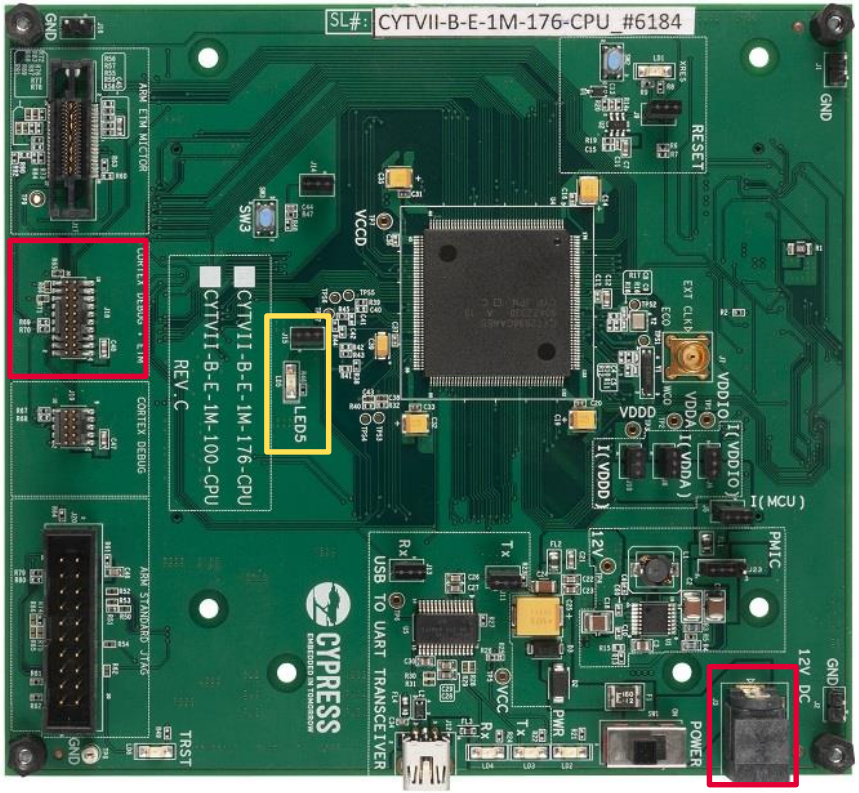
- **CPU Board**:
  - CYTVII-B-E-1M-176-CPU Rev. C Board

SVC

- **Dependency:**
  - None

- **Expectation**:
  - The CPU will blink the LED on the board twice, and lastly gets into hard-fault handler by accessing restricted register in user mode. Recommended to use debugger to check functionality in step-by-step manner.

SVC



Legend:

- Red block for power, debug and USB (Mandatory)
- Yellow block for the example specific connections