› **Description**:
  - This example demonstrates how to enter Sleep mode and exit it through an GPIO interrupt

› **Target Device**:
  - Traveo-II CYT2Bx devices

› **CPU Board**:
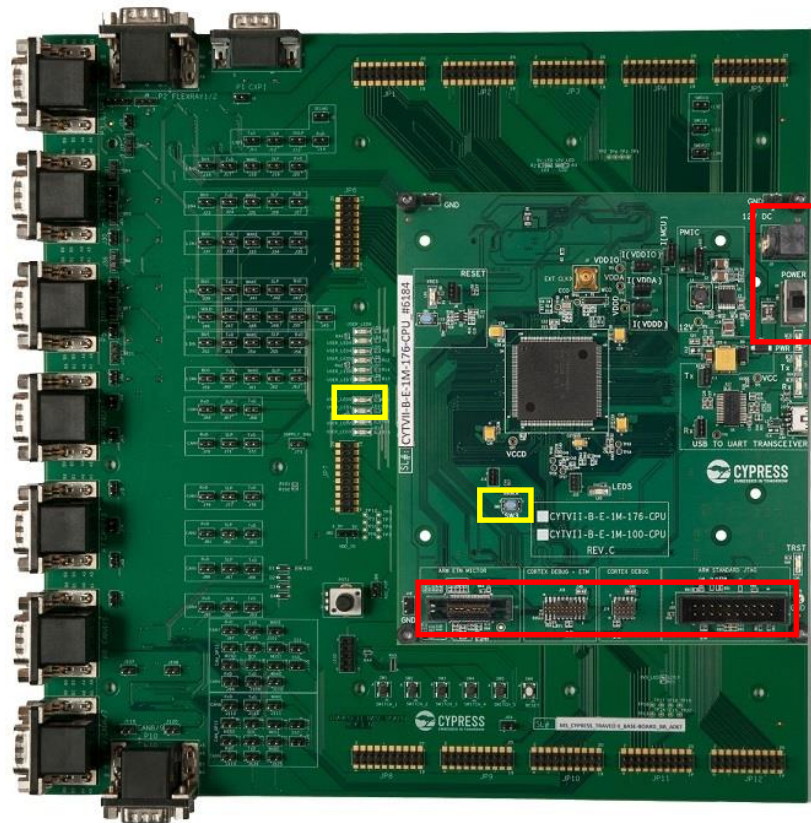 - CYTVII-B-E-176-CPU BOARD REV.C (REV_C)

› **Dependency:**
 – None

› **Expectation**:
 – CM4 enters Sleep mode
 – CM4 wakes up when the button on the CPU is pressed, toggles one LED on the base board and then enters Sleep again

Legend:
- Red block for power and debug (Mandatory)
- Yellow block for the example specific connections