Subject Name: Digital Electronics

Assignment-3

Chapter-3 Combinational Logic Circuit:

No.	Questions
1.	Explain Half Adder in detail.
2.	Explain Full Adder in detail.
3.	Explain Half Subtractor in detail.
4.	Explain Full Subtractor in detail.
5.	Explain BCD to Excess-3 code conversion.
6.	Short note on BCD adder.
7.	Draw circuit of 4-bit comparator and explain.
8.	Give comparisons between encoder and decoder.
9.	Explain 4 x 1 line Multiplexer in detail.
10.	Draw a 2 to 4 line decoder and explain in detail.
11.	Comparison between multiplexer and de-multiplexer.
12.	Explain Binary parallel adder.