

LDRP-ITR, Gandhinagar/VSITR, KADI

Semester: 3 (CE/IT/CSE) All division

Subject Name: Digital Electronics

Assignment-5

Chapter 5: Introduction to State Machines

No.	Questions
1.	What do you mean by the term 'state table'? What do you mean by the term 'state diagram'?
2.	What do you mean by the Moore model and the Melay model of the state diagram?
3.	Draw Mealy model for 1. D flip flop 2. S-R flip flop 3. J-K flip flop Also explain the state diagram and the state table for the same.
4.	Draw Moore model for 1. D flip flop 2. S-R flip flop 3. J-K flip flop Also explain the state diagram and the state table for the same.