

LDRP-ITR, Gandhinagar/VSITR, KADI

Semester: 3 (CE/IT/CSE) All division

Subject Name: Digital Electronics

Assignment-4

Chapter 4: Flip flops and sequential logic and circuits

No.	Questions
1.	Explain R-S Flip Flop in detail.
2.	Explain D Flip Flop in detail.
3.	Explain J-K Flip Flop in detail
4.	Explain Master Slave Flip Flop in detail.
5.	Explain Excitation tables for flip-flops in detail
6.	Comparison between Combinational logic and Sequential logic circuits
7.	What is Register? Explain 4- bit register in detail
8.	Draw a 4- bit register with Parallel load and explain
9.	Write a short note on Shift Registers.
10.	What is a counter? Explain 4-bit binary ripple counter in detail.
11.	Define a synchronous counter and explain 4-bit synchronous counter in detail.
12.	Explain BCD ripple counter in detail.
13.	Draw 4-bit up-down binary counter and explain.
14.	Write a short note on Johnson Counter.