

土龙 / Tulong: A Processor for Research into Secure Enclaves

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Goals

-Our user story is to build a processor with DAWG security, adding mitigations against other known security vulnerabilities of secure enclaves, provides an platform for testing mitigations.

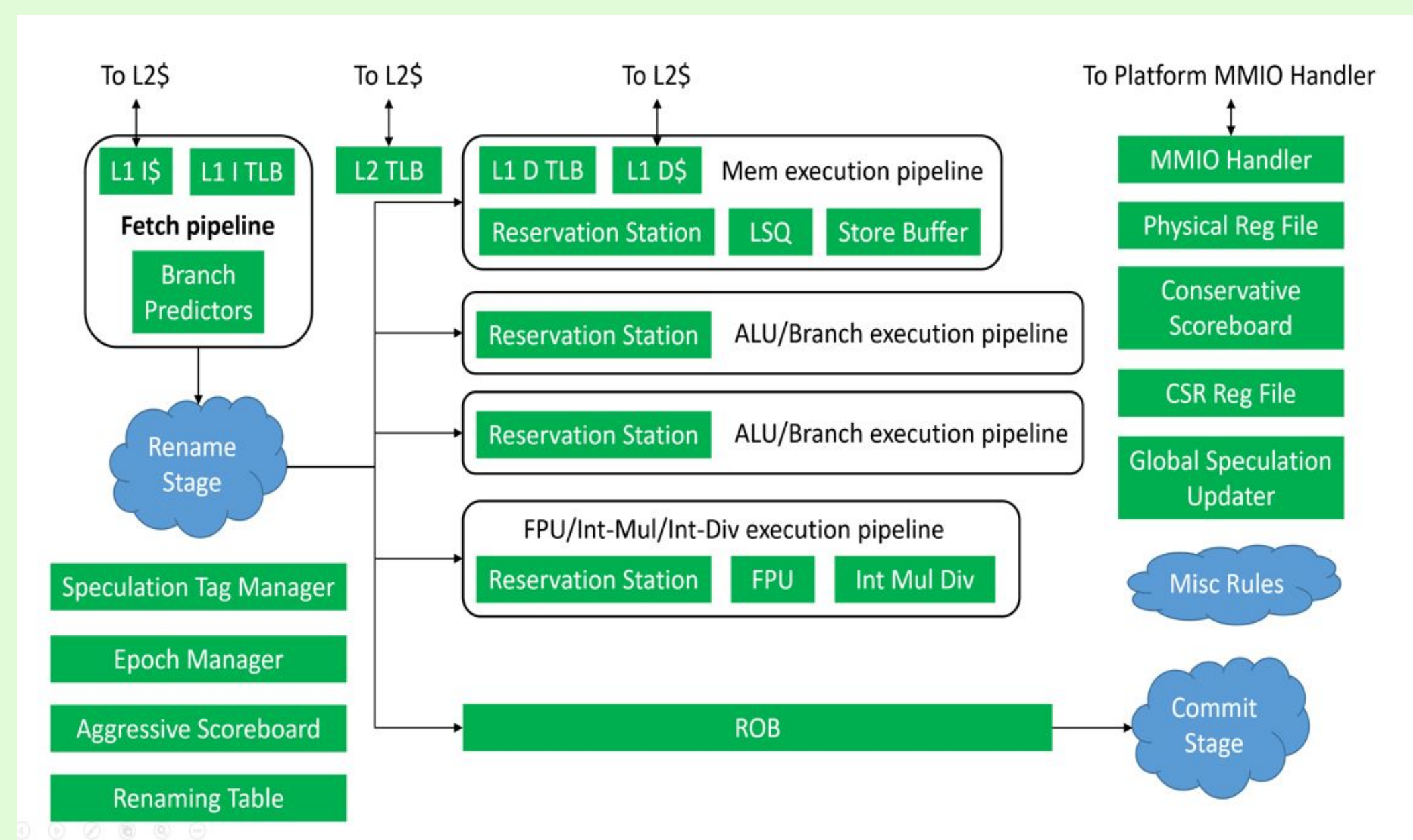
-Our MVP is MI6 processor with DAWG and other vulnerability mitigations.

Methods

- Chose to use regular Verilog, instead of Bluespec
- Meant building OoO processor ourselves
- Got pipelined (in order) code from ASCS Lab
- Built Reorder Buffer
- Need to build Rename Buffer, Reservation Station, and other pieces as future work

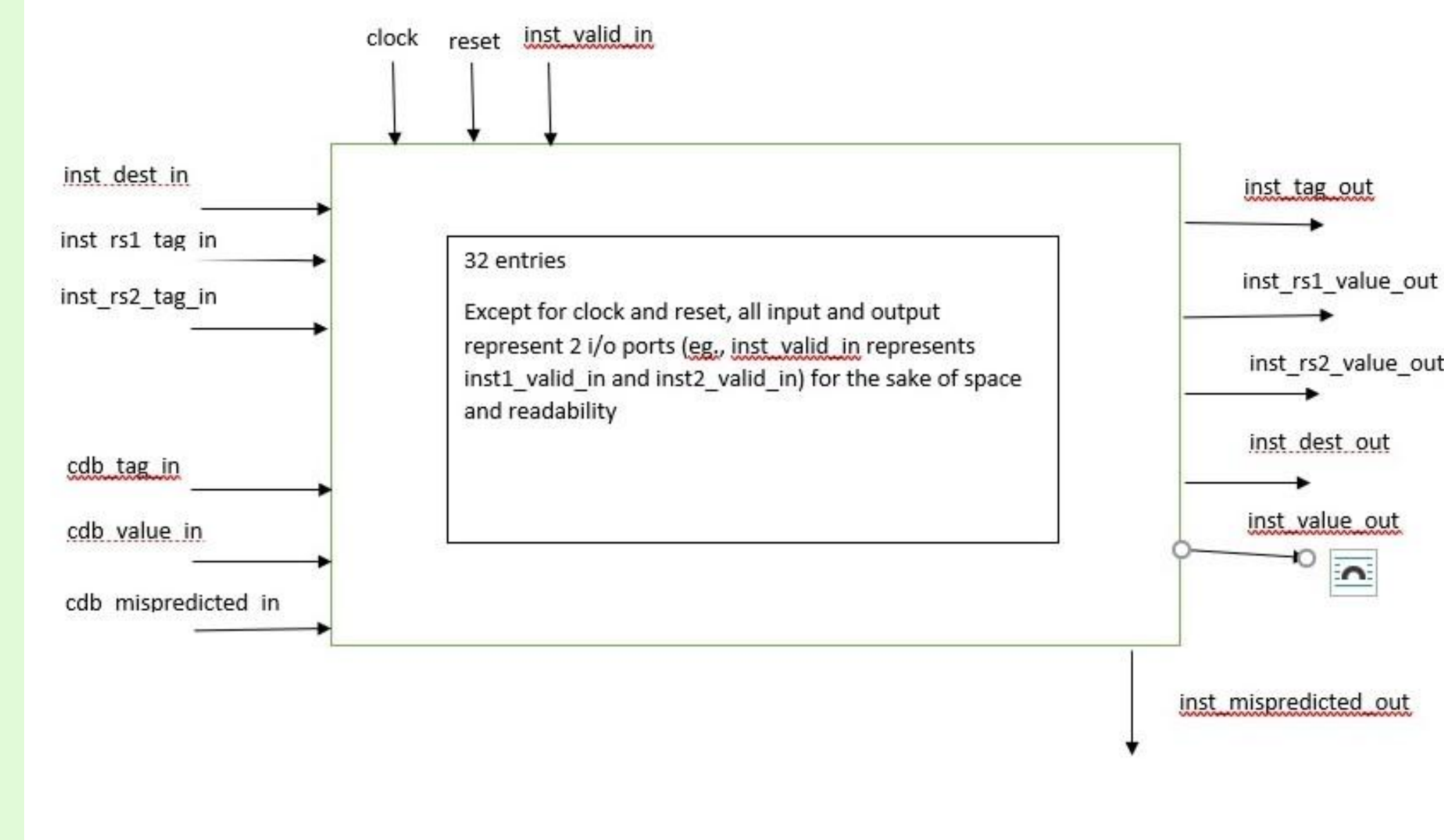
Conclusions & Lessons Learned

- Project has merit, but we made our lives very difficult
- When using open source code as a starting point, make sure all aspects of the source are open source
- Ambition is good, but realism is wise
- Ask for help, early and often



https://github.com/sizhuo-zhang/RiscyOO_design_doc/blob/master/fig/core.pdf

Reorder Buffer



Project code and notes can be found at:

<https://github.com/MargotBauman/601-Main-Project-Secure-Enclaves/tree/master>

Acknowledgements

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References can be found at:

<https://github.com/MargotBauman/601-Main-Project-Secure-Enclaves/blob/master/References>