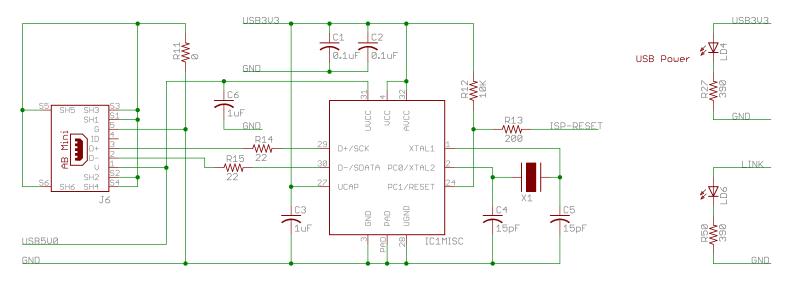
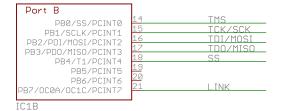


Atmel AT90USB162 USB Interface





PORT C

PC2/PCINT11

PC4/PCINT10
PC5/OC1B/PCINT9
PC6/OC1A/PCINT8
PC7/INT4/ICP1/CLK0

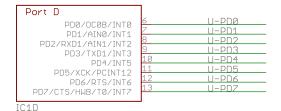
PC1C

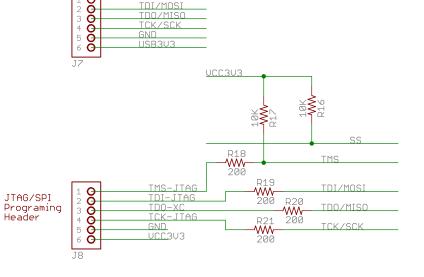
DPC2

U-PC2

U-PC3
23
U-PC6
23
U-PC6
23
U-PC7

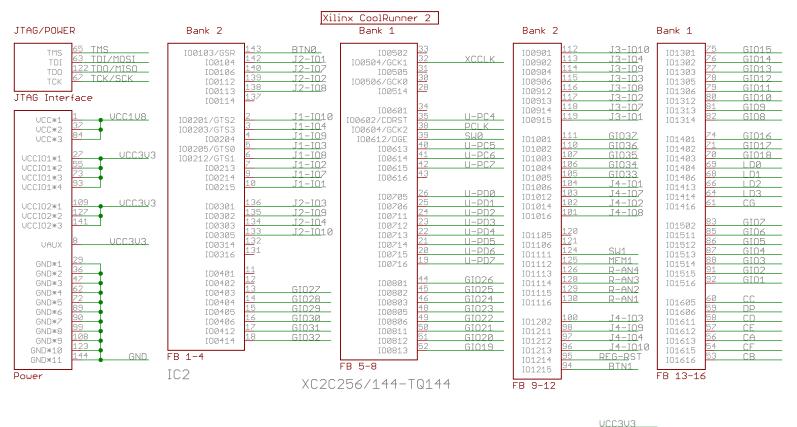
U-PC7

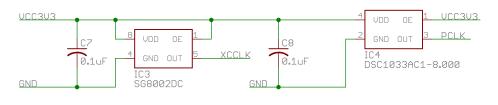


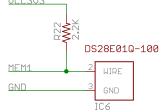


ISP-RESET

CR-II_rev3, Xilinx Inc				
Copyright 2011	Engineer: CC			
SHEET: USB and Programing	Author: GMA			
TITLE: CR-II		Rev: F.0		
Doc#: 500-146	Date: 7/27/2011	Sheet: 2/4		

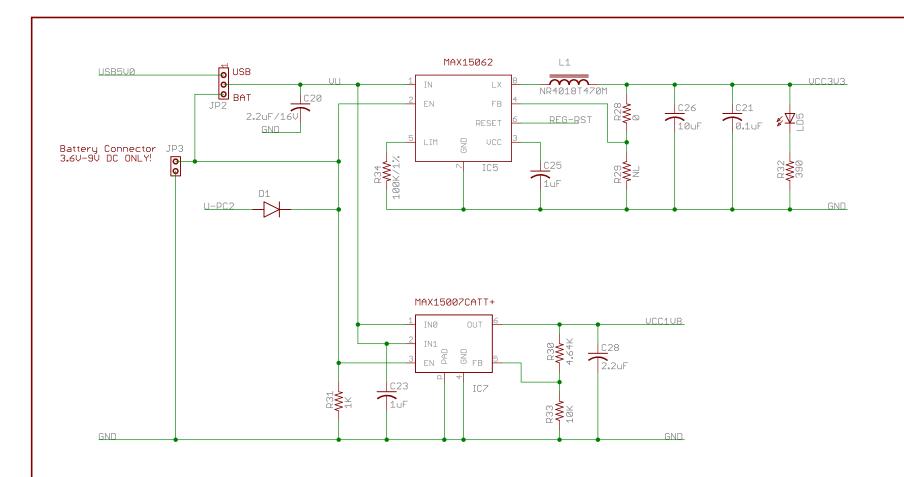






<u> </u>			CPLD	Power	Decoup	ling					,
NCC3N3								•	•	1	
<u>ncc3n3</u>				•	•	•	1				
UCC1U8	•	•	1								
	<u> </u>	<u>C</u> 10	C11 _	C12	C13	C14	C15 _	C16	C17	C18	<u>C</u> 19
	0.1uF	0.1uF	0.1uF	0.1uF	0.1uF	0.1uF	0.1uF	0.1uF	0.1uF	0.1uF	0.1uF
GND											

CR-II_rev3,	Xilinx Inc.	
Copyright 2011	Engineer: CC	
SHEET: Coolrunner 2 and d	Author: GMA	
TITLE: CR-II	Rev: F.0	
Doc#: 500-146	Date: 7/27/2011	Sheet: 3/4



CR-II_rev3, Xilinx Inc					
Copyright 2011	Engineer: CC				
SHEET: Regulator and Curre	Author: GMA				
TITLE: CR-II	Rev: F.0				
Doc#: 500-146	Date: 7/27/2011	Sheet: 4/4			