深圳市全动电子技术有限公司

ShenZhen QDtech electronic technology Co.,LTD.

PRODUCT : LCD MODULE
MODEL NO. : TFT1901
SUPPLIER : QDtech
DATE :September 20.2022

SPECIFICATION

Revion:1.0

TFT1901

Revised History

Part Number	Revision	Revision Content	Revised on
TFT1901	1.0	First Release	2022-09-20

Table of Contents

1 General Description

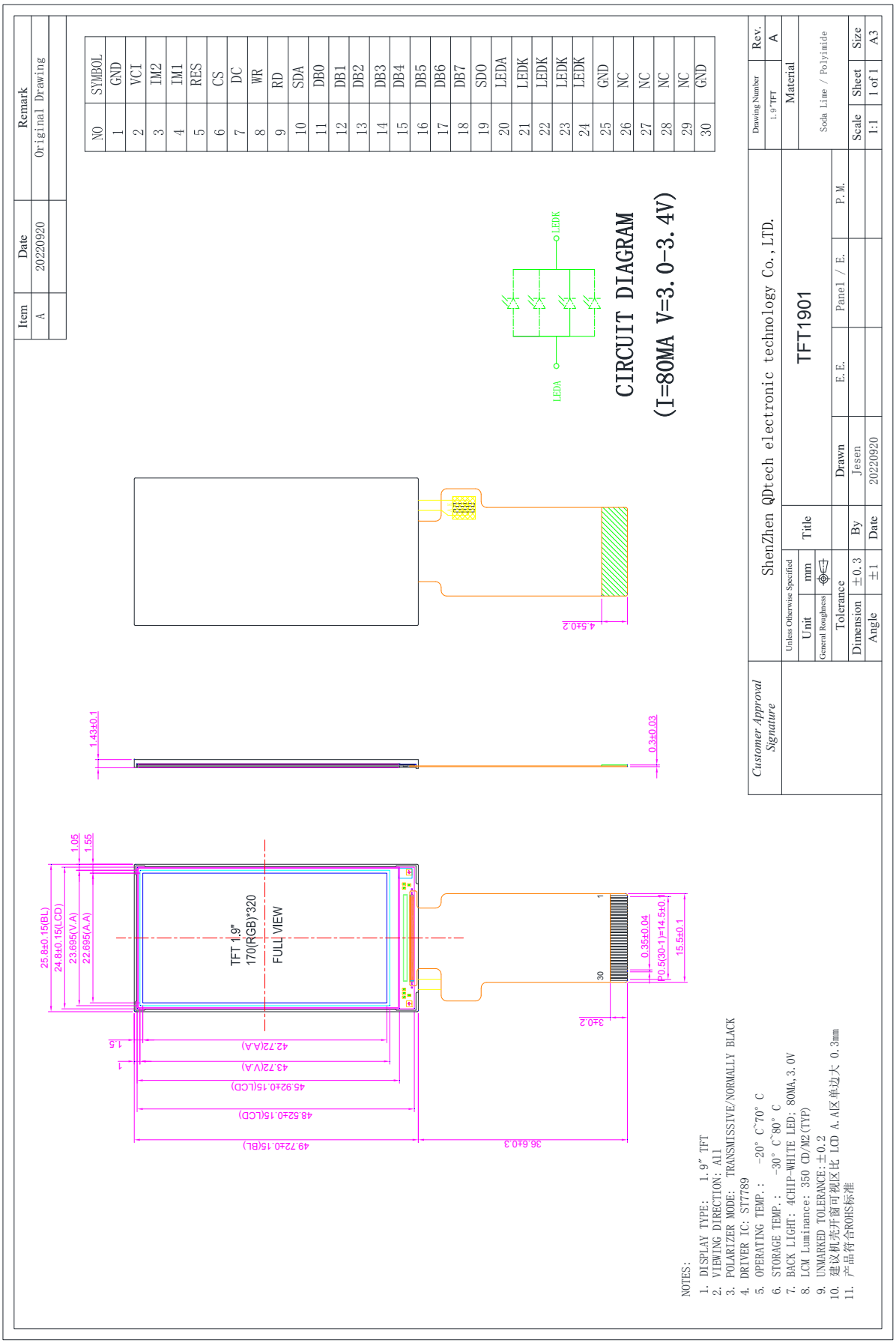
This display module is a transmissive type color active matrix TFT(Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This module is composed of a TFT LCD module, a driver circuit, and a back-light unit.The resolution of a 1.9" contains 170(RGB)X320 dots and can display up to 262k colors.

2 Module Parameter

Features	Details	Unit
Display Size(Diagonal)	1.9(Diagonal)	inch
LCD type	α-Si TFT	-
Display Mode	IPS / Transmissive / Normally Black	-
Resolution	170(H)RGB × 320(V)	-
View Direction	All View	Best image
Module Outline	25.8(H) × 49.72(V) × 1.43(D) (Note 1)	mm
Dot pitch	0.1335(H) × 0.1335(V)	mm
Luminance	350(min)	cd/m2
Backlight	4 White LED	-
Active Area	22.695 (H) × 42.72(V)	mm
Color arrangement	RGB Vtertical stripe	-
Display Colors	262K	-
Interface	4-SPI/8bit 8080 MCU	-
Driver IC	ST7789	-
Operating Temperature	-20～70	ºC
Storage Temperature	-30～80	ºC
Weight	TBD	g

Note 1: Excluding hooks, posts , FPC/FPC tail etc.

3 Mechanical Drawings

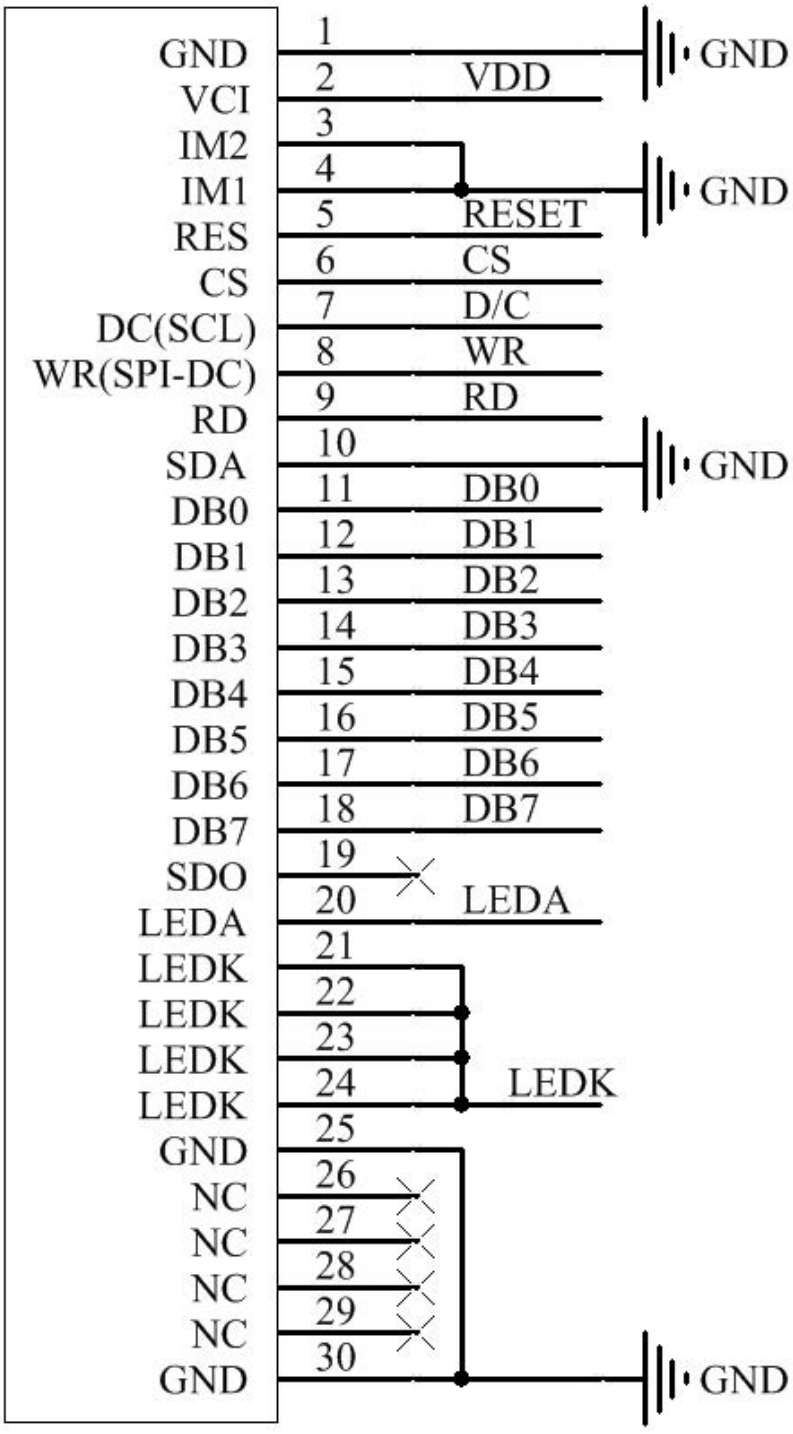


4 Module Interface

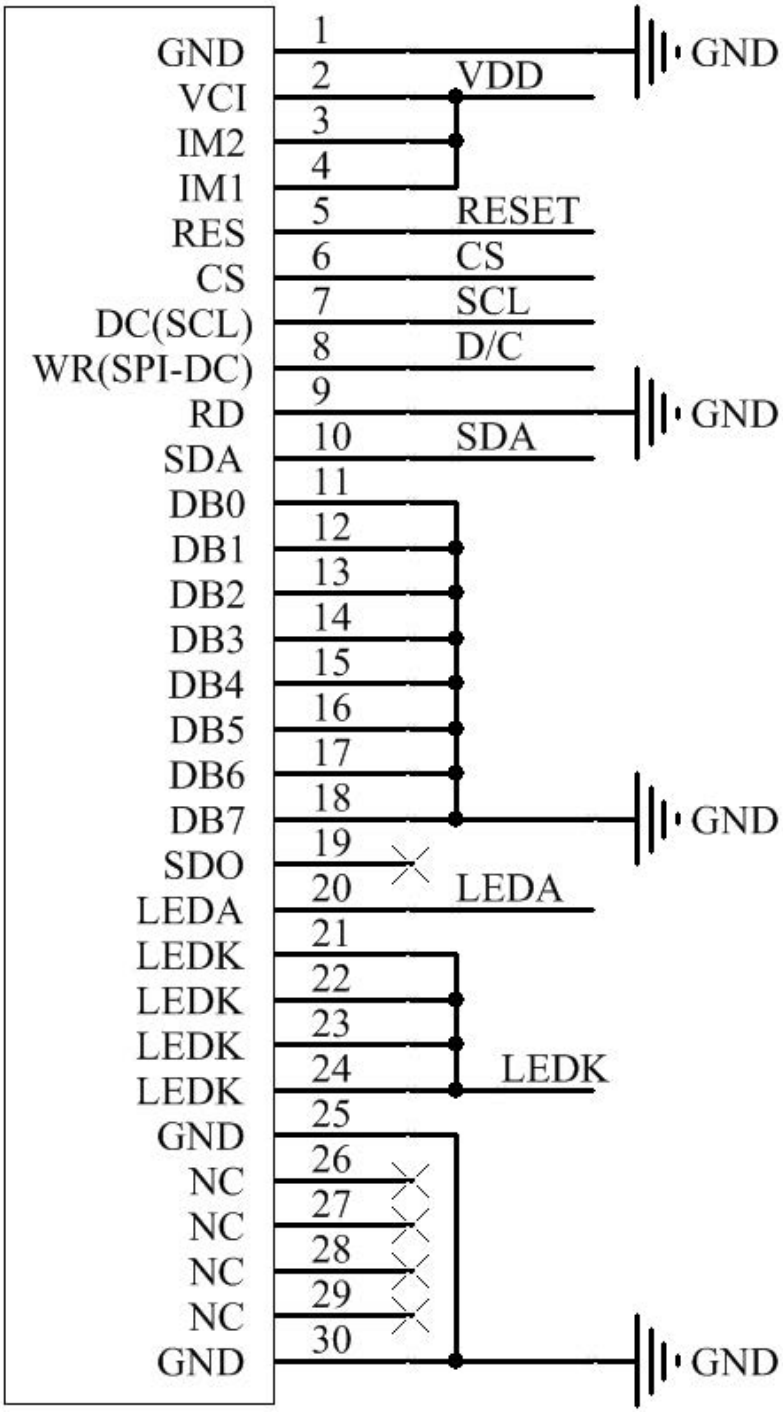
NO	SYMBOL	FUNCTION
1	GND	Power Ground
2	VCI	Power Supply for I/O system, VDD=1.65V~3.3V.
3	IM2	when IM1=0, IM2=0，8080-8bit parallel; when IM1=1, IM2=1，4-line SPI serial.
4	IM1	
5	RESET	This signal will reset the device and it must be applied to properly initialize the chip. Signal is active low.
6	CS	Chip selection pin; Low enable, High disable.
7	D/C	When connecting to an 8080-series 8bit parallel, this pin receives the data/command selection pin .This pin is used to be serial interface clock in 4- line serial interface.
8	WR	When connecting to an 8080-series 8bit parallel, this pin receives the write signal.Display data/command selection pin in 4-line serial interface.
9	RD	When connecting to an 8080-series 8bit parallel, this pin receives the Read signal. Read operation is initiated when this pin is pulled LOW and the chip is selected.When serial interface is selected, this pin must be connected to Ground.
10	SDA	SPI interface input/output pin. The data is latched on the rising edge of the SCL signal. if not use, please fix this pin at VDDI or DGND level.
11-18	D0-D7	MCU parallel interface data bus.
19	SDO	SPI interface output pin. the data is output on the falling edge of the SCL signal if not use, let this pin open.
20	LEDA	LED Anode
21	LEDK	LED Cathode
22	LEDK	LED Cathode
23	LEDK	LED Cathode
24	LEDK	LED Cathode
25	GND	Power Ground
26	NC	Let this pin open.
27	NC	Let this pin open.
28	NC	Let this pin open.
29	NC	Let this pin open.
30	GND	Power Ground

5 Application Circuit

8bit 8080

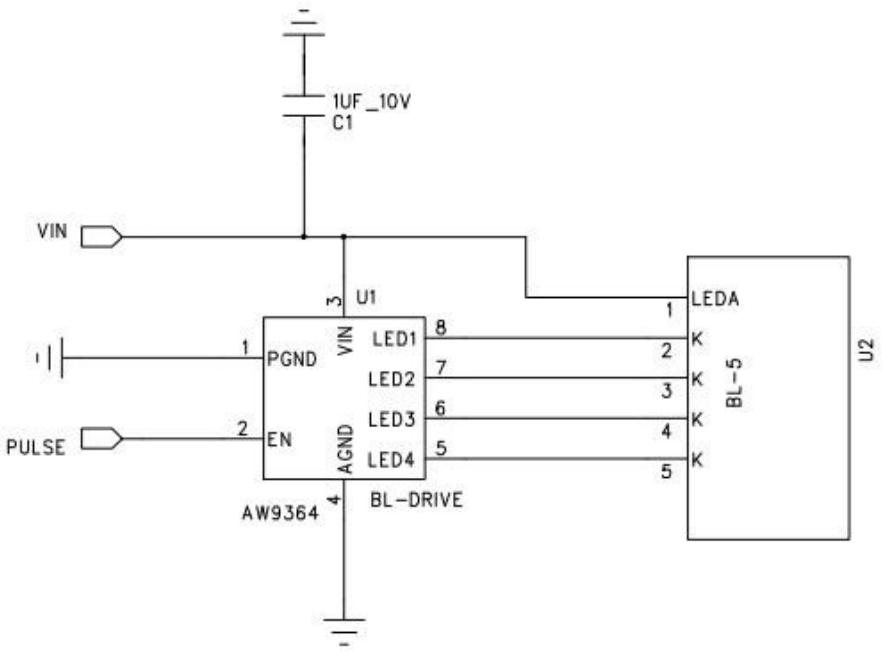


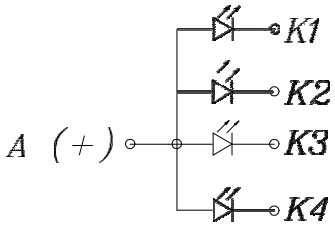
4-SPI


GND
VCI
IM2
IM1
RES
CS
DC(SCL)
WR(SPI-DC)
RD
SDA
DB0
DB1
DB2
DB3
DB4
DB5
DB6
DB7
SDO
LEDA
LEDK
LEDK
LEDK
LEDK
GND
NC
NC
NC
NC
GND
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
VDD
RESET
CS
SCL
D/C
SDA
LEDA
LEDK
GND
GND
GND
GND

Backlight recommended circuit

Motherboard driver backlight is need constant current circuit, if the rated voltage screen after light brightness difference.Current and power consumption of the machine are inconsistent, so recommend a backlight driving circuit is best rated current.It is recommended to use IC (AW9364). The reference circuit is as follows：





Note: constant current circuit for every LED,and though LED lamp current is less than 20mA.Recommand between 15mA and 20 mA for every LED.

6 Absolute Maximum Ratings

VSS=0V, Ta=25ºC

Item		Symbol	Min.	Max.	Unit
Supply Voltage	Power supply	VDD	-0.3	+4.6	V
	Analog	-	-	-	V
	IO	IOVDD	-0.3	+4.6	V
Input Voltage		Vi	-0.3	IOVDD+0.3	V
Storage temperature		T_{stg}	-30	+80	ºC
Operating temperature		T_{op}	-20	+70	ºC
Storage humidity		H_{stg}	10	Note 1	%RH
Operating humidity		H_{op}	10	Note 1	%RH

Note 1: 90%RH max, If Ta is below 50ºC; 60%RH max, If Ta is over 60ºC.

7 Electrical Specification

DC Characteristics

Item		Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	Power supply	VDD	2.4	2.8	3.3	V
	Analog	VCI	2.4	2.8	3.3	V
	IO	IOVDD	1.65	1.8/2.8	3.3	V
Logic Low input voltage		V_{IL}	-0.3IOVDD	-	0.3IOVDD	V
Logic High input voltage		V_{IH}	0.7IOVDD	-	IOVDD	V
Logic Low output voltage		V_{OL}	-	-	0.2IOVDD	V
Logic High output voltage		V_{OH}	0.8IOVDD	-	-	V
Current Consumption	Normal display	Ivdd	-	50	-	mA
	Standby mode	Ivdd	-	20	-	uA
Frame Frequency		f_{FR}	-	60	-	Hz

8 AC Characteristics

Reset timing and interface timing:

Please refer to IC datasheet.

9 Command Table

Please refer to IC datasheet.

10 Recommended Setting and Initialization Flow for Reference

Please refer to attached file.

11 Optical Specifications

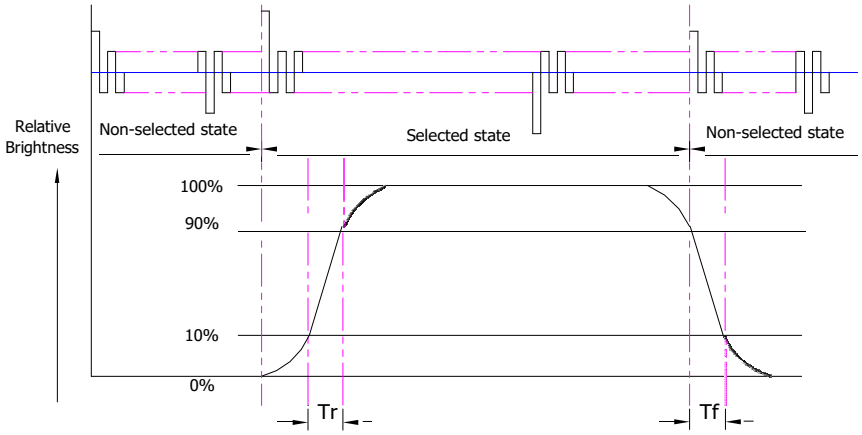
11.1 Optical Specifications

Ta=25ºC, VDD=2.8V, TN LC+ Polarizer

	Item		Symbol	Condition	Specification			Unit
					Min.	Typ.	Max.	
Backlight On (Transmissive Mode)	Luminance on surface(I_f=20mA)		*Lv*	Normally viewing angle $\theta_X = \theta_Y = 0^o$	600	650	-	cd/m²
	Contrast ratio		*CR*		640	800	-	-
	Response time		T_R		-	10	20	ms
			T_F	-	-	20	30	
	Chromaticity Transmissive	Red	X_R	-	0.622	0.624	0.626	-
			Y_R		0.327	0.329	0.331	-
		Green	X_G		0.286	0.288	0.290	-
			Y_G		0.520	0.522	0.524	-
		Blue	X_B		0.134	0.136	0.138	-
			Y_B		0.135	0.137	0.139	-
		White	X_W		0.300	0.302	0.304	-
			Y_W		0.323	0.325	0.327	-
	Viewing Angle	Horizontal	θ_{X+}	Center CR≥10	-	80	-	Deg.
			θ_{X-}		-	80	-	
		Vertical	θ_{Y+}		-	80	-	
			θ_{Y-}		-	80	-	
	NTSC Ratio(Gamut)		-	-	-	60	-	%

11.2 Definition of Response Time

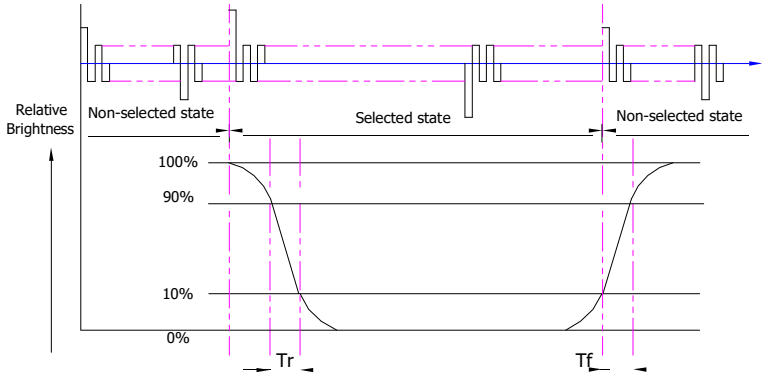
11.2.1 Normally Black Type (Negative)



Tr is the time it takes to change form non-selected state with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

11.2.2 Normally White Type (Positive)



Tr is the time it takes to change form non-selected state with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

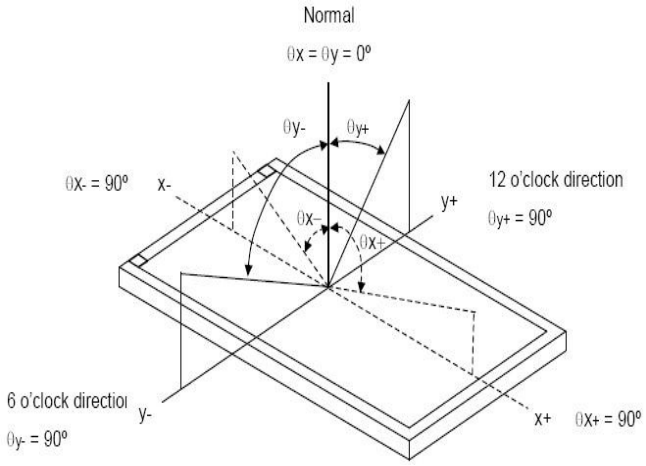
11.3 Definition of Contrast Ratio

Contrast is measured perpendicular to display surface in reflective and transmissive mode. The measurement condition is:

Measuring Equipment	BM-7 or EQUI
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

11.4 Definition of Viewing Angles



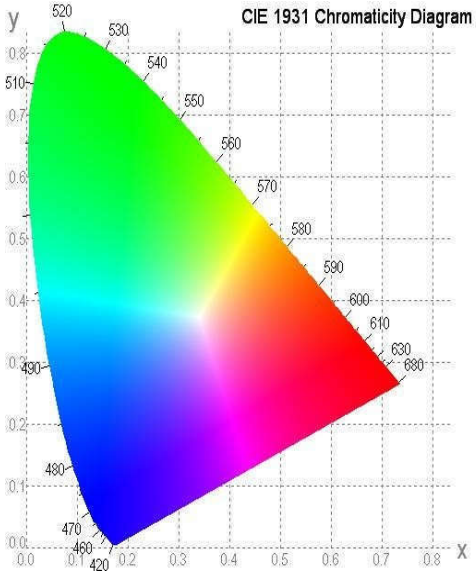
Measuring machine: LCD-5100 or EQUI

11.5 Definition of Color Appearance

R,G,B and W are defined by (x, y) on the TOPchromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



11.6 Definition of Surface Luminance, Uniformity and Transmittance

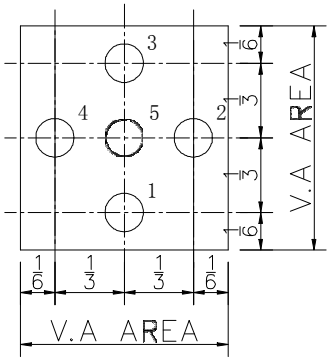
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

11.6.1 Surface Luminance: LV = average (LP1:LP5)

11.6.2 Uniformity = Minimal (LP1:LP5) / Maximal (LP1:LP5) * 100%

11.6.3 Transmittance = LV on LCD / LV on Backlight * 100%

Note :Measuring machine:BM-7



12 Quality Assurance

12.1 Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer by Iexcellence display.

12.2 Agreement Items

Iexcellence and customer shall negotiate if the following situation occurs:

12.2.1 Discrepancies between Iexcellence 's QA standards and customer's QAstandards.

12.2.2 Additional requirement to be added in product specification.

12.2.3 Any other special problem.

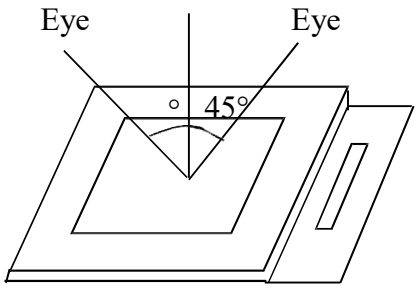
12.3 Standard of the Product Visual Inspection

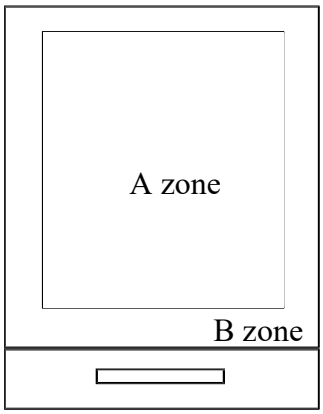
12.3.1 Appearance inspection:

12.3.1.1 The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

12.3.1.2 The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

12.3.1.3 Definition of area: A Zone: Active Area, B Zone: Viewing Area.





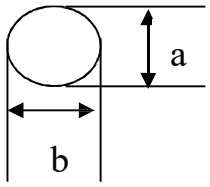
12.3.2 Basic principle:A set of sample to indicate the limit of acceptable quality level mustbe discussed by both Iexcellence and customer when there is any dispute happened.

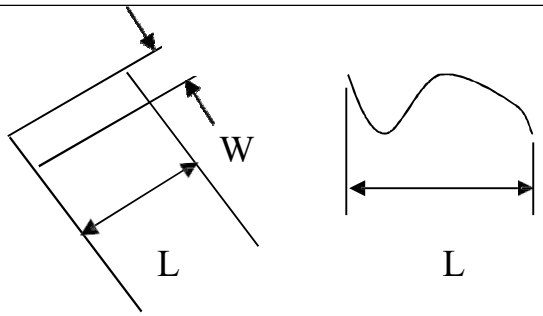
12.4 Inspection Specification

Sampling plan according to GB/T2828.1-2012/ISO 2859-1：1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

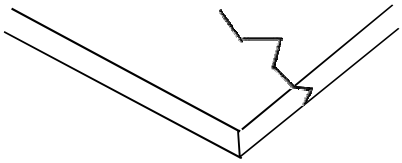
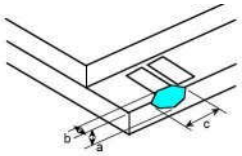
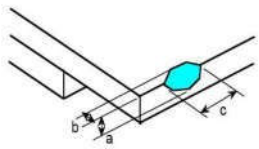
Major defect: AQL 0.4

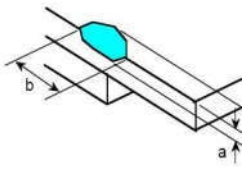
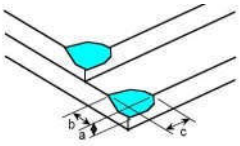
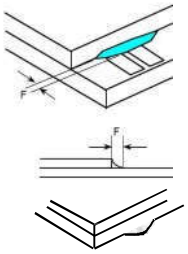
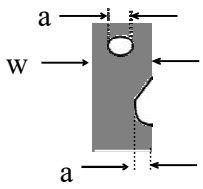
Minor defect: AQL 1.0

<table>
<tr><th>No.</th><th>Item</th><th>Criteria (Unit: mm)</th></tr>
<tr><td>01</td><td>Black / White spot
Foreign material
(Round type)
Pinholes Stain
Particles inside cell.
(Minor defect)</td><td>


φ= (a + b) /2
<table>
<tr><th>Area / Size</th><th>Acc. Qty</th></tr>
<tr><td>φ≤0.10</td><td>Ignore</td></tr>
<tr><td>0.10<φ≤0.15</td><td>2</td></tr>
<tr><td>0.15<φ≤0.20</td><td>1</td></tr>
<tr><td>0.20<φ</td><td>0</td></tr>
<tr><td>Total</td><td>2
(no include
φ≤ 0.10)</td></tr>
</table>
Distance between 2 defects should more than 5mm apart.
</td></tr>
<tr><td>02</td><td>Black and White line
Scratch
Foreign material
(Line type)</td><td>


<table>
<tr><th>Length</th><th>Width</th><th>Acc. Qty</th></tr>
<tr><td>/</td><td>W ≦ 0.03</td><td>Ignore</td></tr>
<tr><td>L ≦ 2</td><td>0.03 ＜ W ≦ 0.05</td><td>1</td></tr>
<tr><td>/</td><td>0.05 ＜ W</td><td>0</td></tr>
<tr><td colspan="2">Total</td><td>1</td></tr>
</table>
</td></tr>
</table>

<table>
<tr><td></td><td>(Minor defect)</td><td>Distance between 2 defects should more than 5mm apart.
Scratches not viewable through the back of the display are acceptable.</td></tr>
<tr><td>03</td><td>Glass Crack
(Minor defect)</td><td> 
LCD with extensible crack line is unacceptable(When press the cracked LCD area, the line will expand, we define it is extensible crack line)</td></tr>
<tr><td>04</td><td>Glass Chipping Pad Area: (Minor defect)
  </td><td><table><tr><td>Length and Width</td><td>Acc. Qty</td></tr><tr><td>c ＜ 5.0, b< 0.4</td><td>Ignore</td></tr></table></td></tr>
<tr><td>05</td><td>Glass Chipping Rear of Pad Area:
(Minor defect)
  </td><td><table><tr><td>Length and Width</td><td>Acc. Qty</td></tr><tr><td>c > 3.0, b< 1.0</td><td>1</td></tr><tr><td>c< 3.0, b< 1.0</td><td>2</td></tr><tr><td>c< 3.0, b< 0.5</td><td>4</td></tr><tr><td colspan="2">a<Glass Thickness</td></tr></table></td></tr>
</table>

<table>
<tr><th>No.</th><th>Item</th><th>Criteria (Unit: mm)</th></tr>
<tr><td>06</td><td>Glass Chipping
Except Pad Area:
(Minor defect)
  </td><td><table><tr><td>Length and Width</td><td>Acc. Qty</td></tr><tr><td>c ≤0.6, b< 5.0</td><td>Ignore</td></tr><tr><td colspan="2">a<Glass Thickness</td></tr></table></td></tr>
<tr><td>07</td><td>Glass Corner
Chipping:
(Minor defect)
  </td><td><table><tr><td>Length and width</td><td>Acc. Qty</td></tr><tr><td>c < 2.0, b< 1.5</td><td>Ignore</td></tr><tr><td>c < 1.5, b< 2</td><td>Ignore</td></tr><tr><td colspan="2">a<Glass Thickness</td></tr></table></td></tr>
<tr><td>08</td><td>Glass Burr:
(Minor defect)
  </td><td>Glass burr don’t affect assemble and module dimension
<table><tr><td>Length</td><td>Acc. Qty</td></tr><tr><td>F < 0.5</td><td>Ignore</td></tr></table></td></tr>
<tr><td>09</td><td>FPC Defect:
(Minor defect)
  </td><td>9.1 Dent, pinhole width a<w/2.
(w: circuitry width.)
9.2 Open circuit is unacceptable.
9.3 No oxidation, contamination and distortion.</td></tr>
<tr><td>10</td><td>Screen deformation
  </td><td>Test for insertion of plug gauge at highest warping point：
(0.96-3.1inches does not contain3.1)
H≦0.25MM
The client has special requirements,according to drawing</td></tr>
</table>

<table>
<tr><th>No.</th><th>Item</th><th>Criteria (Unit: mm)</th></tr>
<tr><td>11</td><td>Bubble on Polarizer
(Minor defect)</td><td>
<table>
<tr><td>Diameter</td><td>Acc. Qty</td></tr>
<tr><td>φ≤0.15</td><td>Ignore</td></tr>
<tr><td>0.15 <φ≤0.20</td><td>2</td></tr>
<tr><td>0.20 <φ≤0.30</td><td>1</td></tr>
<tr><td>0.3 < φ</td><td>None</td></tr>
</table>
</td></tr>
<tr><td>12</td><td>Dent on Polarizer
(Minor defect)</td><td>
<table>
<tr><td>Diameter</td><td>Acc. Qty</td></tr>
<tr><td>φ≤0.15</td><td>Ignore</td></tr>
<tr><td>0.15 <φ≤0.20</td><td>2</td></tr>
<tr><td>0.20 <φ≤0.30</td><td>1</td></tr>
<tr><td>0.3 < φ</td><td>None</td></tr>
</table>
</td></tr>
<tr><td>13</td><td>Bezel</td><td>13.1 No rust, distortion on the Bezel.
13.2 No visible fingerprints, stains or other contamination.</td></tr>
<tr><td>14</td><td>Touch Panel</td><td>D: Diameter W: width L: length
14.1 Spot: D≤0.20 is acceptable
0.20<D≤0.3, acceptable QTY, 3
2dots are acceptable and the distance between defects should more than 5mm.
D>0.3 is unacceptable
14.2 Dent: D>0.30 is unacceptable
14.3 Scratch: W≤0.03, L≤10 is acceptable,
0.03<W≤0.10, L≤10 ,acceptable QTY, 3
Distance between 2 defects should more than 5 mm.
W>0.10 is unacceptable.</td></tr>
<tr><td>15</td><td>PCB</td><td>15.1 No distortion or contamination on PCB terminals.
15.2 All components on PCB must same as documented on the BOM/component layout.
15.3 Follow IPC-A-600F.</td></tr>
<tr><td>16</td><td>Soldering</td><td>Follow IPC-A-610C standard</td></tr>
</table>

No.	Item	Criteria (Unit: mm)
17	Electrical Defect (Major defect)	The below defects must be rejected. 17.1 Missing vertical / horizontal segment, 17.2 Abnormal Display. 17.3 No function or no display. 17.4 Current exceeds product specifications. 17.5 LCD viewing angle defect. 17.6 No Backlight. 17.7 Dark Backlight. 17.8 Touch Panel no function. 17.9 Dark Dot –one Allowed. 17.10 Bright Dot – one Allowed. Remark: 1. A pixel defect is acceptable if one color is none functionaland causes a bright dot. The display may have one case where one color is out and cause a dark dot. 2. Bright dot caused by scratch and foreign object accords to item1.
18	Leak	Yellow light,OK; White light,According to the limit sample

Remark: Visual and cosmetic defects are rejectable only if these fall within the LCD viewing area.

12.5 Classification of Defects

Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

12.6 Identification/marking criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

12.7 Packing

12.7.1 There should be no damage of the outside carton box, each packaging box should has label in the correct location per packing drawing requirement.

12.7.2 All direct package materials shall offer ESD protection.

13 Reliability Specification

Item	Condition	Cycle Time	Quantity	Remark
Constant Temp. and Constant Humidity Operation Test	+40 ± 3ºC,90 ± 3%RH	96hrs	--	*1
High Temp. Operation Test	+70 ± 3ºC	96hrs	--	
Low Temp. Operation Test	-20 ± 3ºC	96hrs	--	
Thermal Shock Test	-20 ± 3ºC (30min) +70 ± 3ºC (30min)	10cycles	--	
ESD Test(end product)	150pF, 330Ω, ±2KV, Contact	10times	--	*2, *3
	150pF, 330Ω, ±6KV, Air			
Vibration Test (for packaging)	Frequency: 10Hz to 55Hz to10Hz,Swing:1.5mm,time：X,Y,Z each 2H.	6hrs	One inner carton	*4

Note 1. For humidity test, DI water should be used.

Inspection Standard: Inspect after 1-2hrs storage at room temperature, the sample shall be free from the following defects:

- Air bubble in the LCD
- Seal Leakage
- Non-display
- Missing Segment
- Glass Crack
- IDD is greater than twice initial value.
- Others as per QA Inspection Criteria

Note 2. No defect is allowed after testing

The End Product ESD value is only indicative and depends on customer ESD protection design for the whole system.

Note 3. ESD should be applied to LCD glass panel, not other areas (such as on IC and so on)

IDD should be within twice initial value.

In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

Note 4. Only upon request.

14 Precautions and Warranty

14.1 Safety

14.1.1 The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap andwater.

14.1.2 Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

14.2 Handling

14.2.1 Reverse and use within ratings in order to keep performance and prevent damage.

14.2.2 Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

14.3 Operation

14.3.1 Do not drive LCD with DC voltage

14.3.2 Response time will increase below lower temperature

14.3.3 Display may change color with different temperature

14.3.4 Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.

14.4 Static Electricity

14.4.1 CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.

14.4.2 The normal static prevention measures should be observed for work clothes and benches.

14.4.3 The module should be kept into anti-static bags or other containers resistant to static for storage.

14.5 Limited Warranty

14.5.1 Unless otherwise agreed between TOP-DISPLAY and customer, TOP-DISPLAY will replace or repair any of its LCD and LCM which TOP-DISPLAY found to be defective electrically and visually when inspected in accordance with TOP-DISPLAY Quality Standards, for a period of one year from date of shipment.

14.5.2 The warranty liability of TOP-DISPLAY is limited to repair and/or replacement. TOP-DISPLAY will not be responsible for any consequentialloss.

14.5.3 If possible, we suggest you use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module beused.

15 Packaging

TBD

16 Prior Consult Matter

1. ForTOP-DISPLAY standard products, we keep the right to change material, process for improving the product property without prior notice to our customer.

2. For OEM products, if any changes are needed which may affect the product property,we will consult with our customer in advance.

3. If you have special requirement about reliability condition, please let us know before you start the test on our samples.