

A

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# Yonaguni

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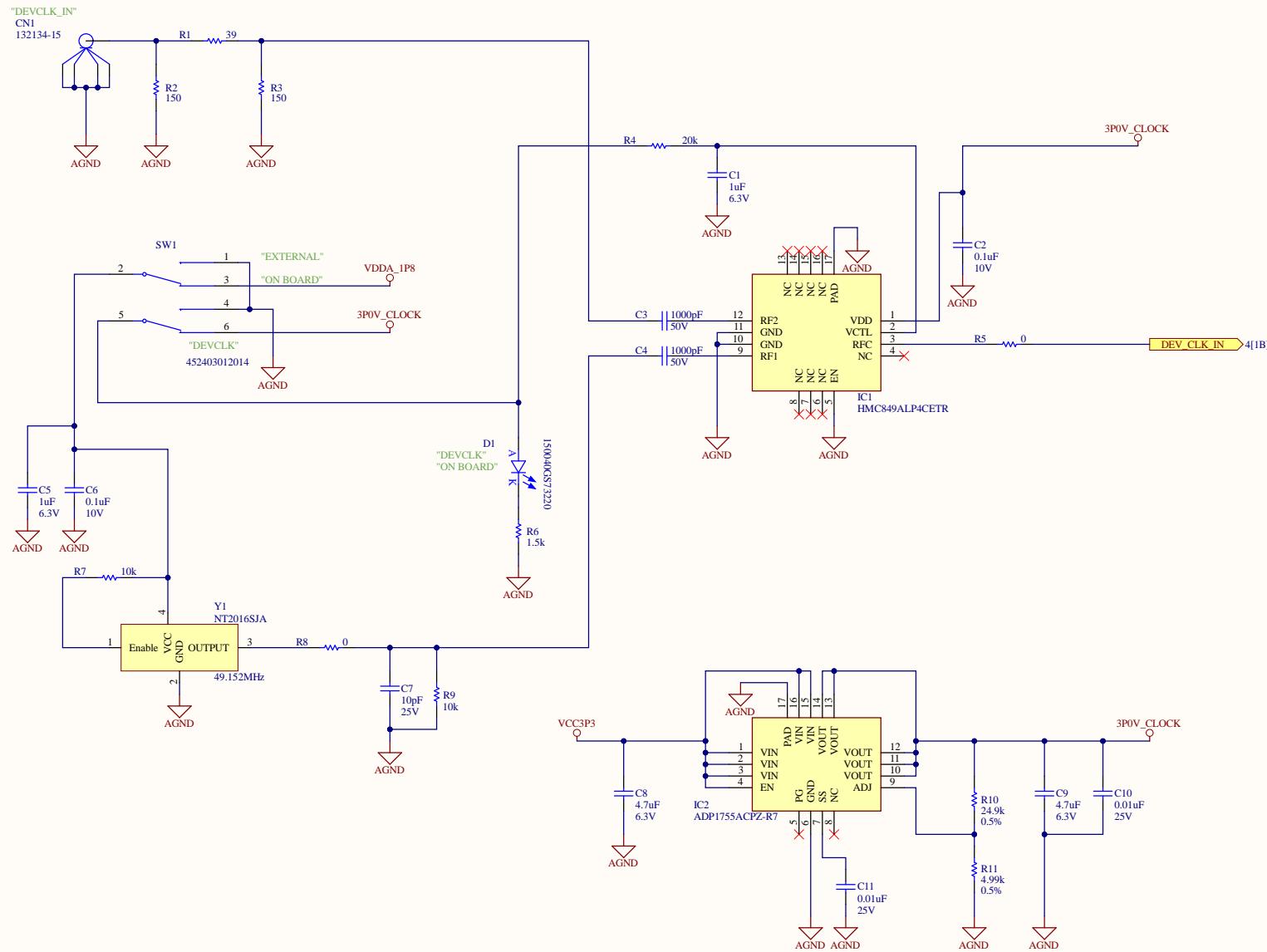
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Page	Title
01	Cover Page
02	Navassa Clock
03	Navassa RF TX, RX, RFLO
04	Navassa RFIO, Digital I/F, Data Port
05	Navassa Power
06	Navassa Power Pin
07	SoC BANK3 & BANK6
08	SoC BANK4 & BANK5 & BANK8
09	SoC BANK7
10	SoC POWER & Clocks
11	SoC Configuration
12	SoC GND
13	SoC Decoupling capacitor
14	Clock & Reset
15	Boot Device & GPIO & Boot Select & Clock Select
16	SDRAM
17	Audio Codec
18	USB2.0
19	USB UART
20	USB Power Delivery
21	Power Management LTC2977
22	Power Management LTC2974
23	Power Supply 1.0V, 1.1V, 1.5V, 1.8V, 2.5V, 3.3V, 5V
24	Power Supply Navassa 1.8V, 1.3V
25	History of revisions

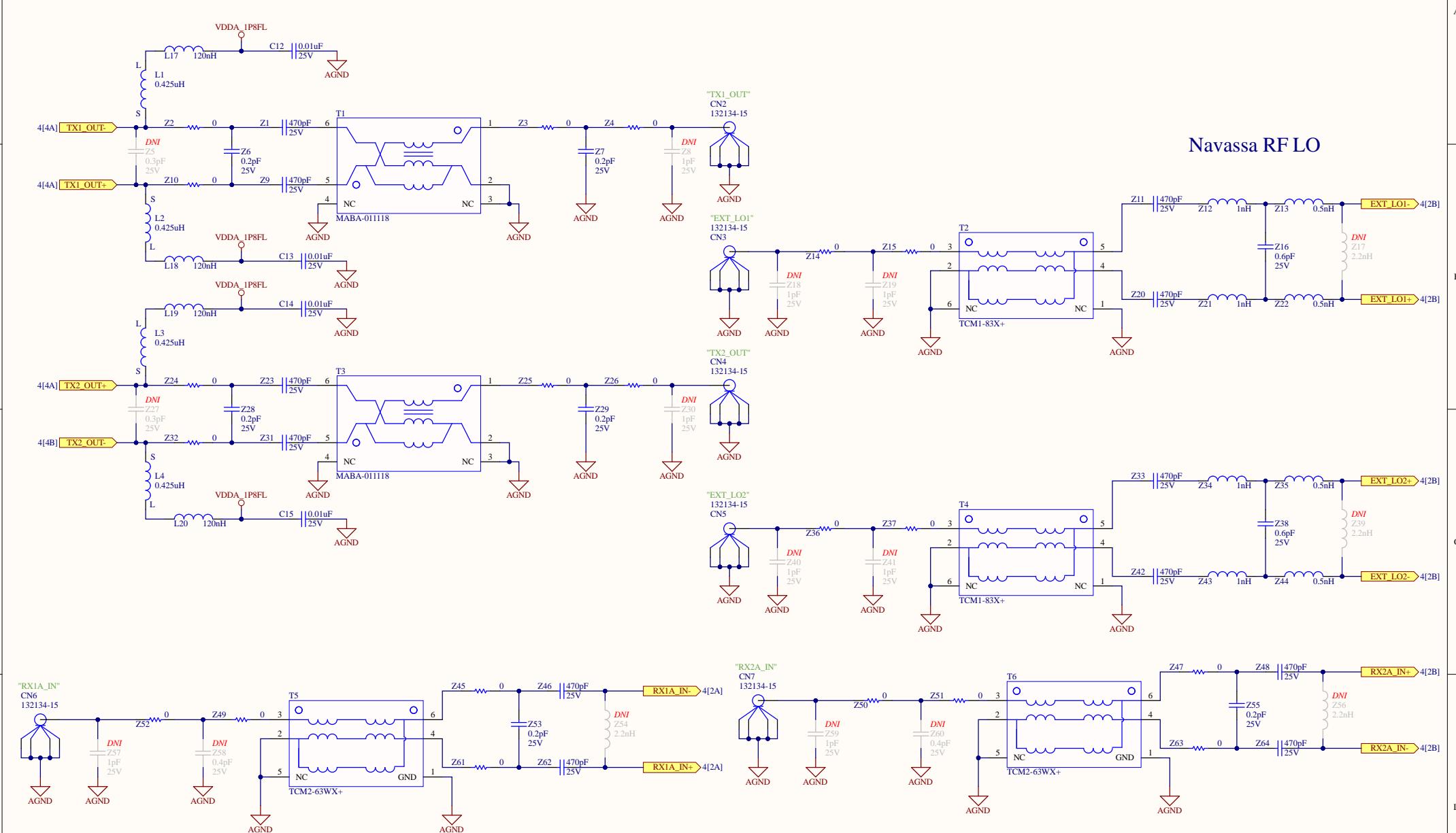
Approval H.Sato	File Name <a href="#">Index.SchDoc</a>	Title <b>Yonaguni</b>	Sheet 1 / 25
Inspection H.Sato	Date 2022/1/12	Size A3	
Drawing Hiroyuki Tsuchiya		DWG No <a href="#">1113-01-YG-302-001</a>	Rev 2.00Draft9

# Navassa Clock



Approval	File Name	Title	
H.Sato	Navassa_Clk.SchDoc	Yonaguni	
Inspection	Date 2022/1/12	Size A3	Sheet 2 / 25
Drawing			
Hirofumi Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001	Rev 2.00Draft9

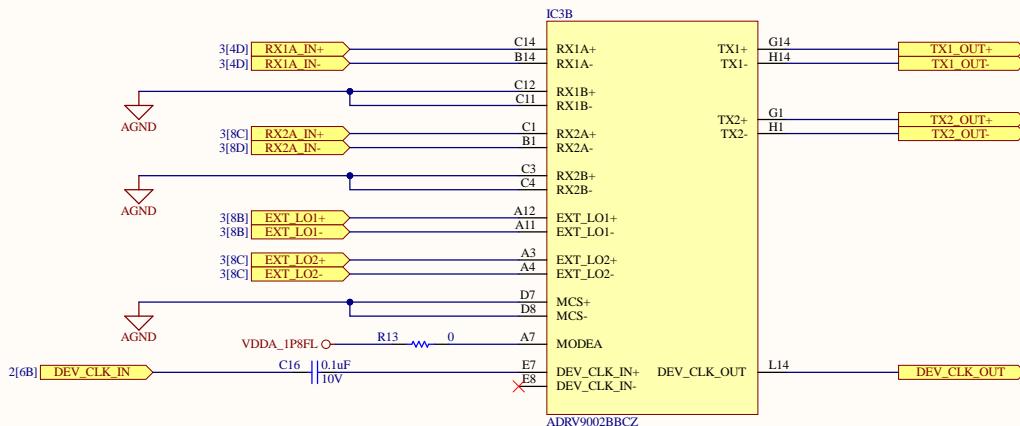
# Navassa RF TX, RX



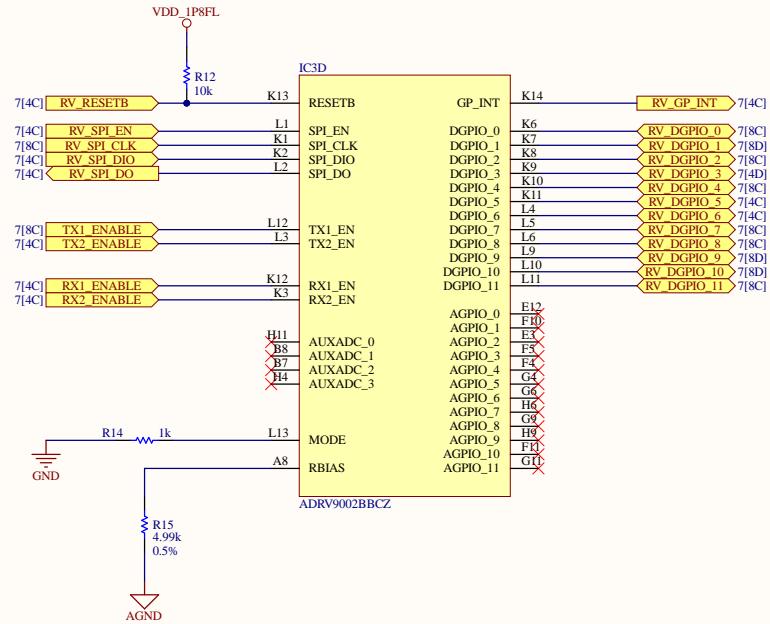
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H.Sato	Navassa_TXRX_RFLO.SchDoc	Yonaguni
Inspection	Date 2022/1/12	Sheet 3 / 25
Drawing	Size A3	
Hirofumi Tsuchiya	DWG No 1113-01-YG-302-001	Rev 2.00Draft9
Marimo Electronics Co.,Ltd.		

## Navassa RF IO

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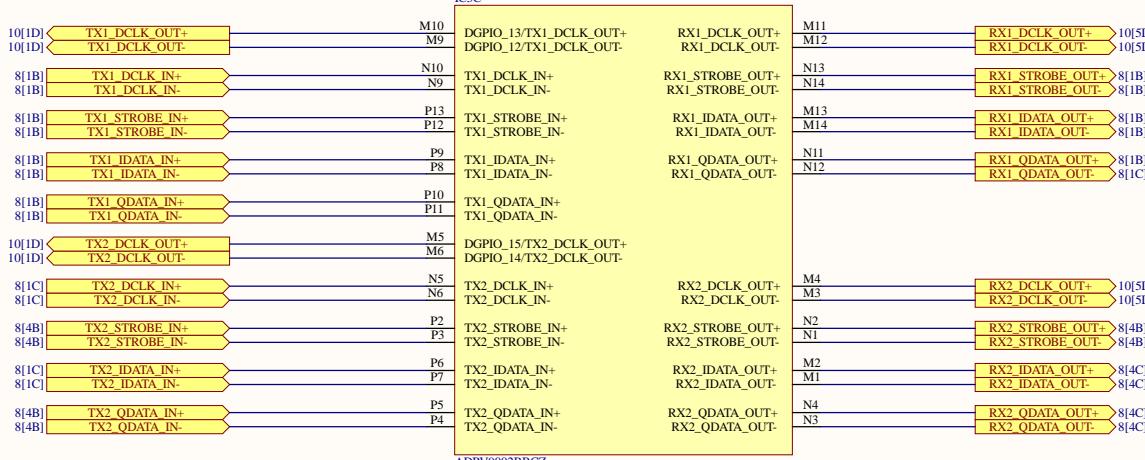


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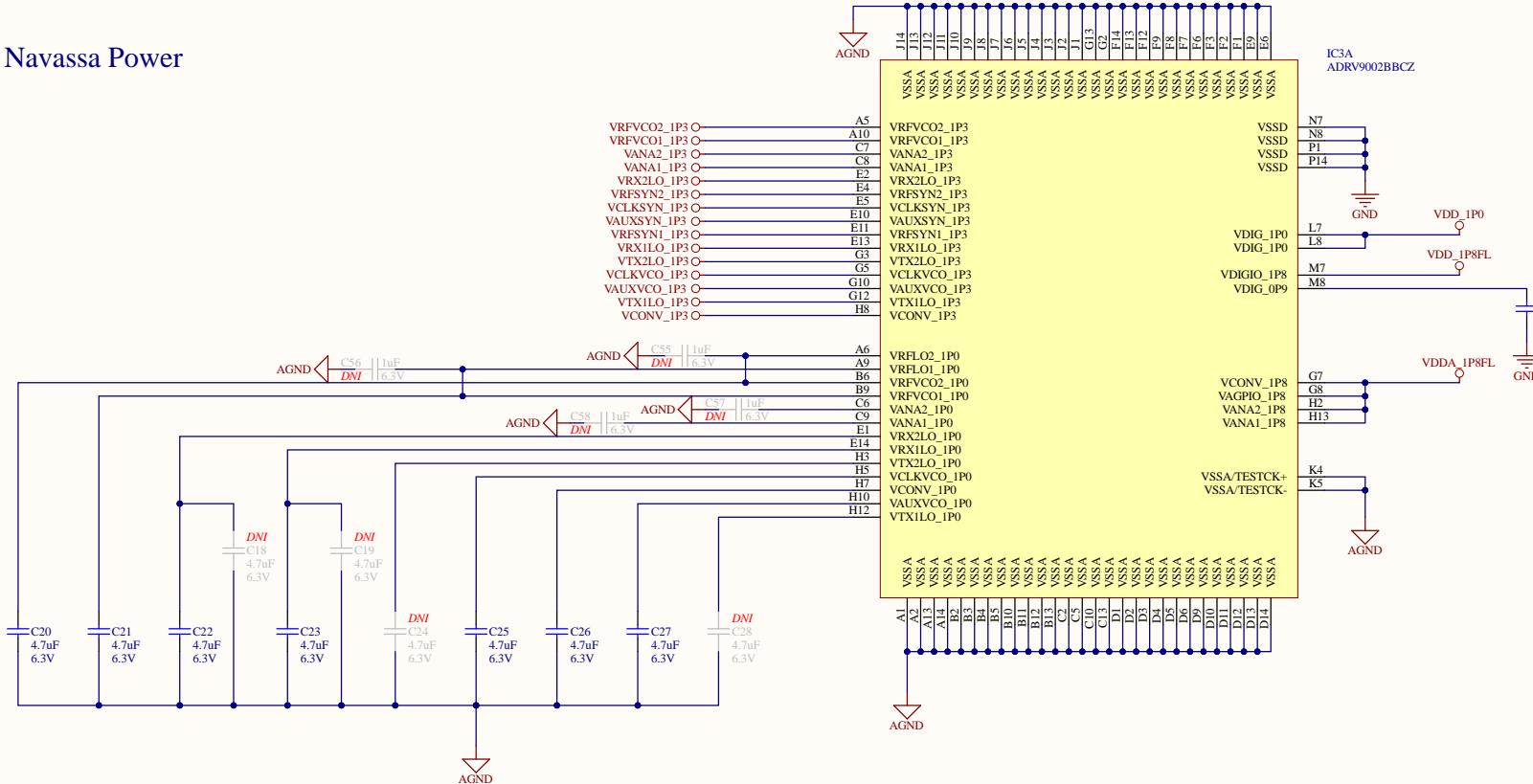
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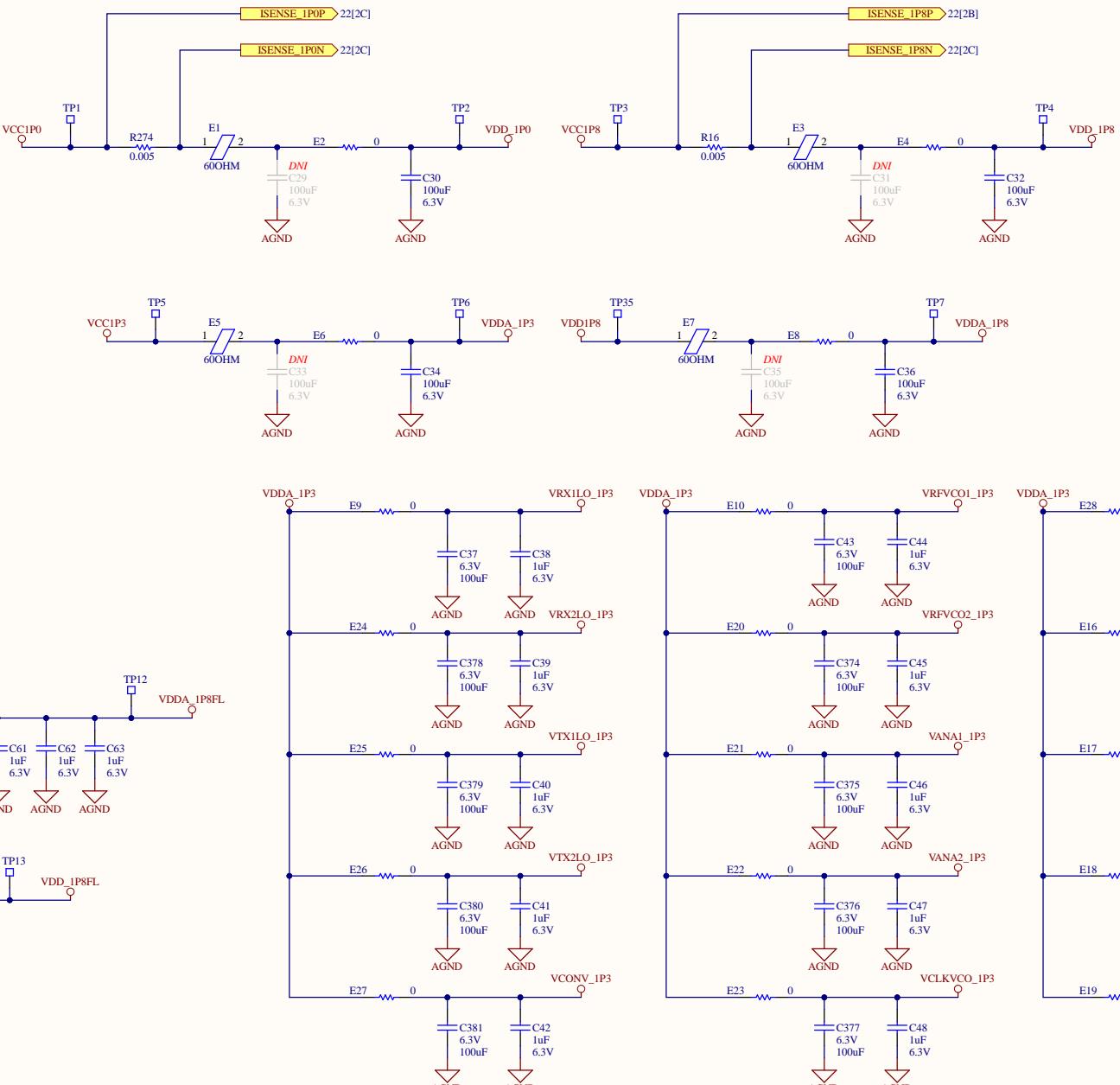
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Inspection	Date 2022/1/12	Size A3
Drawing	Hirofumi Tsuchiya	DWG No 1113-01-YG-302-001
	Marimo Electronics Co.,Ltd.	Rev 2.00Draft9

## Navassa Power



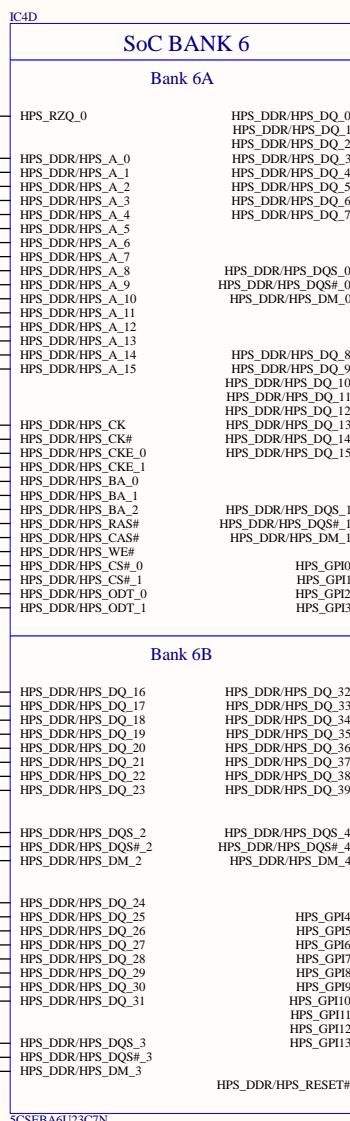
Approval H.Sato	File Name Navassa_Power.SchDoc	Title Yonaguni	Sheet 5 / 25
Inspection H.Sato	Date 2022/1/12	Size A3	
Drawing Hiroyuki Tsuchiya		DWG No 1113-01-YG-302-001	Rev 2.00Draft9

## Navassa Power Pin

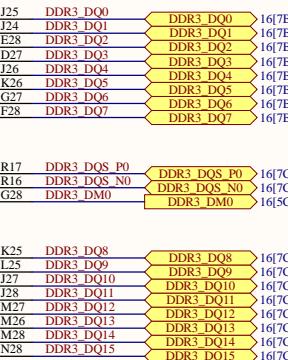


Approval H.Sato	File Name Navassa_PowerPin.SchDoc	Title Yonaguni	Sheet 6 / 25
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Drawing Hiroyuki Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001	Rev 2.00Draft9

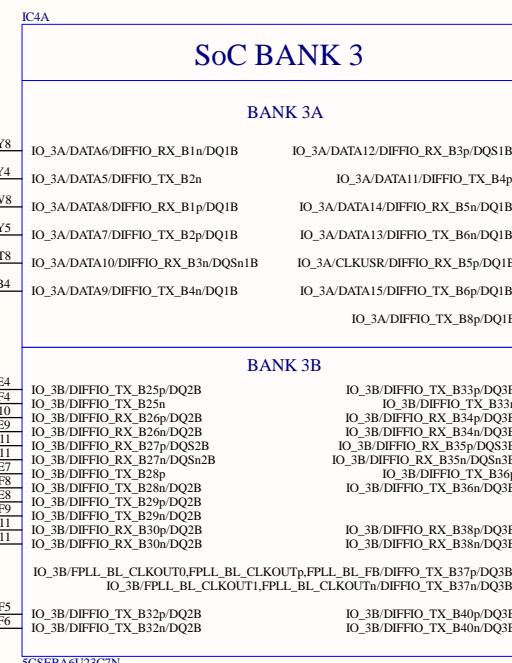
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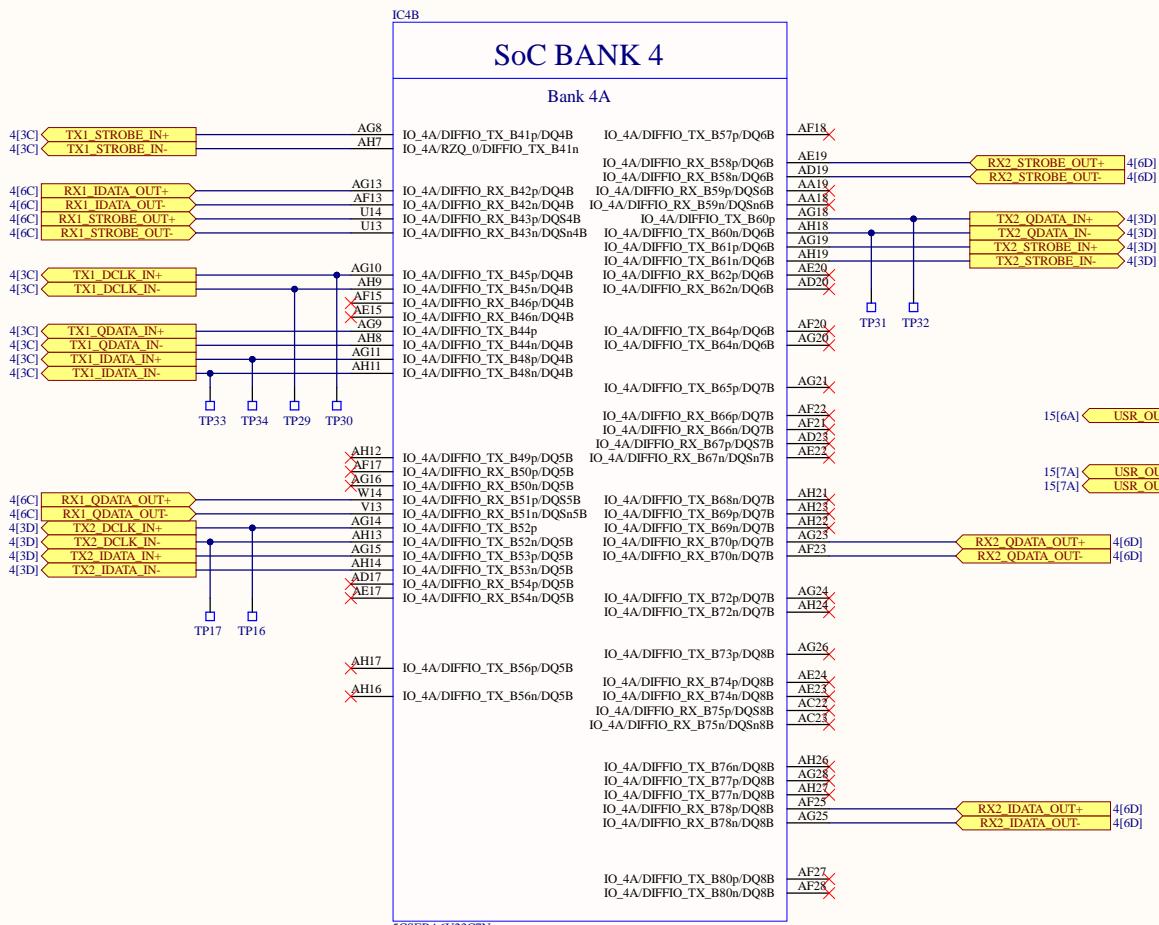
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Approval H.Sato	File Name SoC_BANK3&6.SchDoc	Title Yonaguni
Inspection H.Sato	Date 2022/1/12	Size A3
Drawing Hiroyuki Tsuchiya	Sheet 7 / 25	
	DWG No 1113-01-YG-302-001	Rev 2.00Draft9

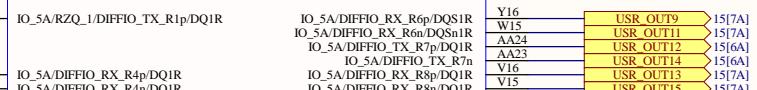
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### SoC BANK 5

#### Bank 5A



#### Bank 5B



5CSEBA6U23C7N

### Bank 8A



5CSEBA6U23C7N

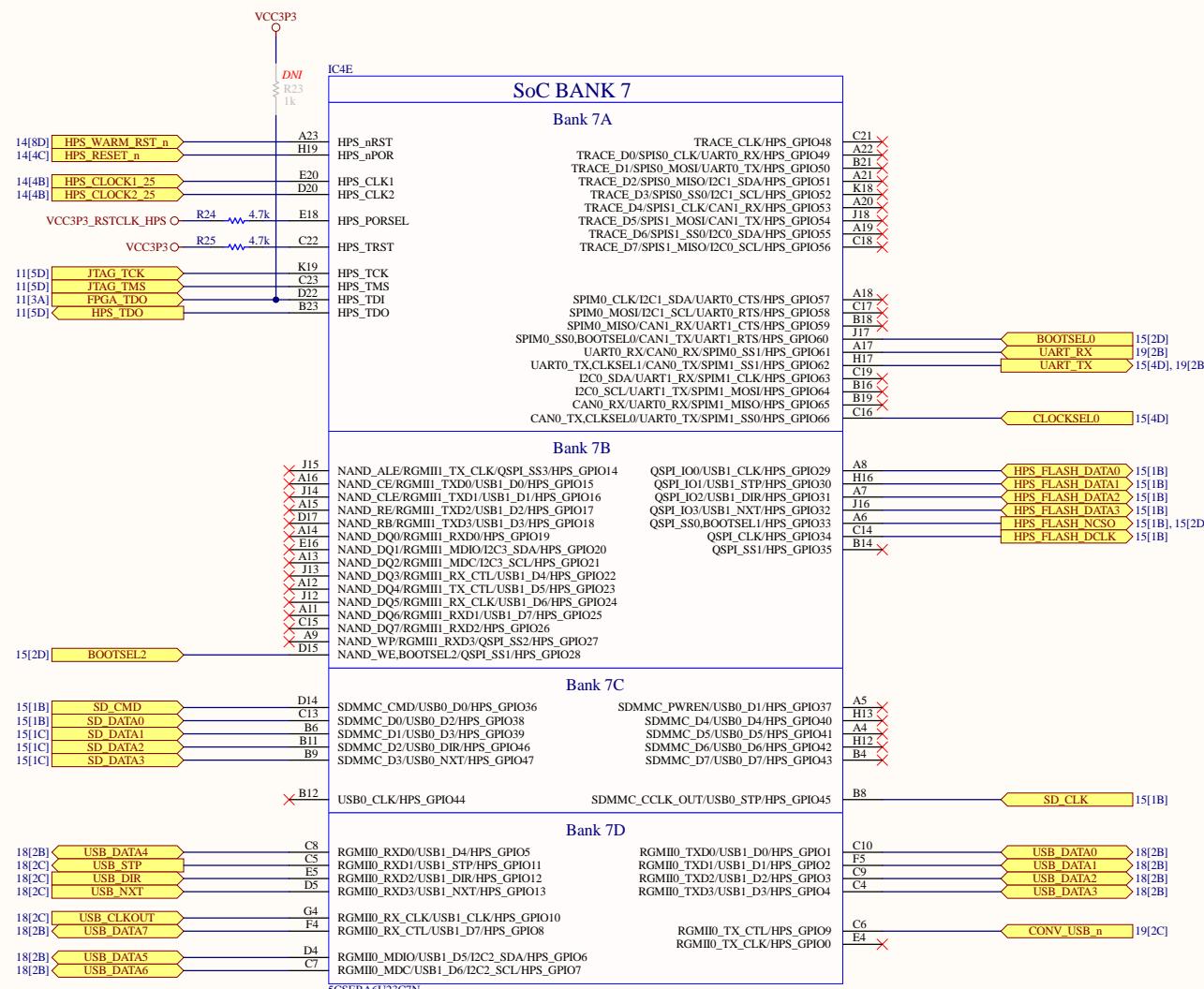
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Approval	File Name	Title	Sheet
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Inspection	Date	Size	
H.Sato	2022/1/12	A3	
Drawing	DWG No	Rev	
Hirofumi Tsuchiya	Marimo Electronics Co.,Ltd.	2.00Draft9	
	1113-01-YG-302-001		

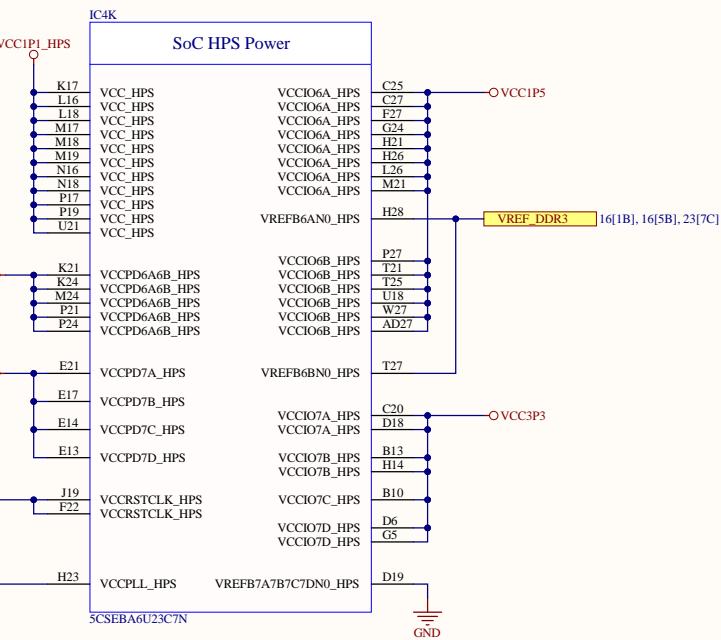
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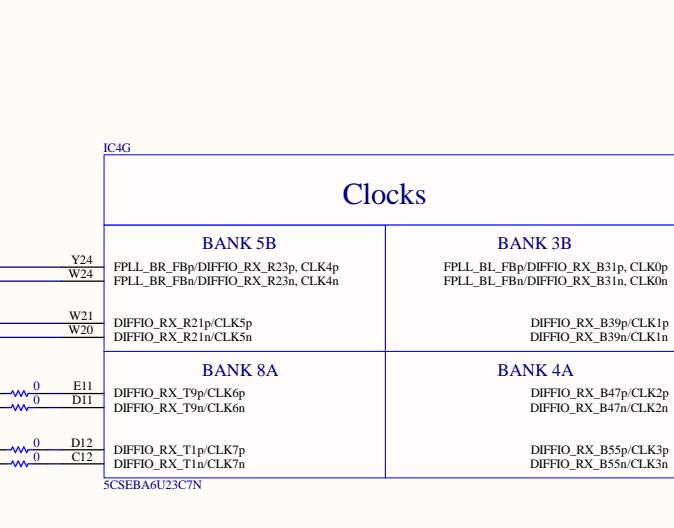


Approval H.Sato	File Name SoC_BANK7.SchDoc	Title Yonaguni
Inspection H.Sato	Date 2022/1/12	Size A3
Drawing Hiroyuki Tsuchiya	DWG No 1113-01-YG-302-001	Sheet 9 / 25
	Rev Marimo Electronics Co.,Ltd.	Rev 2.00Draft9

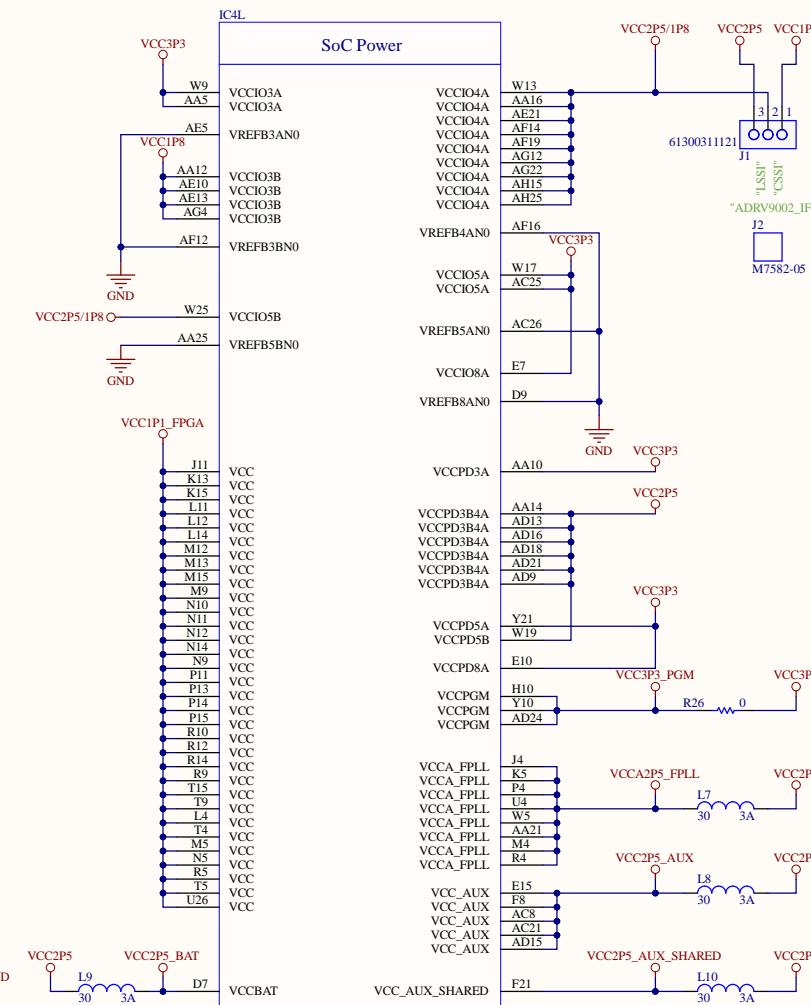
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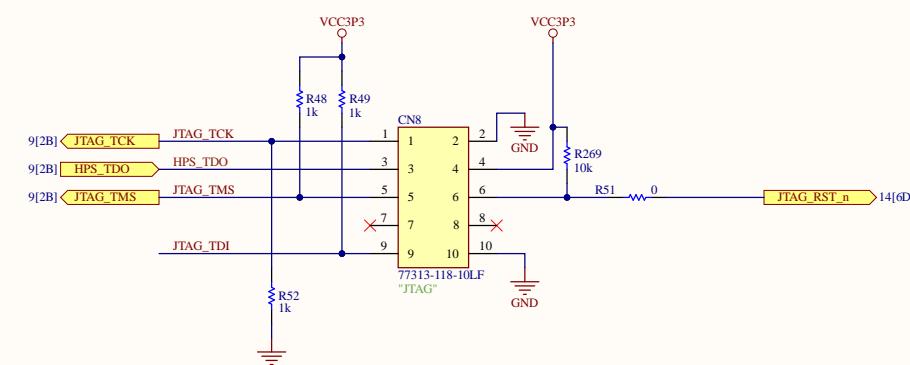
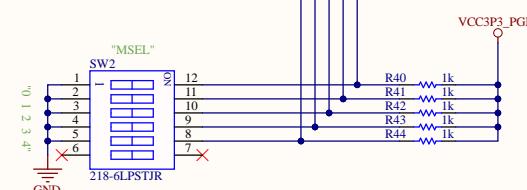
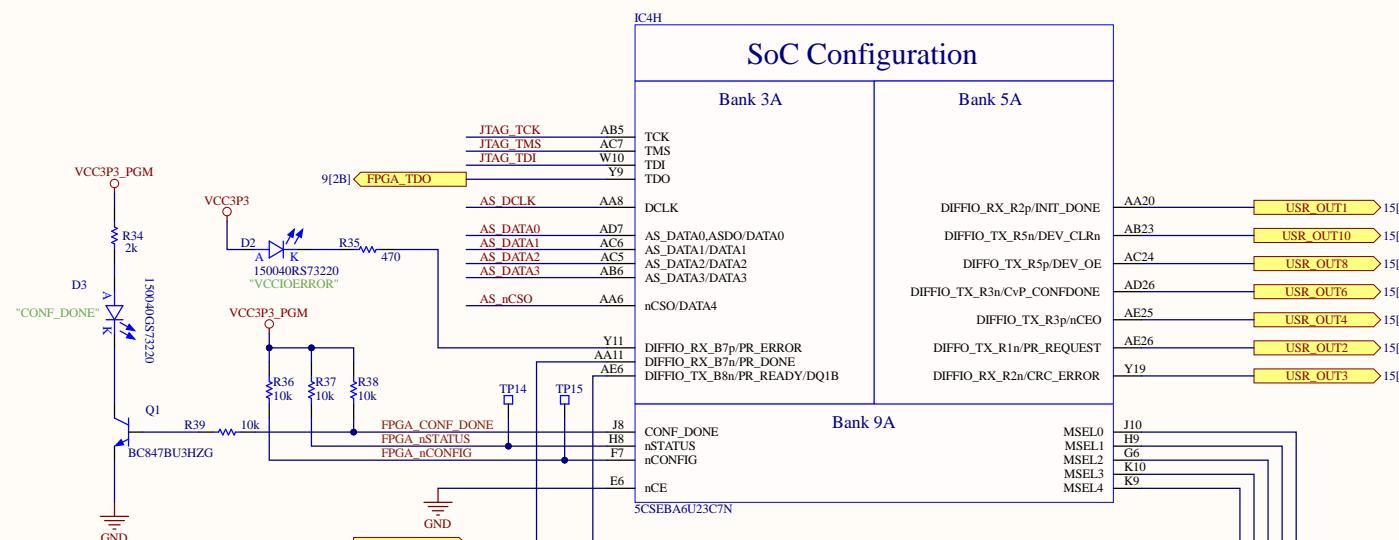
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H.Sato	SoC_Power_Clk.SchDoc	Yonaguni
Inspection	Date 2022/1/12	Size A3
H.Sato	Drawing Hirokuni Tsuchiya	Sheet 10 / 25
	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001
		Rev 2.00Draft9



Approval	File Name	Title	Sheet
H.Sato	SoC_Conf.SchDoc	Yonaguni	
Inspection	Date 2022/1/12	Size A3	11 / 25
Drawing	Hirokazu Tsuchiya	DWG No 1113-01-YG-302-001	Rev 2.00Draft9

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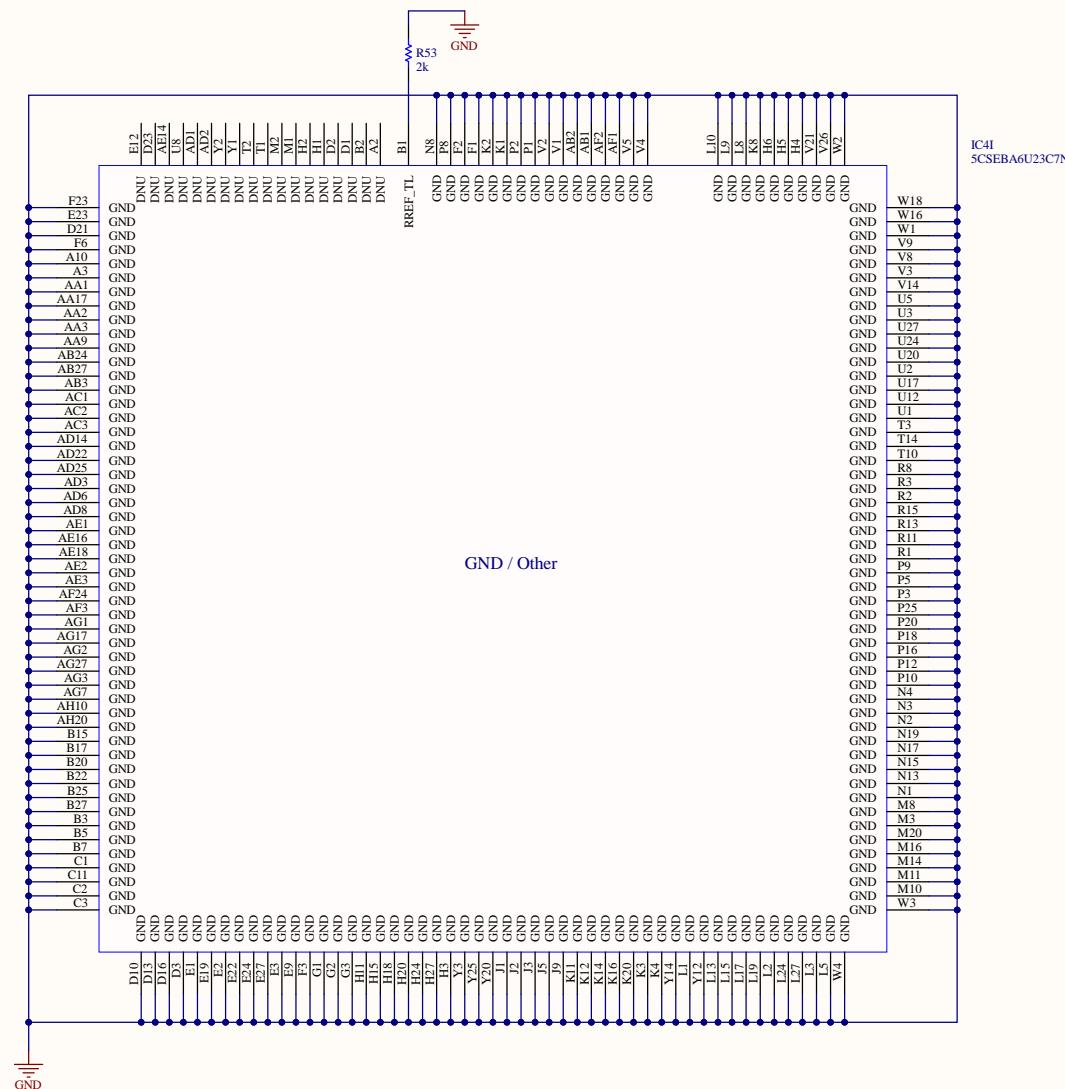
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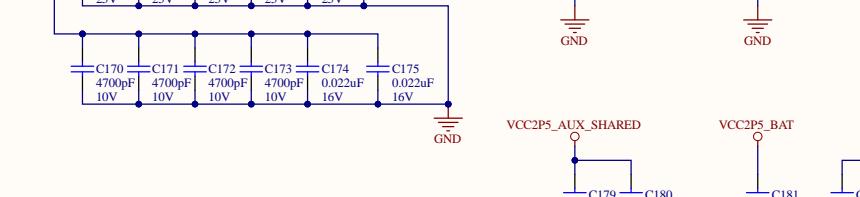
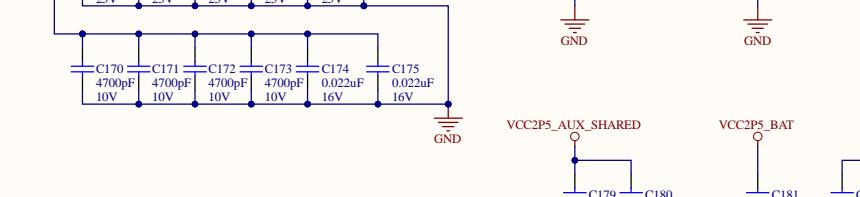
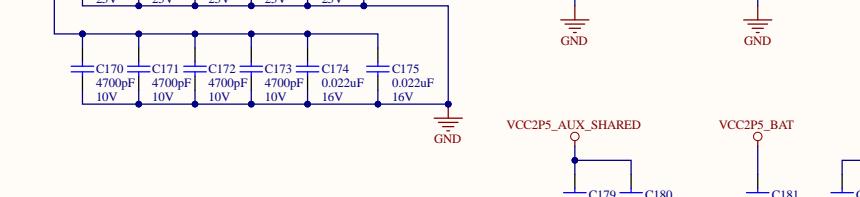
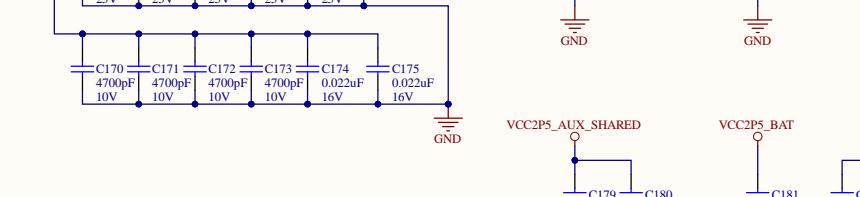
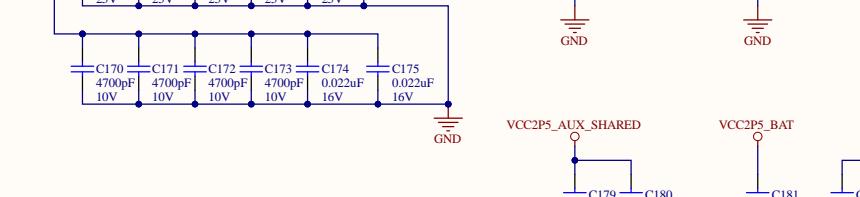
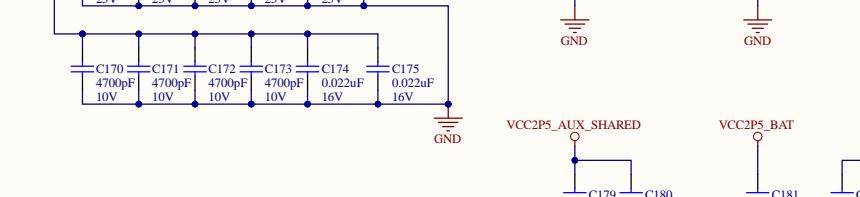
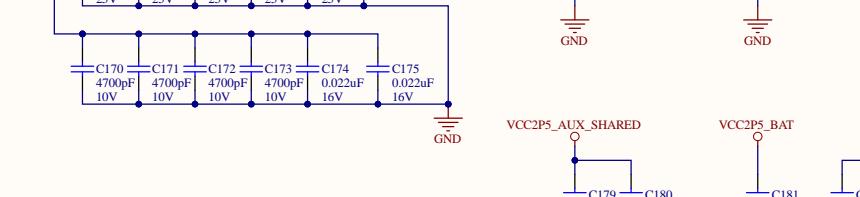
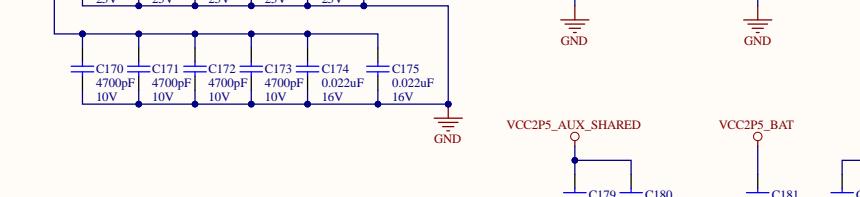
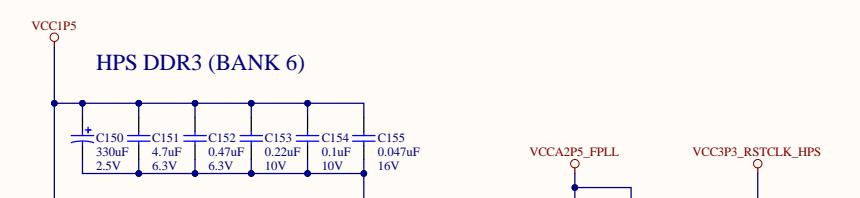
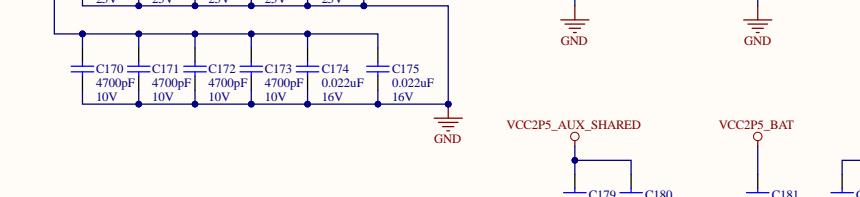
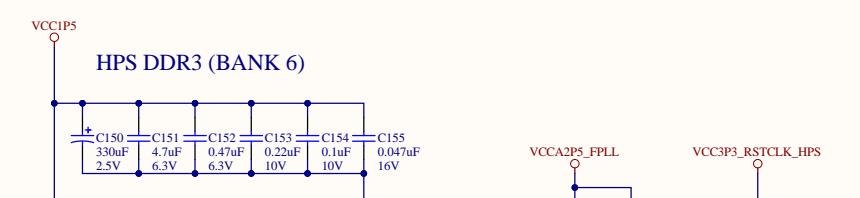
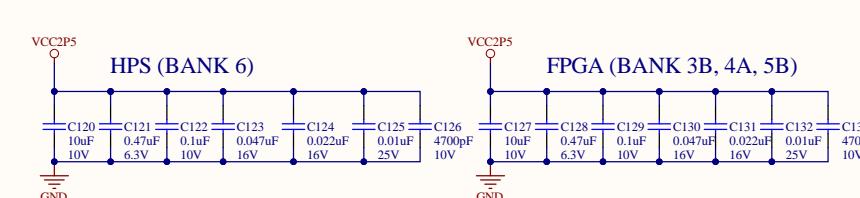
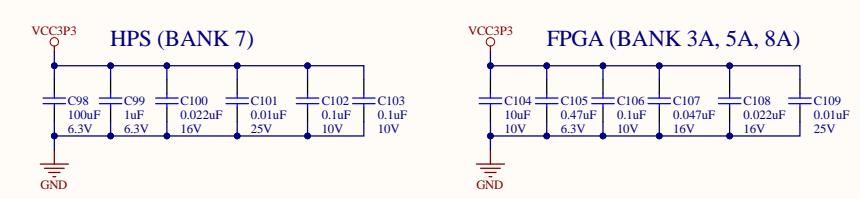
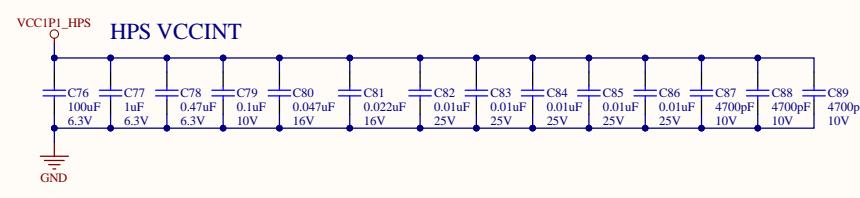
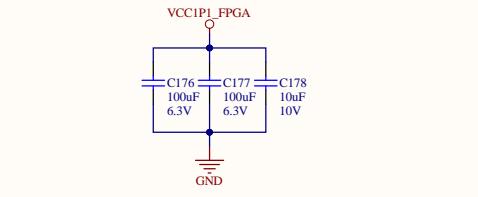
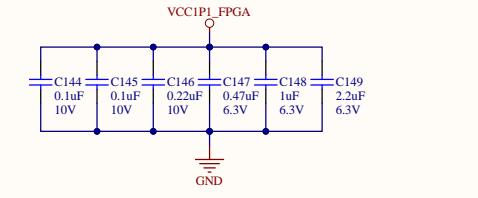
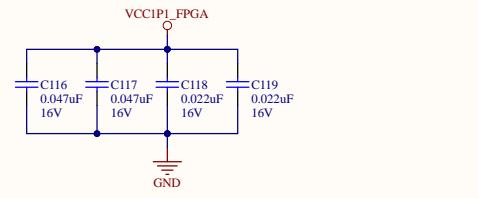
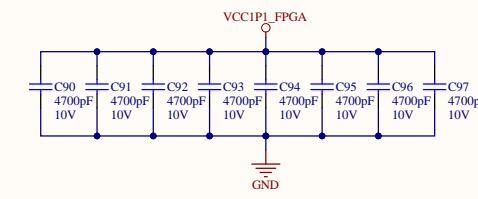
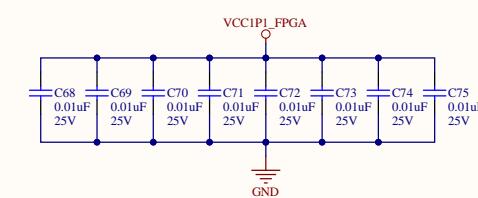
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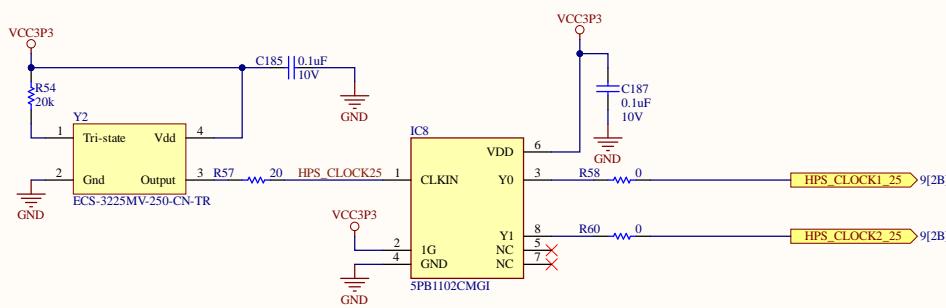
Approval H.Sato	File Name SoC_GND.SchDoc	Title Yonaguni	Sheet 12 / 25
Inspection H.Sato	Date 2022/1/12	Size A3	
Drawing Hiroyuki Tsuchiya		DWG No 1113-01-YG-302-001	Rev 2.00Draft9

**FPGA VCCINT**

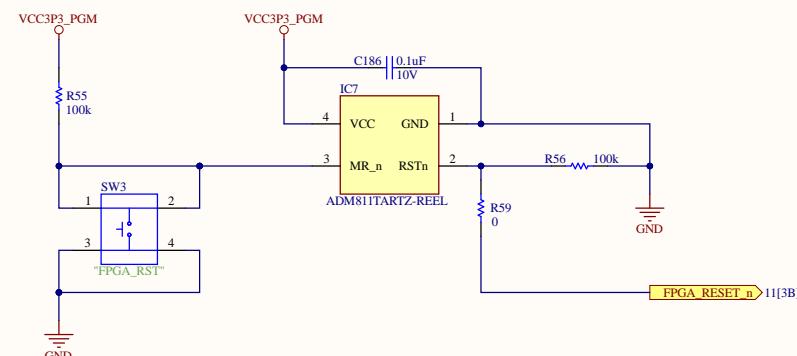
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H.Sato	SoC_PassConSchDoc	Yonaguni	
Inspection	Date 2022/1/12	Size A3	
H.Sato			/ 25
Drawing			
Hirofumi Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001	Rev 2.00Draft9

## Clock / Reset

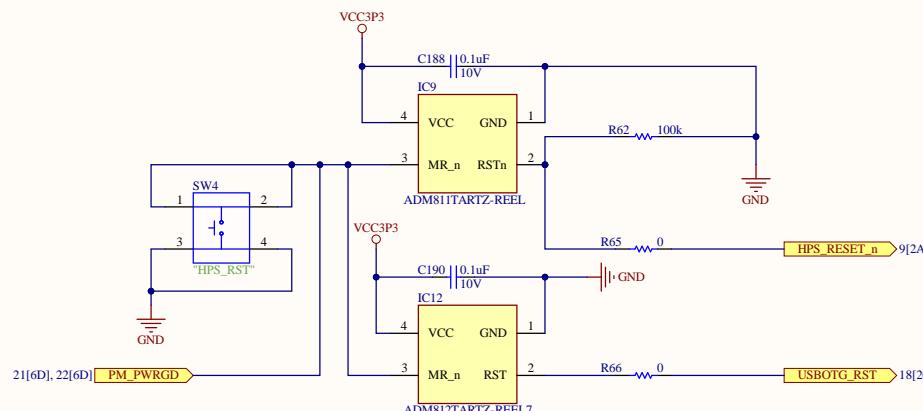
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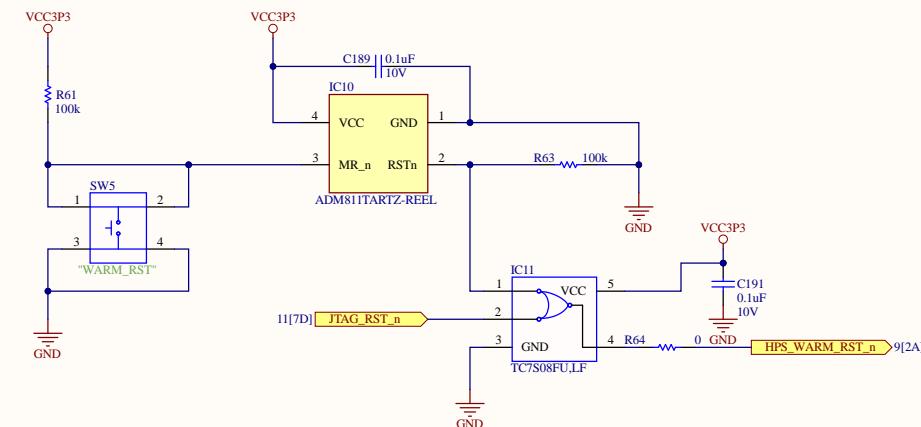
### FPGA Reset



### HPS Reset



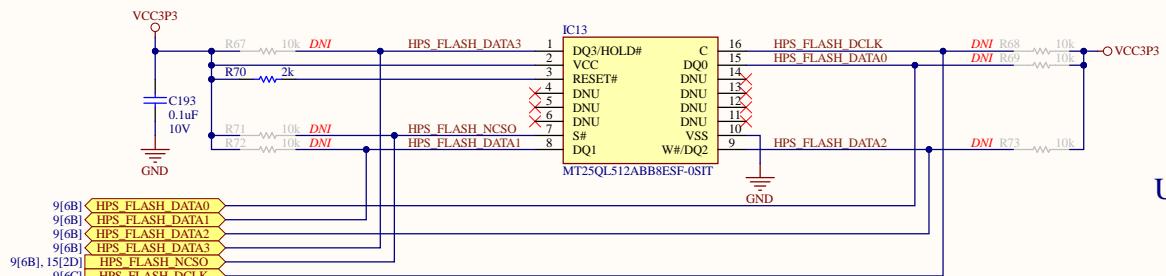
### HPS Warm Reset



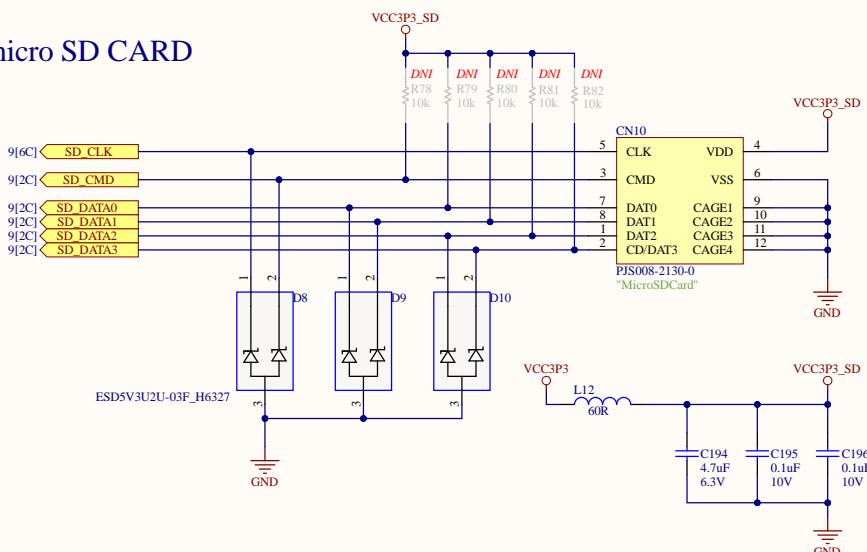
Approval	File Name	Title	Sheet
H.Sato	Clk_Rst.SchDoc	Yonaguni	
Inspection	Date		
	2022/1/12	Size	A3
Drawing			
Hirokatsu Tsuchiya		DWG No	1113-01-YG-302-001
		Rev	2.00Draft9

## Boot Device

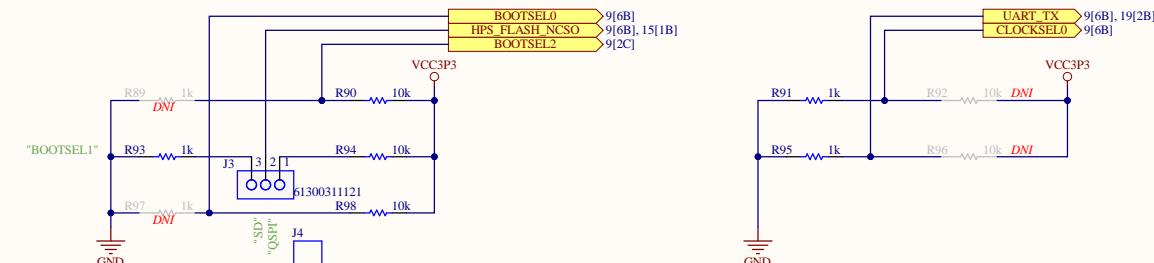
### Quad SPI Flash



### micro SD CARD

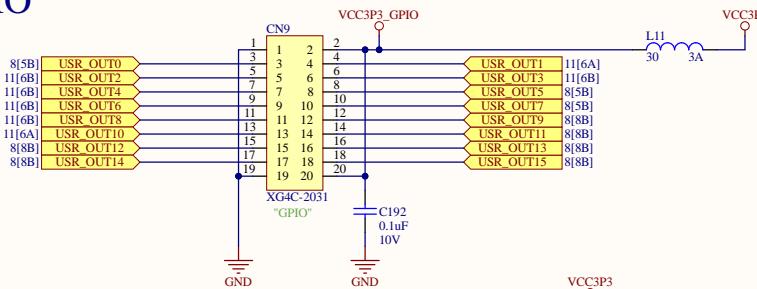


### Boot Select

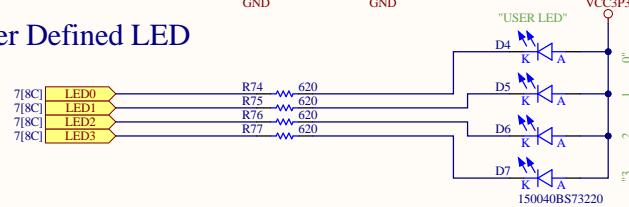


### Clock Select

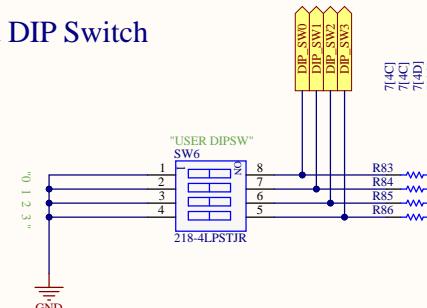
## GPIO



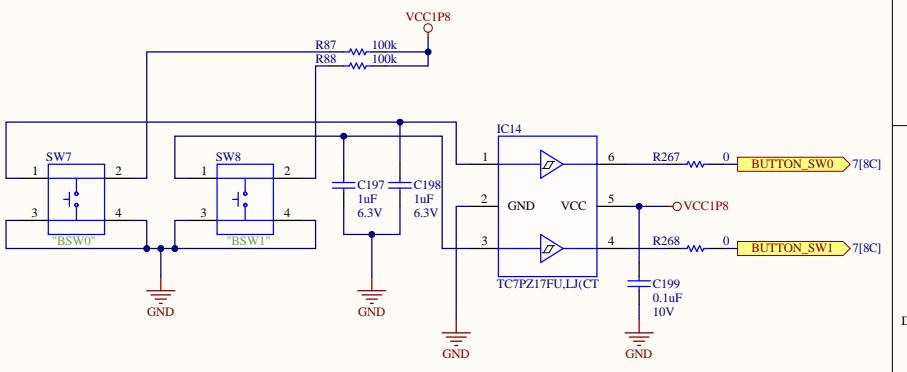
### User Defined LED



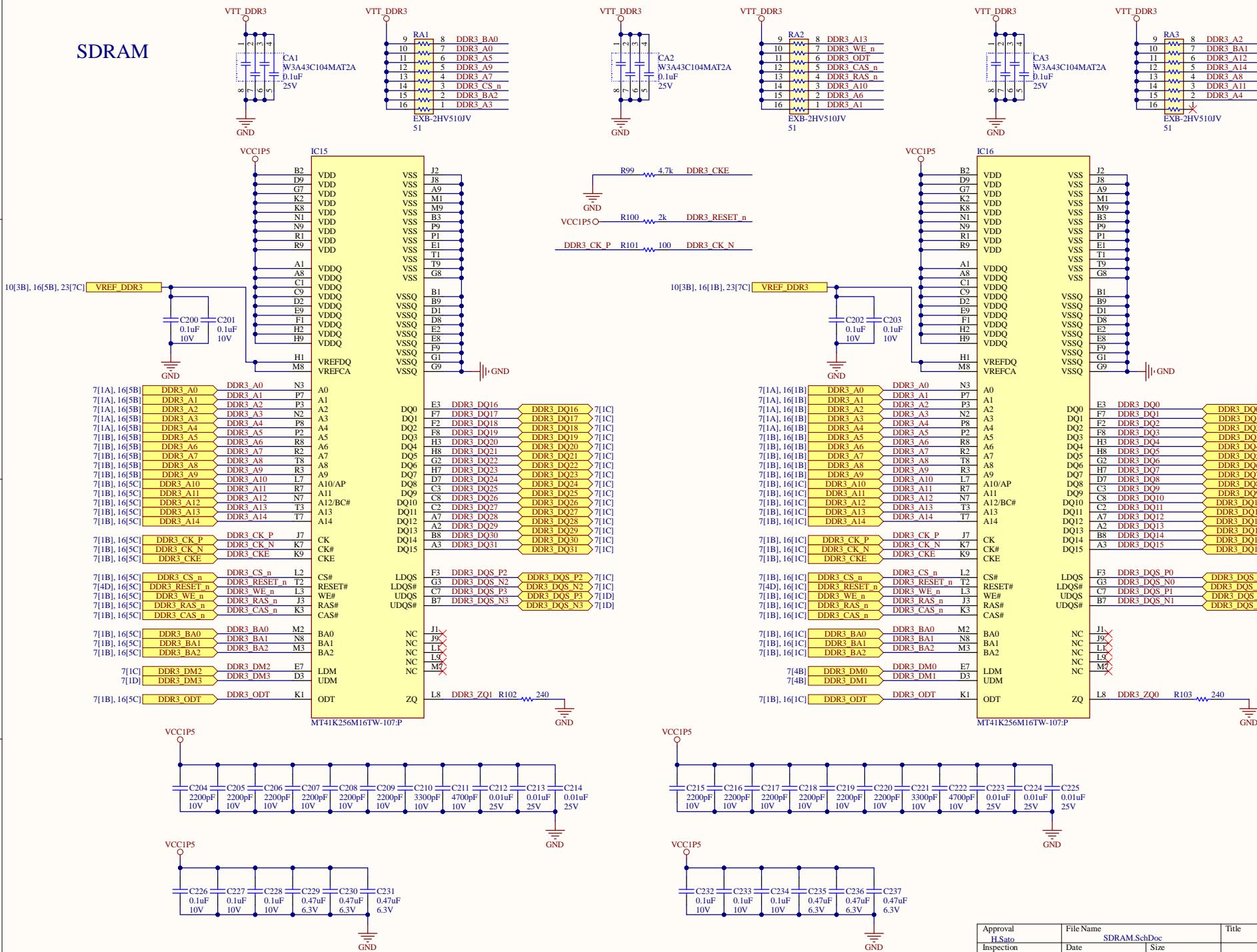
### User Defined DIP Switch



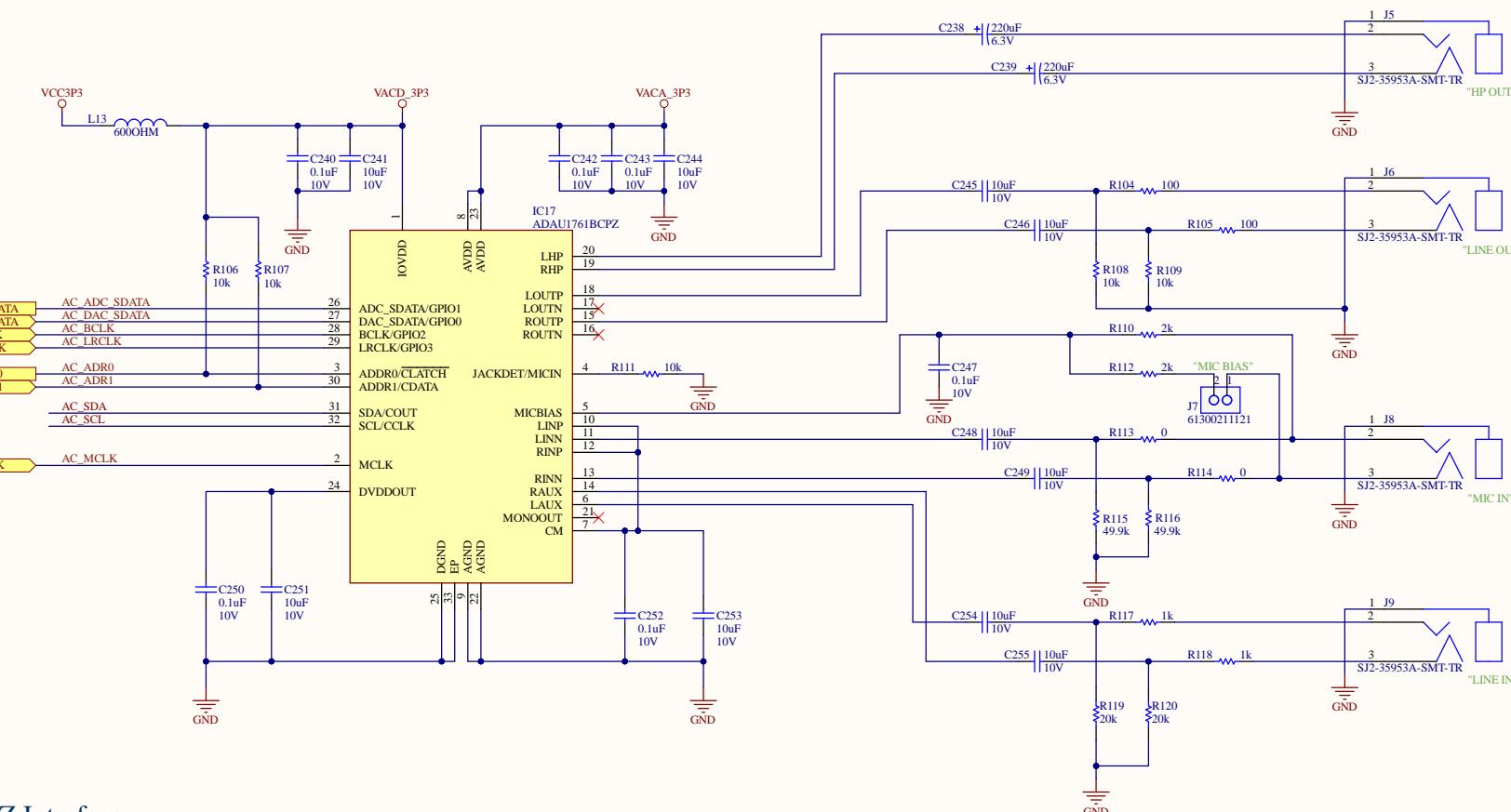
### User Defined Button Switch



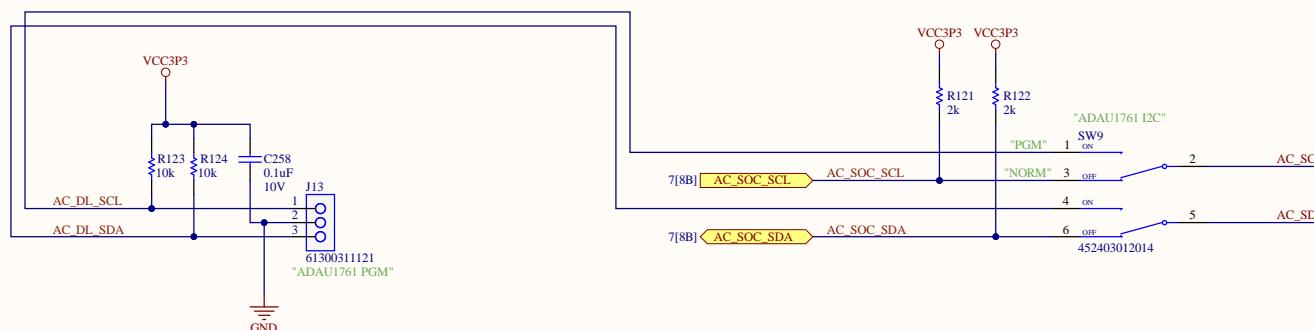
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Inspection	Date 2022/1/12	Size A3
Drawing	Drawing Hiroyuki Tsuchiya	DWG No 1113-01-YG-302-001
		Rev 2.00Draft9

**SDRAM**

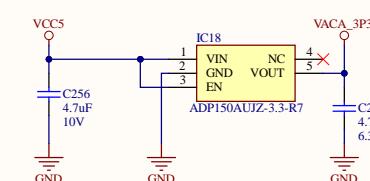
## Audio Codec



## ADUSB2EBZ Interface

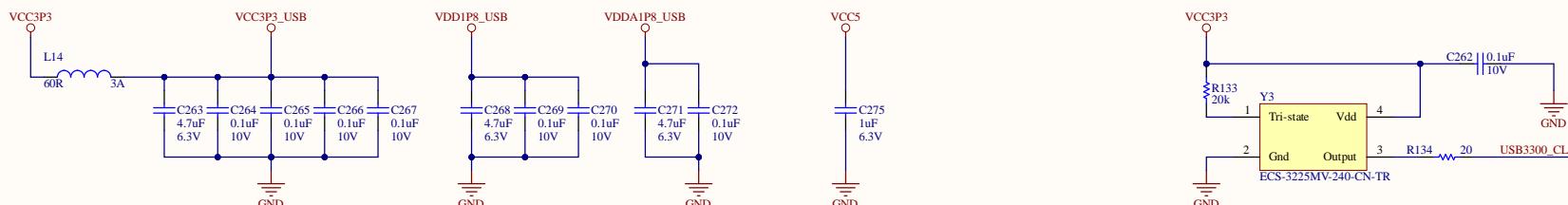
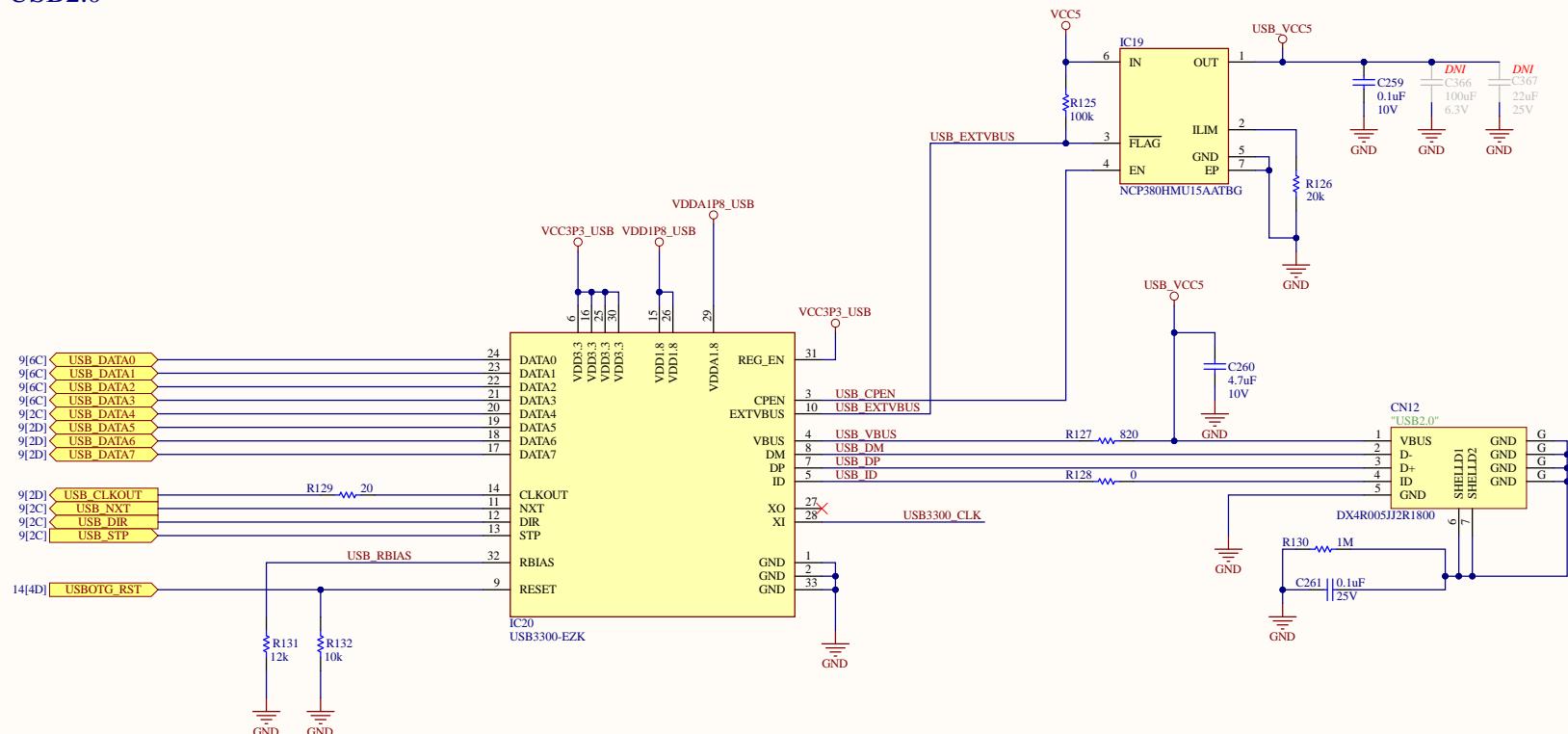


## Audio CODEC 3.3V Power Supply



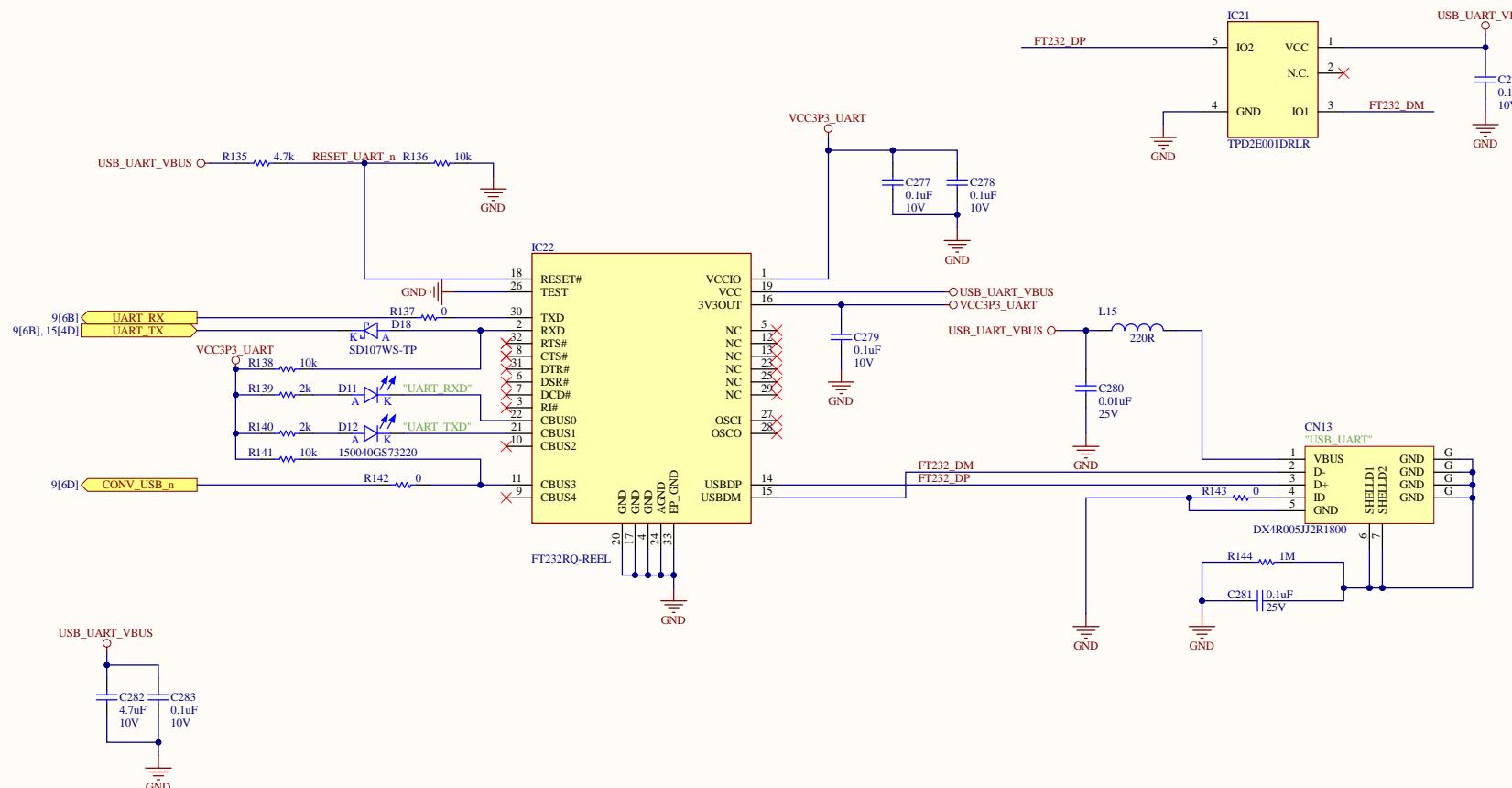
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H.Sato	AudioCodec.SchDoc	Yonaguni
Inspection	Date 2022/1/12	Size A3
H.Sato		Sheet 17 / 25
Drawing		
Hirokuni Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001
		Rev 2.00Draft9

## USB2.0



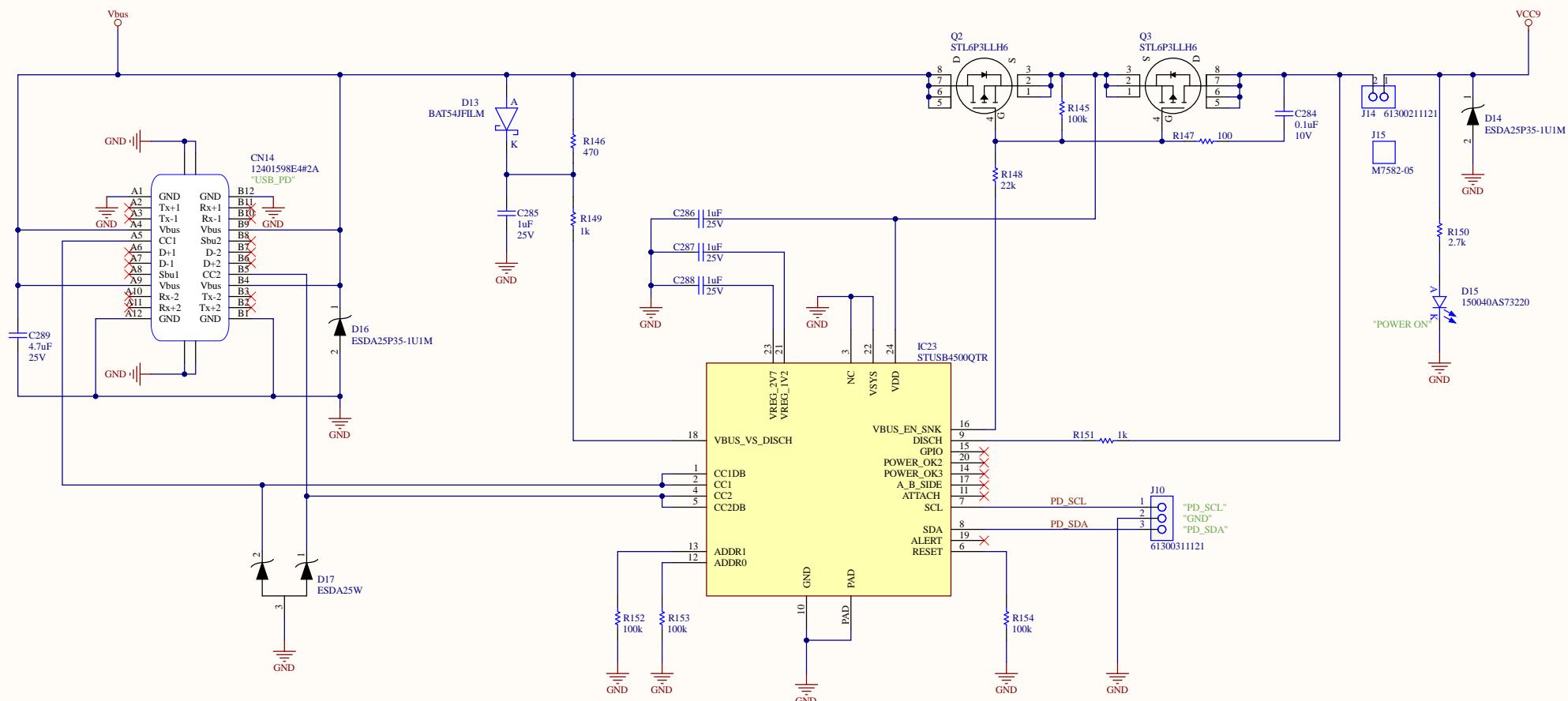
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Inspection	Date 2022/1/12	Size A3
Drawing	Rev 2.00Draft9	Sheet 18 / 25
Hiroaki Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001

## USB UART



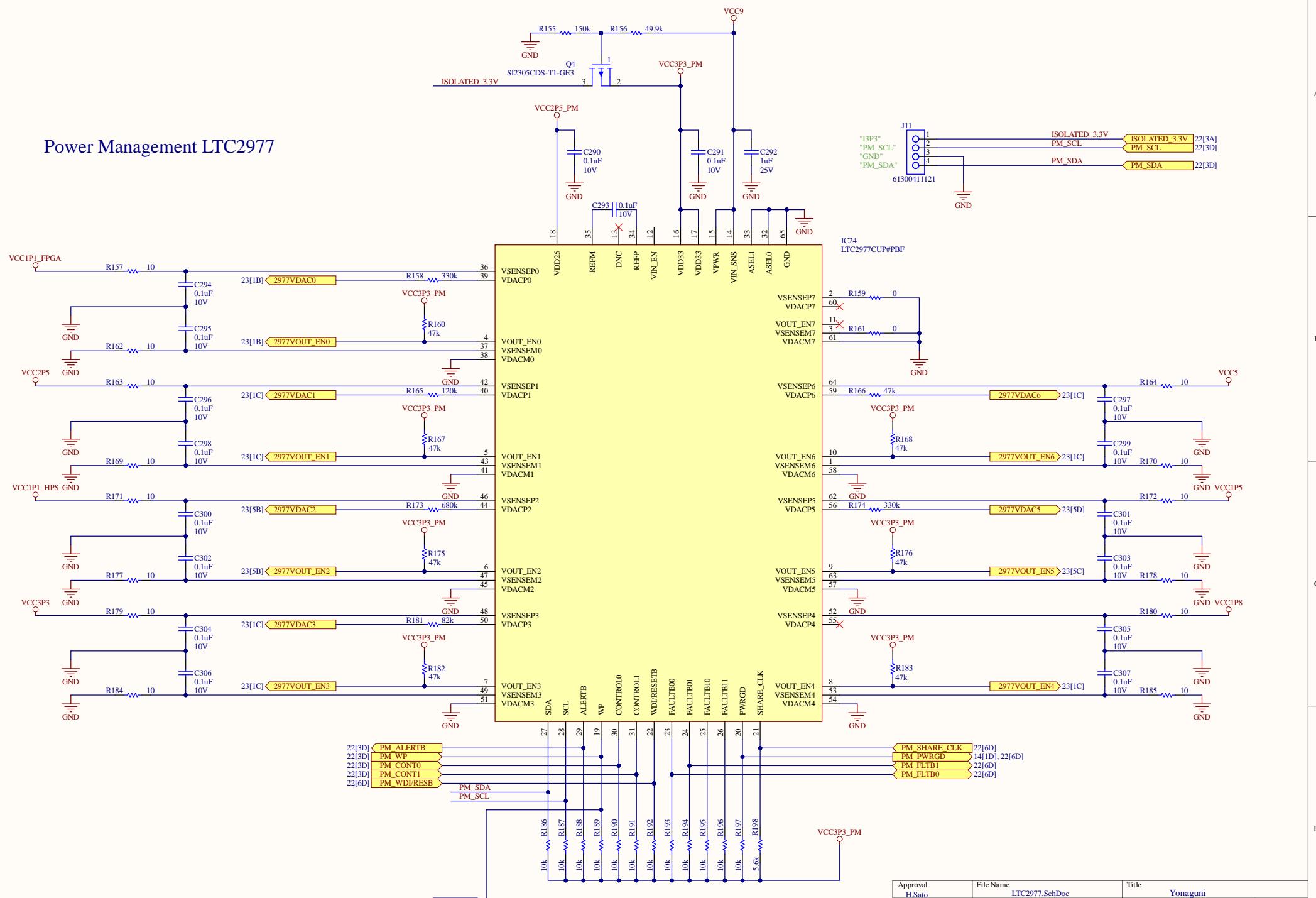
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Inspection H.Sato	Date 2022/1/12	Size A3
Drawing Hiroyuki Tsuchiya		Sheet 19 / 25
	DWG No 1113-01-YG-302-001	Rev 2.00Draft9

## USB Power Delivery

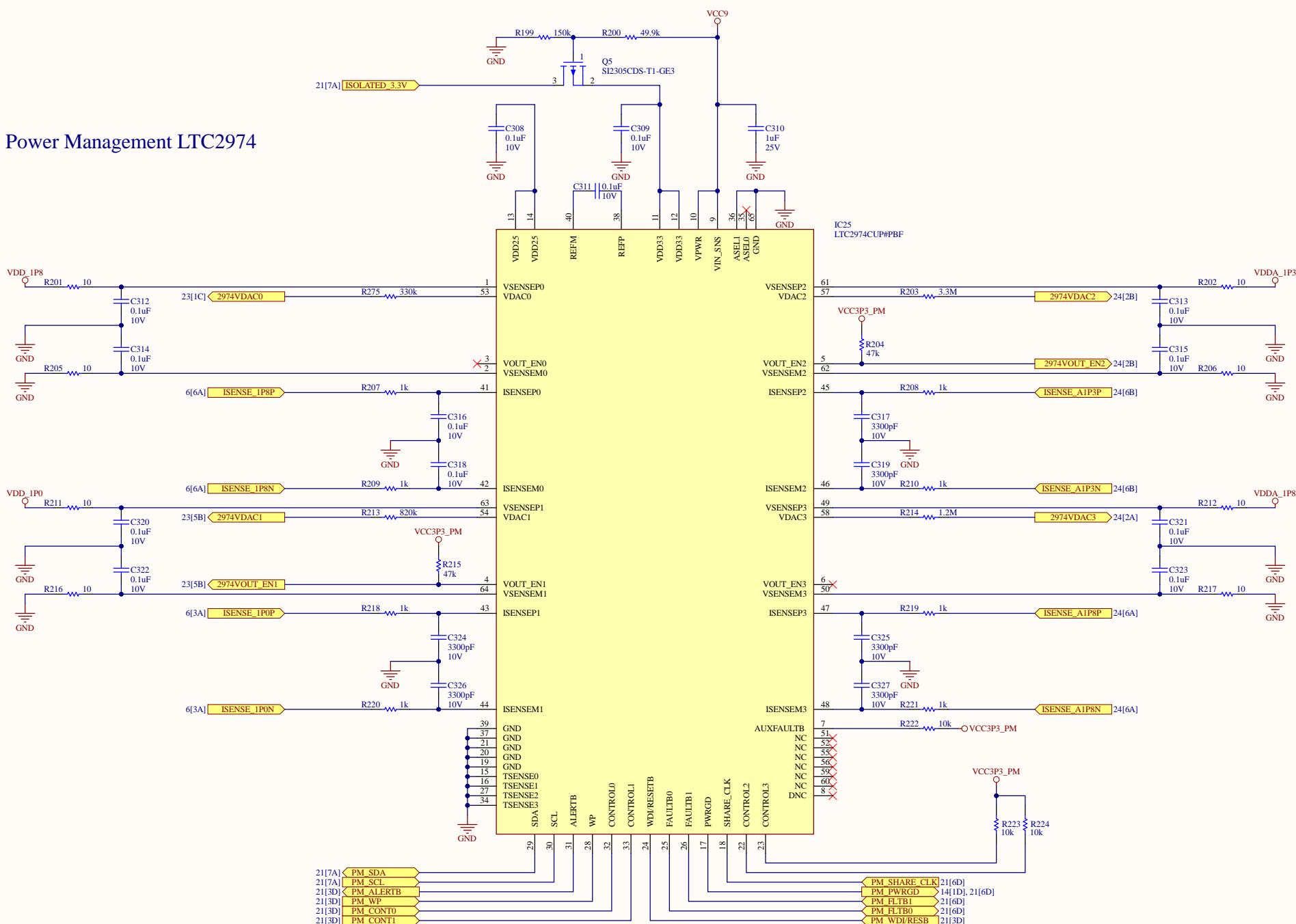


Approval H.Sato	File Name USBDPD.SchDoc	Title Yonaguni	Sheet 20 / 25
Inspection H.Sato	Date 2022/1/12	Size A3	
Drawing Hiroyuki Tsuchiya	DWG No 1113-01-YG-302-001	Rev 2.00Draft9	

## Power Management LTC2977

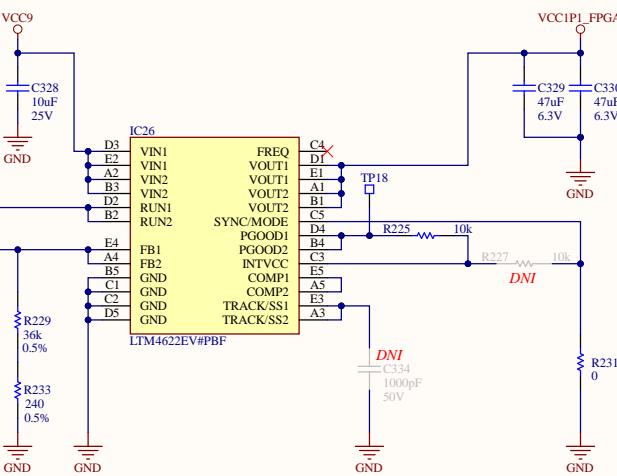


## Power Management LTC2974

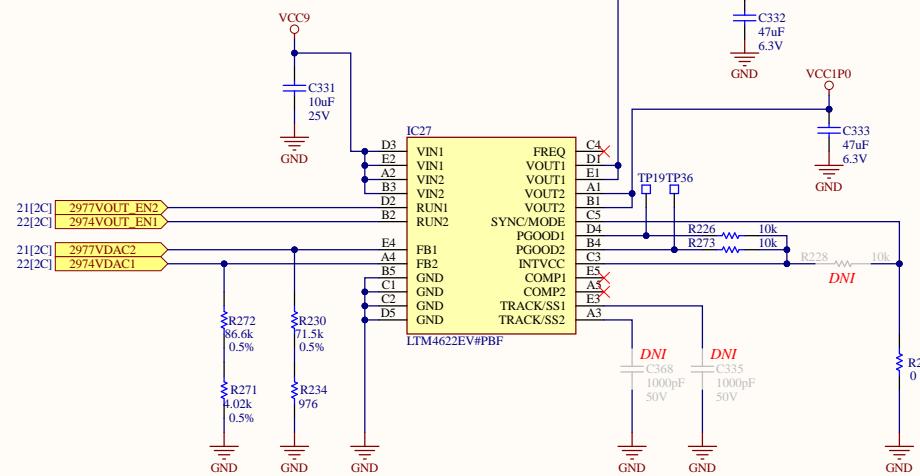


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H.Sato	LTC2974.SchDoc	Yonaguni	
Inspection	Date 2022/1/12	Size A3	22 / 25
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Hirokuya Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001	Rev 2.00Draft9

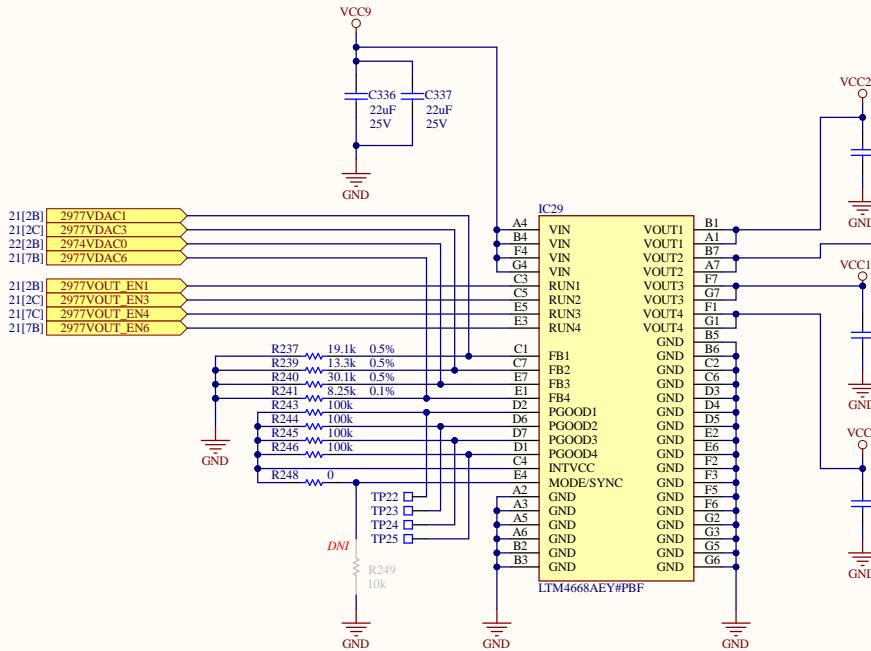
## FPGA 1.1V Power Supply



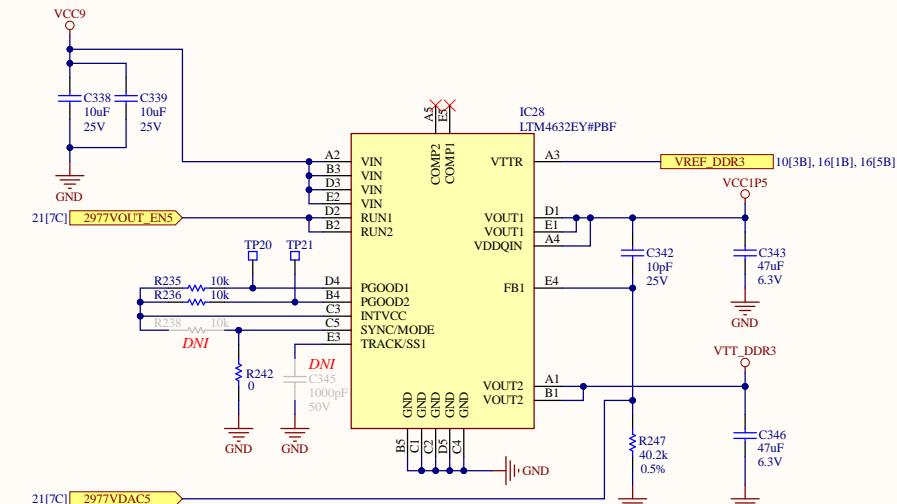
## HPS 1.1V Power Supply



## SoC/Audio CODEC/Navassa 5V, 3.3V, 2.5V, 1.8V Power Supply

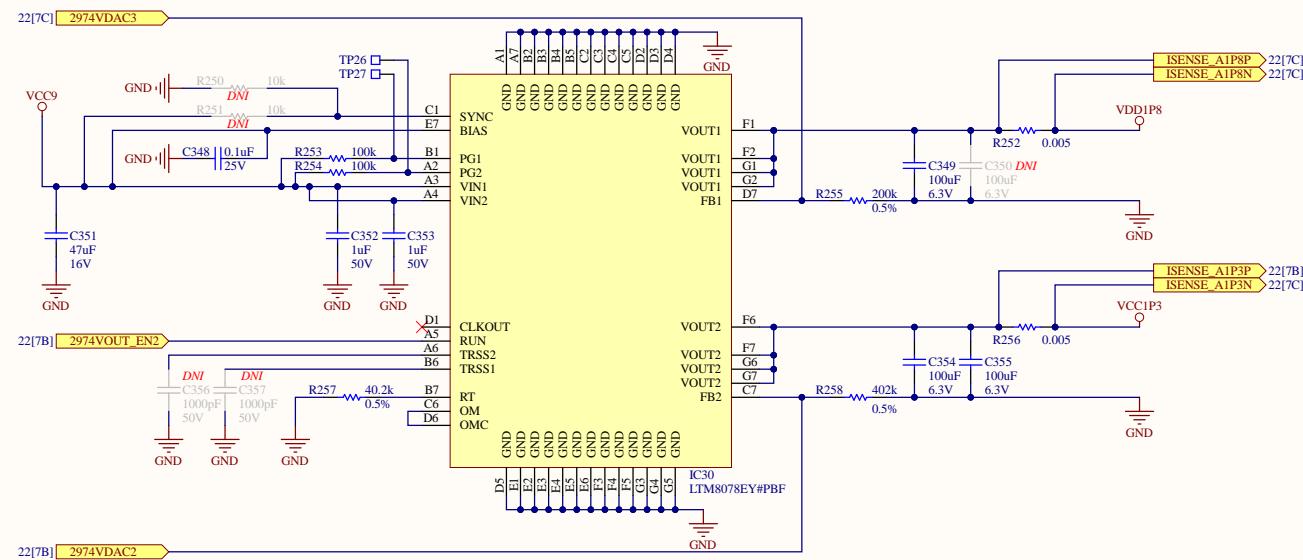


## DDR3 1.5V Power Supply



Approval H.Sato	File Name Power1.SchDoc	Title Yonaguni
Inspection H.Sato	Date 2022/1/12	Size A3
Drawing Hiroyuki Tsuchiya	DWG No 1113-01-YG-302-001	Sheet 23 / 25 Rev 2.00Draft9

## Navassa 1.8V, 1.3V Power Supply



Approval	File Name	Title
H.Sato	Power2.SchDoc	Yonaguni
Inspection	Date 2022/1/12	Size A3
Drawing	Rev 2.00Draft9	DWG No 1113-01-YG-302-001
Hirofumi Tsuchiya	Marimo Electronics Co.,Ltd.	

<b>Missing Parts Number</b>		<b>Revision</b>	<b>History of revisions</b>	A	B	C	D
C54,C159,C161,C273,C274,C358,C359, C360,C361,C362,C363,C364,C365, R17,R18,R19,R20,R21,R259,R260, R261,R262,R263,R264,R265,R266, E11,E14,E15,TP8,TP9,TP10,TP11,TP28, CN11,L16,IC31	1.01~1.18		<ul style="list-style-type: none"> <li>• See Yonaguni schematic R1.18</li> </ul>				
		2.00	<ul style="list-style-type: none"> <li>• Changed R6 to 1.5kΩ 0402size. (p.1)</li> <li>• Changed Z6,Z7,Z28,Z29,Z53,Z55 to 0.2pF. (p.2)</li> <li>• Changed Z16,Z38 to 0.6pF. (p.2)</li> <li>• Changed Z14,Z36,Z50,Z52 to 0Ω. (p.2)</li> <li>• Changed Z58,Z60 to DNI. (p.2)</li> <li>• Changed IC3 to ADRV9002BBCZ. (p.4, p.5)</li> <li>• Removed Port VRFVCO1_IP0, VRFVCO2_IP0. (p.5)</li> <li>• Added R274 and Port ISENSE_1POP, ISENSE1P0N. (p.6)</li> <li>• Moved R16 between TP3 and E3 and added (p.6)</li> <li>• Added Port ISENSE_1P8P, ISENSE1P8N.(p.6)</li> <li>• Added VDD1P8 and TP35 connecting to E7.(p.6)</li> <li>• Removed R17, R18, and connect VDDA_1P3 to E9. (p.6)</li> <li>• Removed TP10, TP11, E11, R19, R20, R21, C54. (p.6)</li> <li>• Added R270 connecting to IC4A V10 pin.(p.7)</li> <li>• Changed R34 to 2kΩ 0402size, R35 to 0402size, and R269 to 0201 size. (p.11)</li> <li>• Changed D2 to sink drive. (p.11)</li> <li>• Changed the part model name of SW3, SW4, SW5. (p.14)</li> <li>• Changed D4-D7 to sink drive and R74-R77 to 620Ω 0402size. (p.15)</li> <li>• Changed IC19 to NCP380HMU15AATBG and C375 to 1uF 6.3V. (p.18)</li> <li>• Added C366 and C367. (p.18)</li> <li>• Changed R139 and R140 to 2kΩ 0402size. (p.19)</li> <li>• Changed R150 to 0402size. (p.20)</li> <li>• Changed R173 to 680kΩ. (p.21)</li> <li>• Changed IC25 ch0 to Monitoring of VDD_1P8. (p.22)</li> <li>• Removed 2974VOUT_EN0 and added 2974VOUT_EN2. (p.22)</li> <li>• Moved R204 to pullup of 2974VOUT_EN2. (p.22)</li> <li>• Changed to 820kΩ. R213, and 1.2MΩ. R214. (p.22)</li> <li>• Changed IC27 to generating VCC1P1_HPS and VCC1P0. (p.23)</li> <li>• Changed IC29 E7 pin to connecting to 2974VDAC0. (p.23)</li> <li>• Changed R227,R228,R231,R232,R238,R242,R248,R249 to 0402size. (p.23)</li> <li>• Changed C334,C335,C345 to 0402size. (p.23)</li> <li>• Changed IC30 to generating VDD1P8. (p.24)</li> <li>• Changed IC30 A5 pin to connecting to 2974VOUT_EN2. (p.24)</li> <li>• Changed R250,R251,C356,C357 to 0402size. (p.24)</li> <li>• Removed LT8625S and its peripheral circuits. (p.24)</li> <li>• Added R275. (p.22)</li> <li>• Branched VDDA_1P3 to 15 power supply lines. (p.5, p.6)</li> <li>• Changed VCC5 to USB_UART_VBUS. (p.19)</li> <li>• Changed R138 0Ω dumping to 10kΩ pullup, and added D18 (p.19)</li> <li>• Deleted "DNI" of C37, C43, C269-C281 (p.6)</li> </ul>				

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Drawing Hiroyuki Tsuchiya		DWG No <a href="#">1113-01-YG-302-001</a>	Rev 2.00Draft9