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A

Yonaguni

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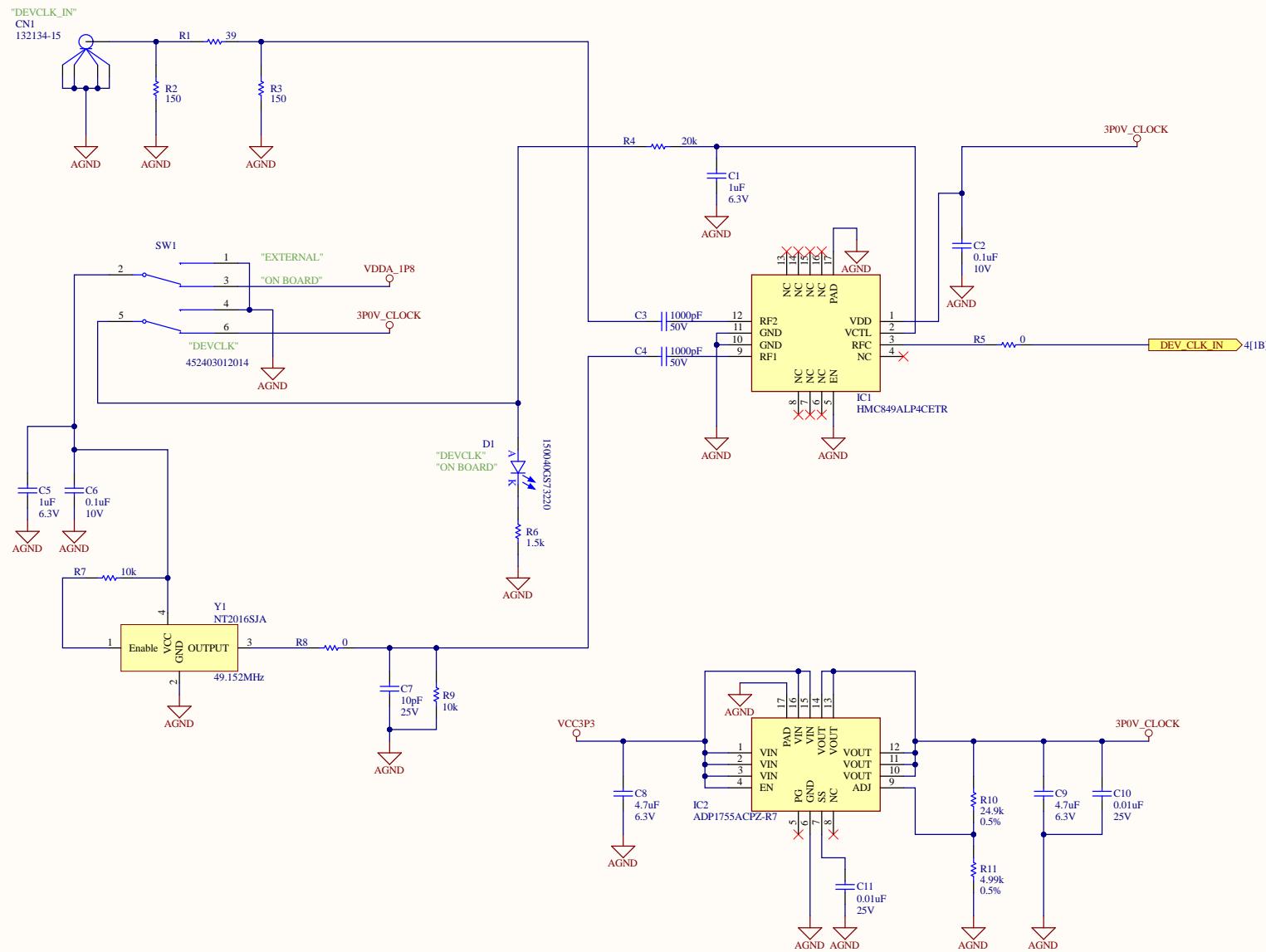
D

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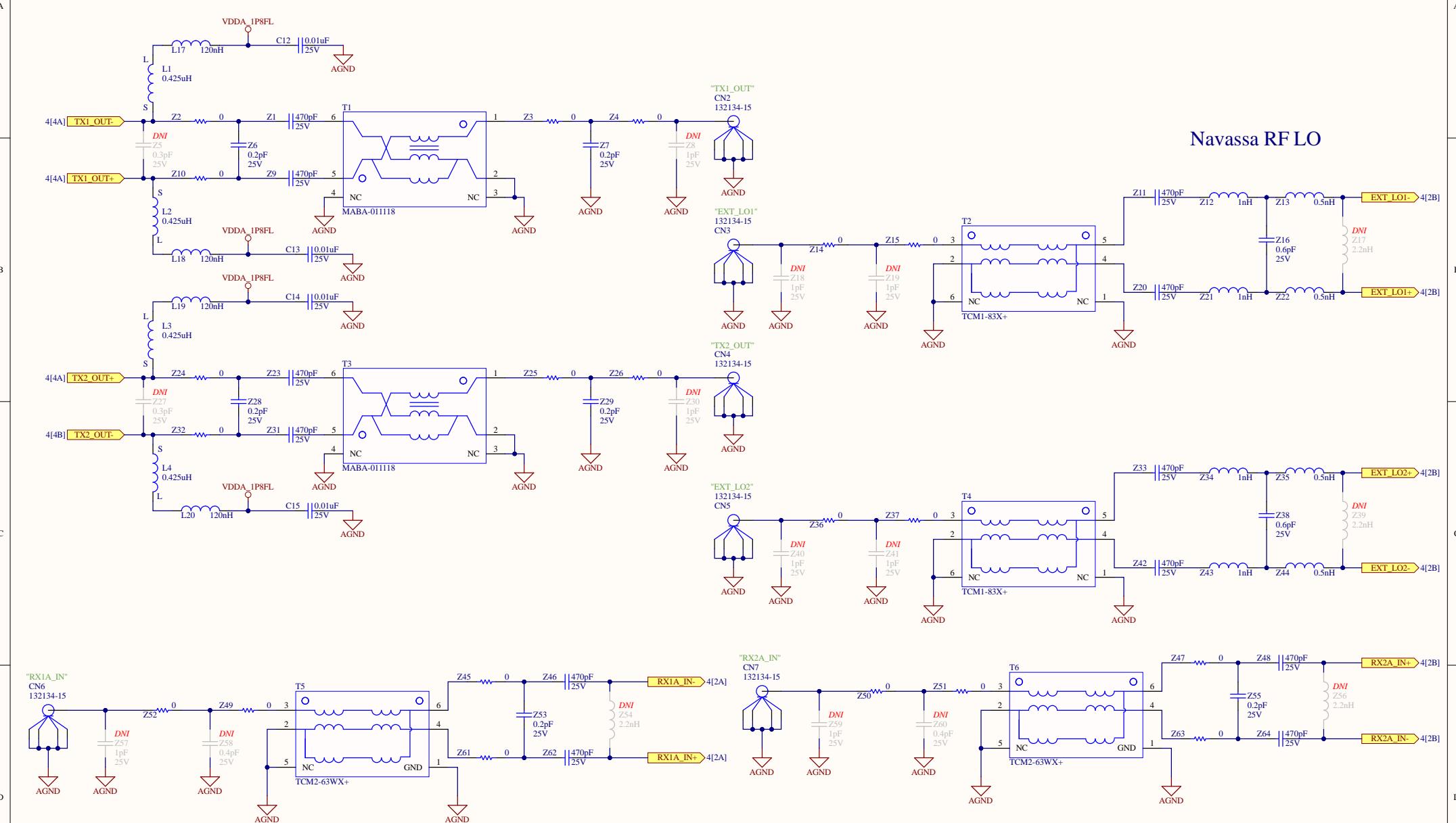
Approval H.Sato	File Name Index.SchDoc	Title Yonaguni	Sheet 1 / 25
Inspection H.Sato	Date 2022/7/19	Size A3	
Drawing Hiroyuki Tsuchiya		DWG No 1113-01-YG-302-001	Rev 2.02

Navassa Clock



Approval	File Name	Title
H.Sato	Navassa_Clk.SchDoc	Yonaguni
Inspection	Date 2022/7/19	Size A3
Drawing	Rev 2.02	Sheet 2 / 25
Hirofumi Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001

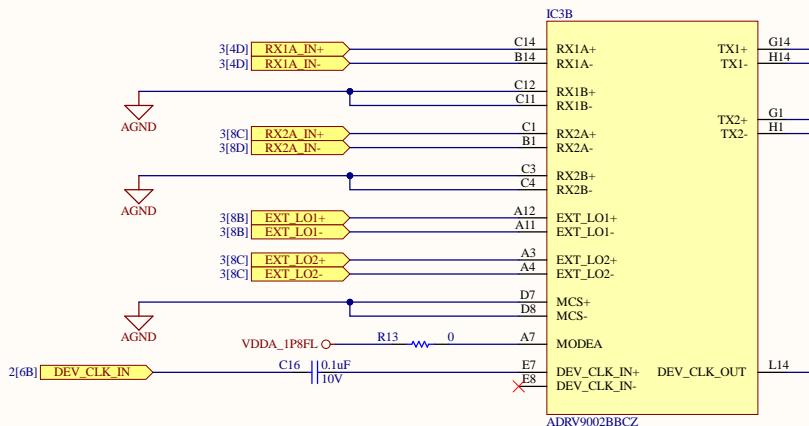
Navassa RF TX, RX



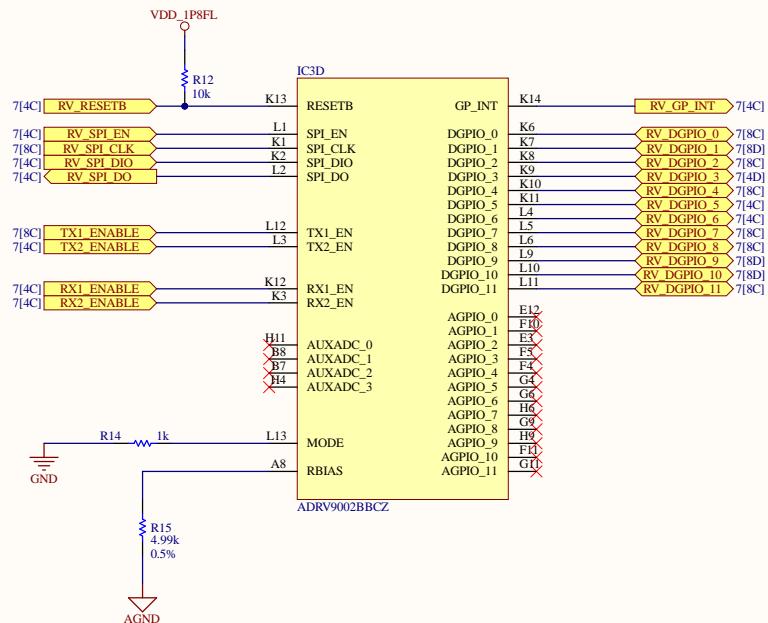
Approval	File Name	Title
H.Sato	Navassa_TXRX_RFLO.SchDoc	Yonaguni
Inspection	Date 2022/7/19	Sheet 3 / 25
Drawing	Size A3	
Hirofumi Tsuchiya	DWG No 1113-01-YG-302-001	Rev 2.02
Marimo Electronics Co.,Ltd.		

Navassa RF IO

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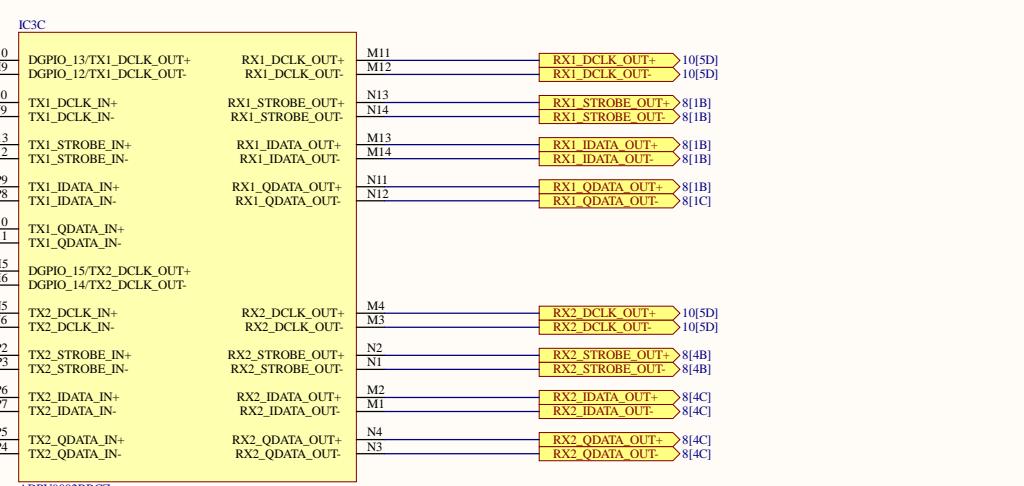
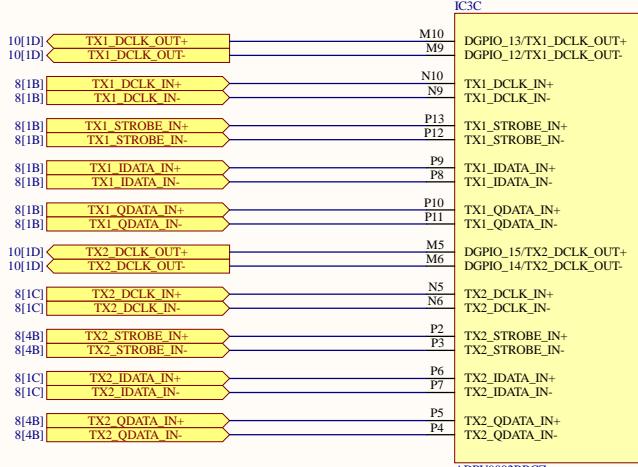


Navassa Digital I/F



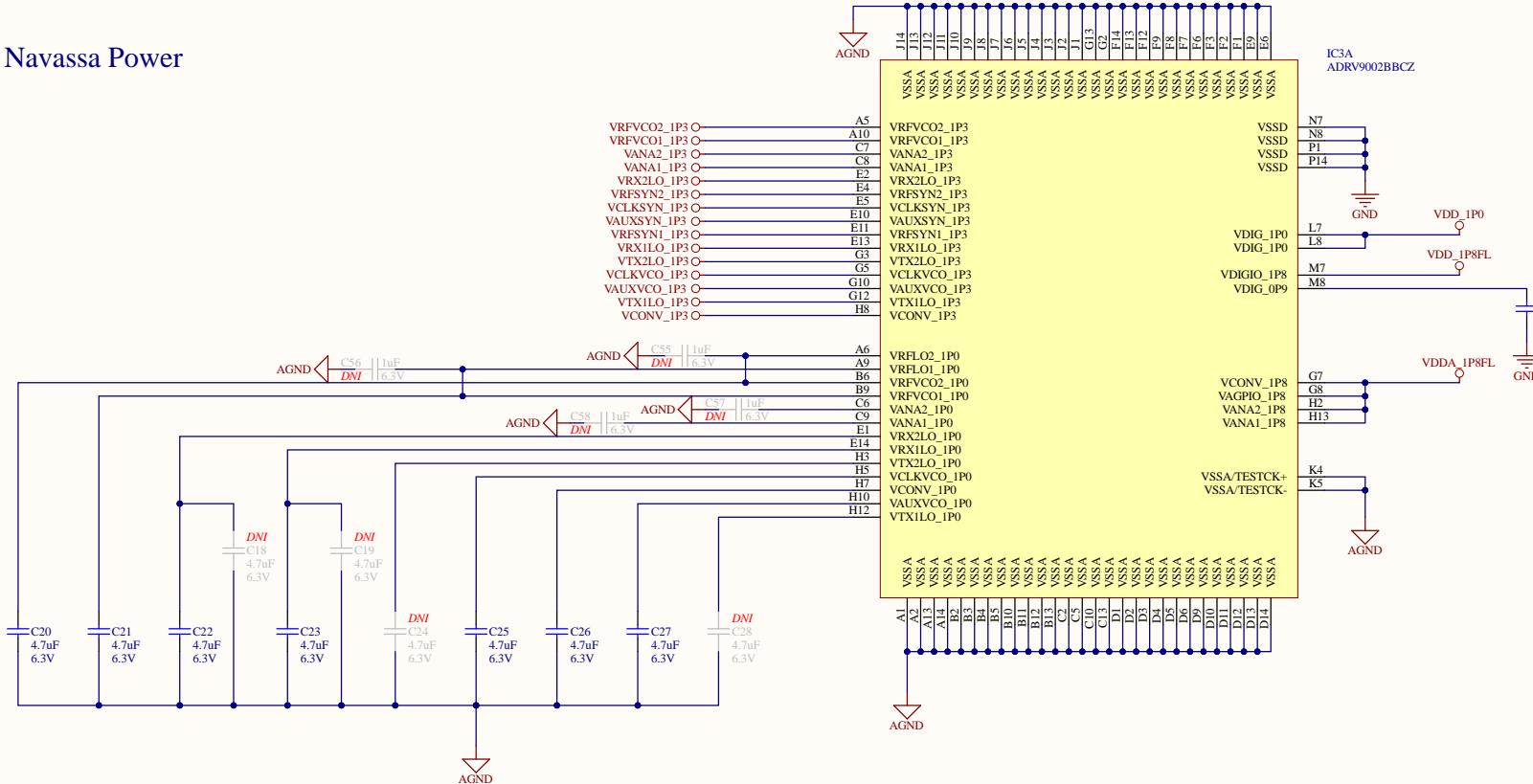
Navassa Data Port

C

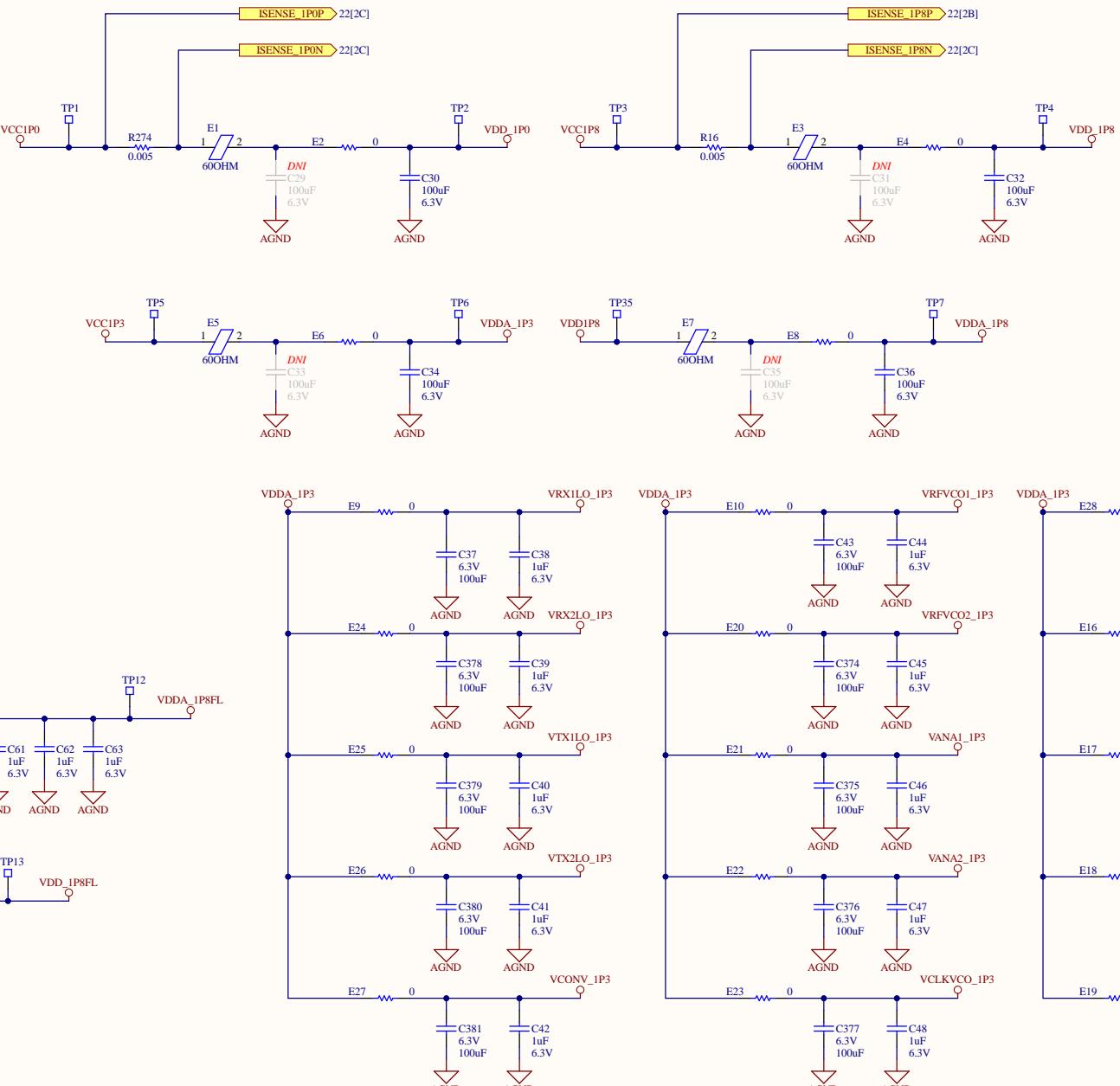


Approval	File Name	Title
H.Sato	Navassa_RFIO_DP_D_IF.SchDoc	Yonaguni
Inspection	Date 2022/7/19	Size A3
Drawing	Rev 2.02	Sheet 4 / 25
Hirofumi Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001

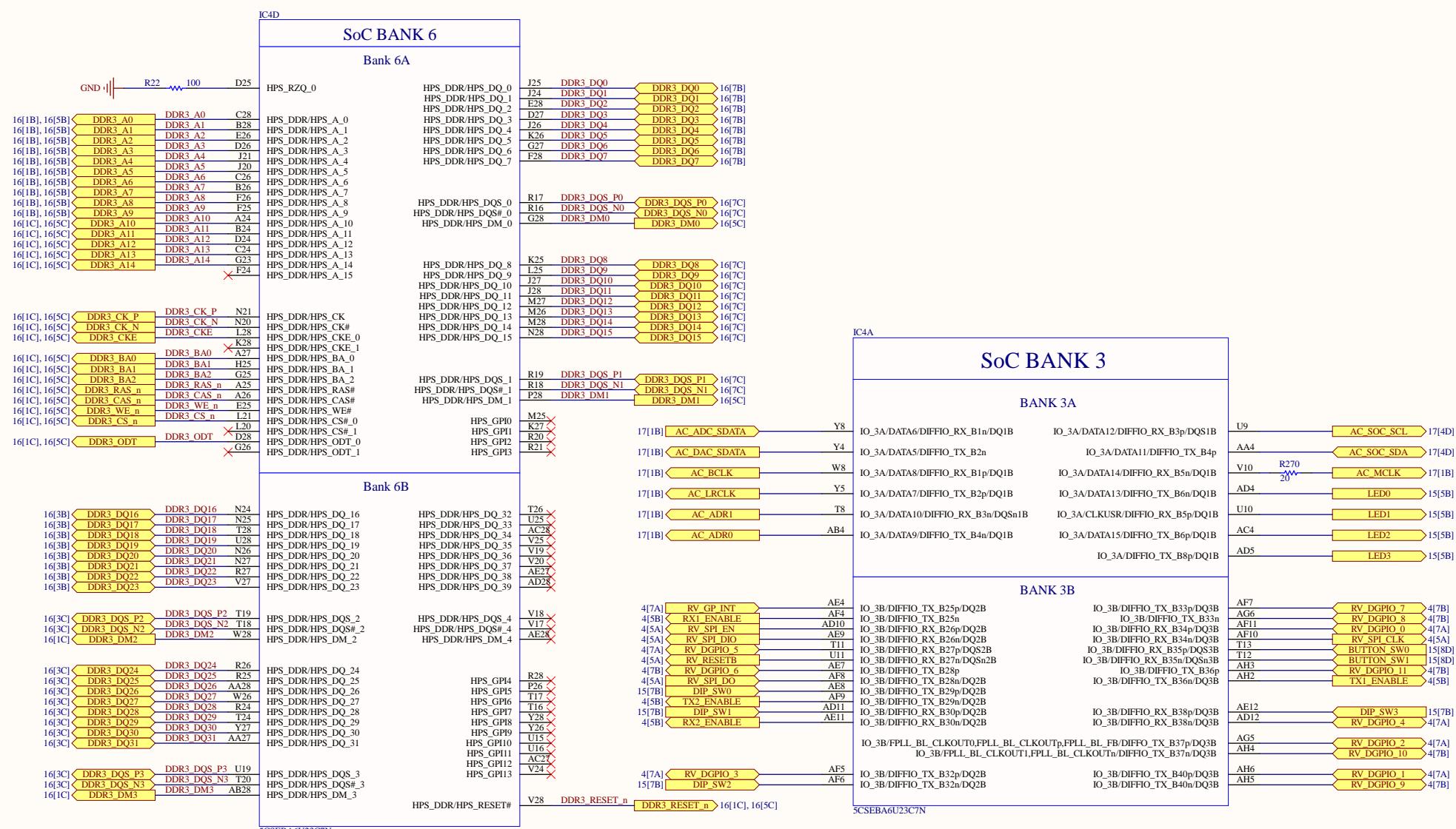
Navassa Power



Navassa Power Pin



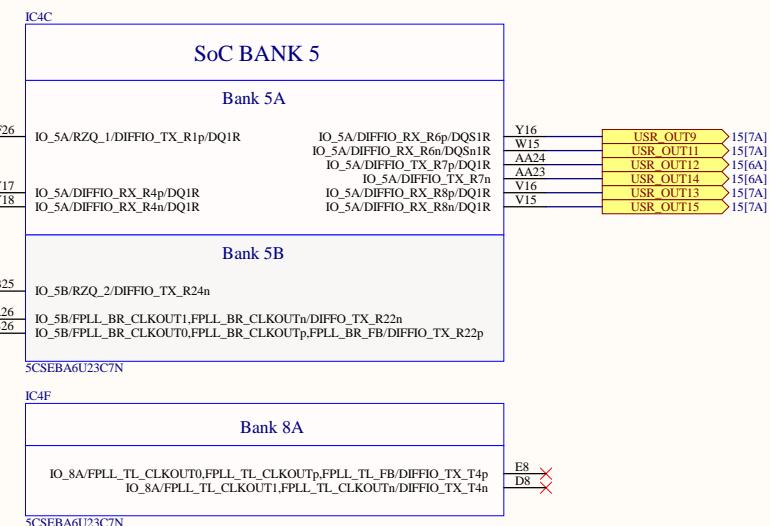
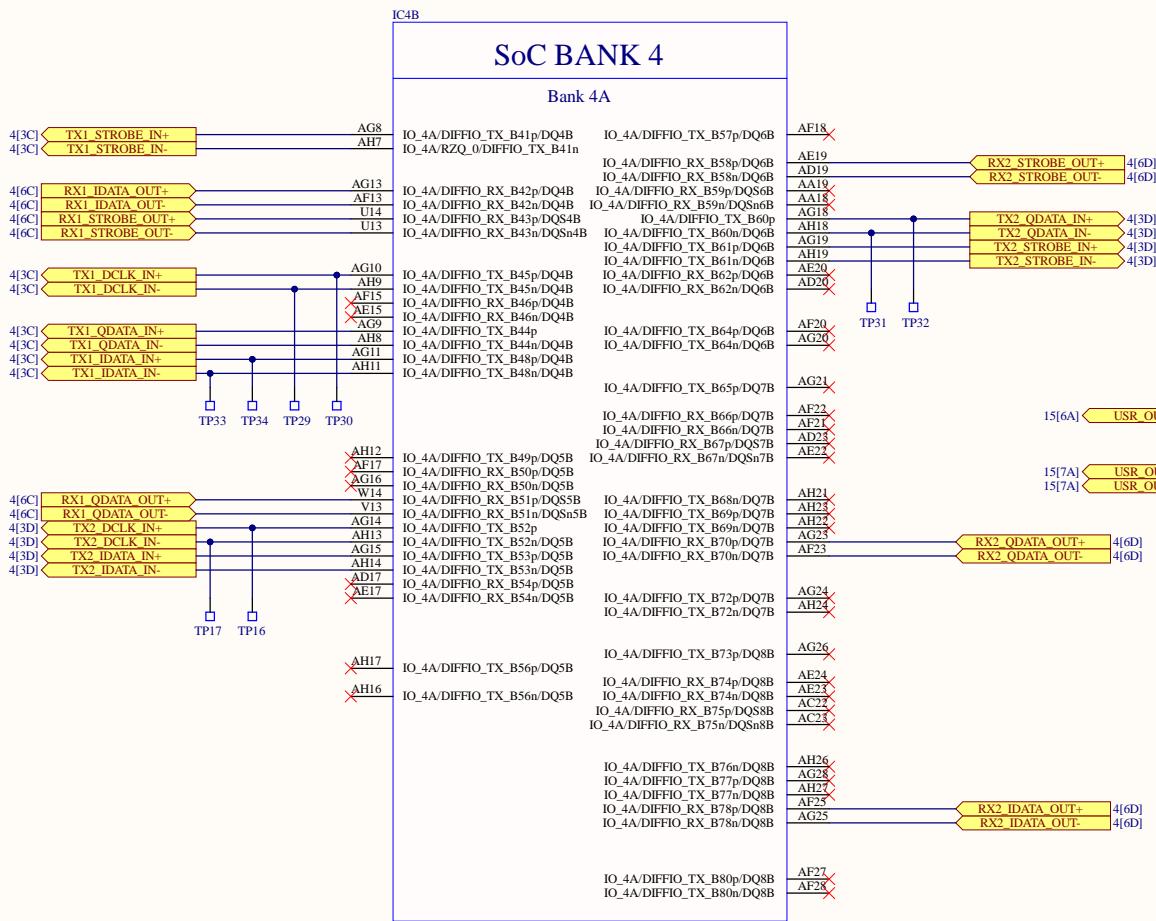
Approval H.Sato	File Name Navassa_PowerPin.SchDoc	Title Yonaguni	Sheet 6 / 25
Inspection H.Sato	Date 2022/7/19	Size A3	
Drawing Hiroyuki Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001	Rev 2.02



Approval <u>H.Sato</u>	File Name <u>SoC_BANK3&6.SchDoc</u>	Title <u>Yonaguni</u>	
Inspection <u>H.Sato</u>	Date <u>2022/7/19</u>	Size <u>A3</u>	Sheet <u>7 / 25</u>
Drawing <u>Hiroyuki Tsuchiya</u>	Marimo Electronics Co.,Ltd.	DWG No <u>1113-01-YG-302-001</u>	Rev <u>2.02</u>

A

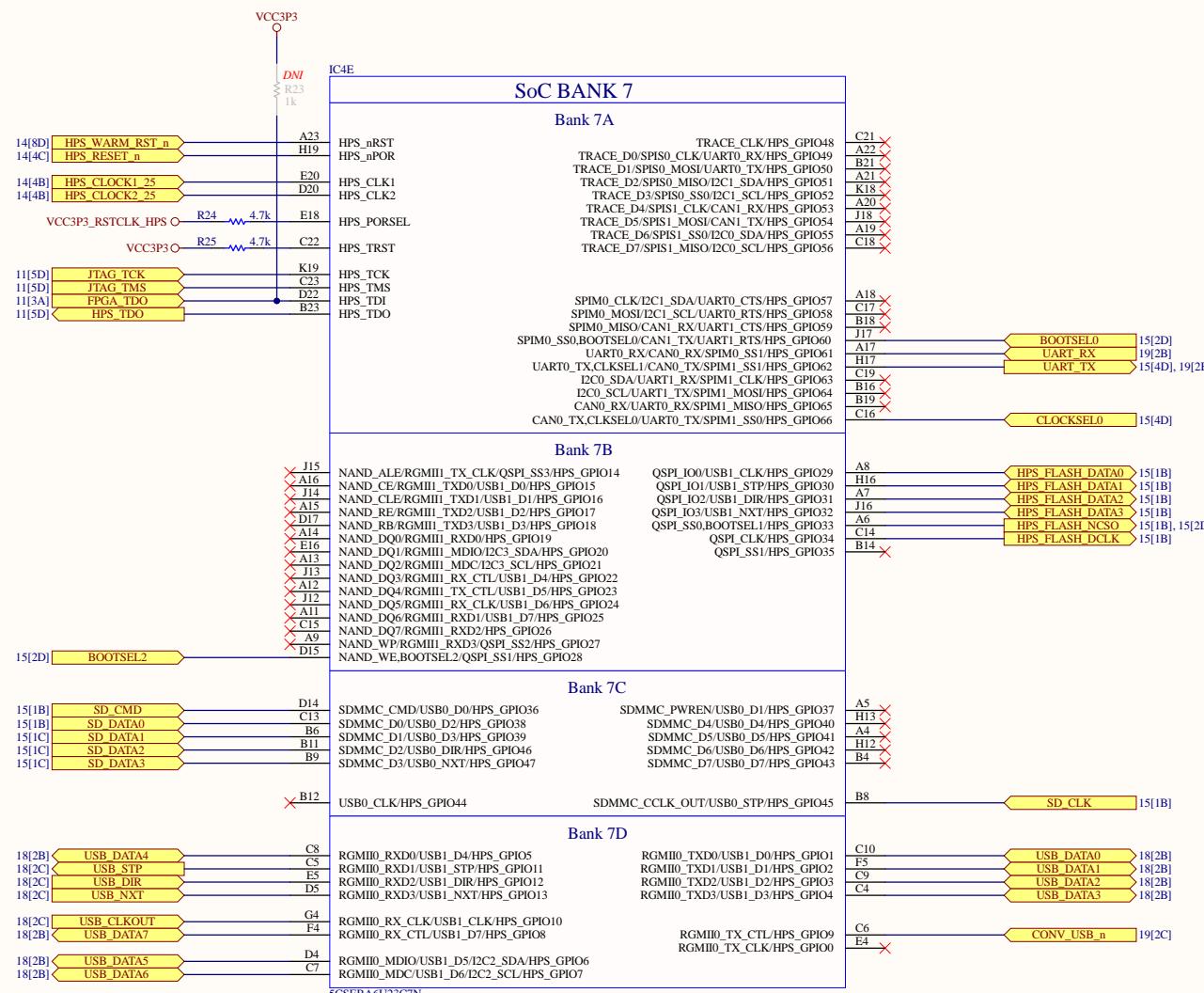
A



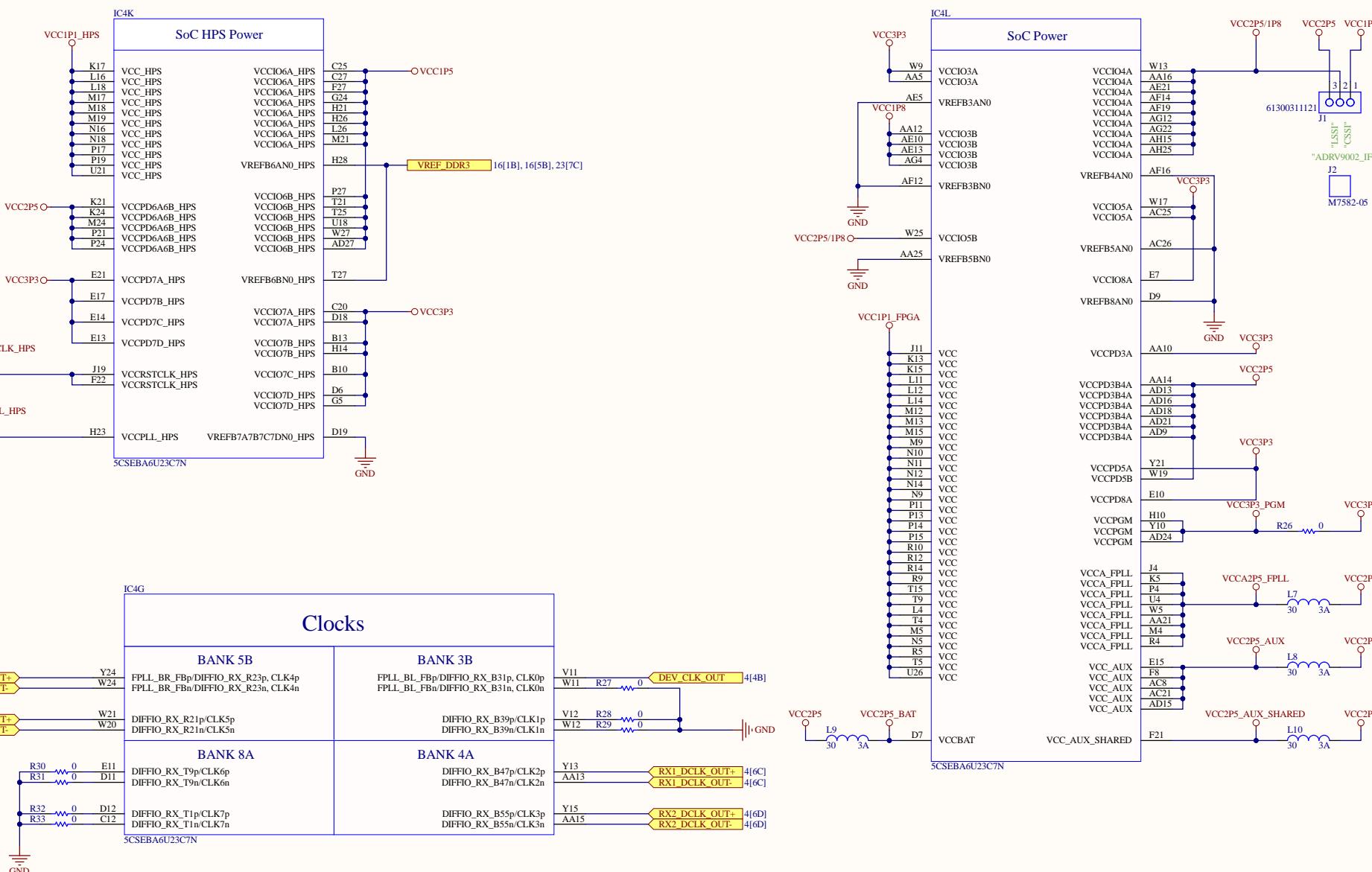
Approval	File Name	Title
H.Sato	SoC_BANK4&5&8.SchDoc	Yonaguni
Inspection	Date 2022/7/19	Size A3
H.Sato		Sheet 8 / 25
Drawing		DWG No 1113-01-YG-302-001
Hirofumi Tsuchiya	Marimo Electronics Co.,Ltd.	Rev 2.02

A

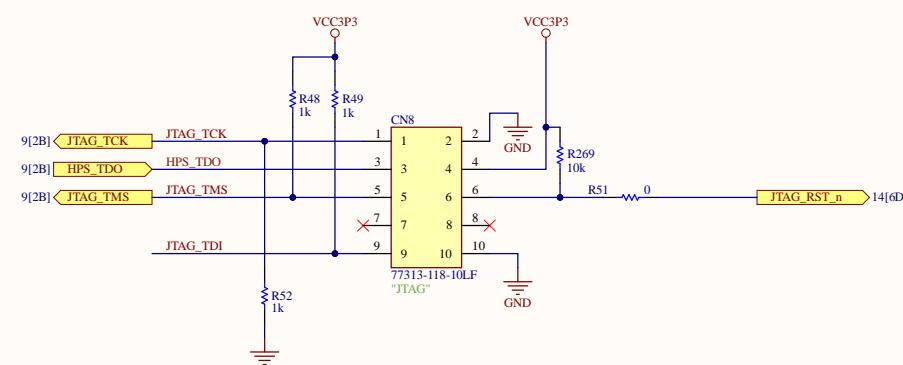
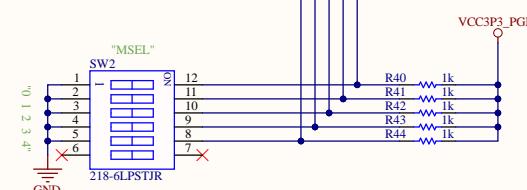
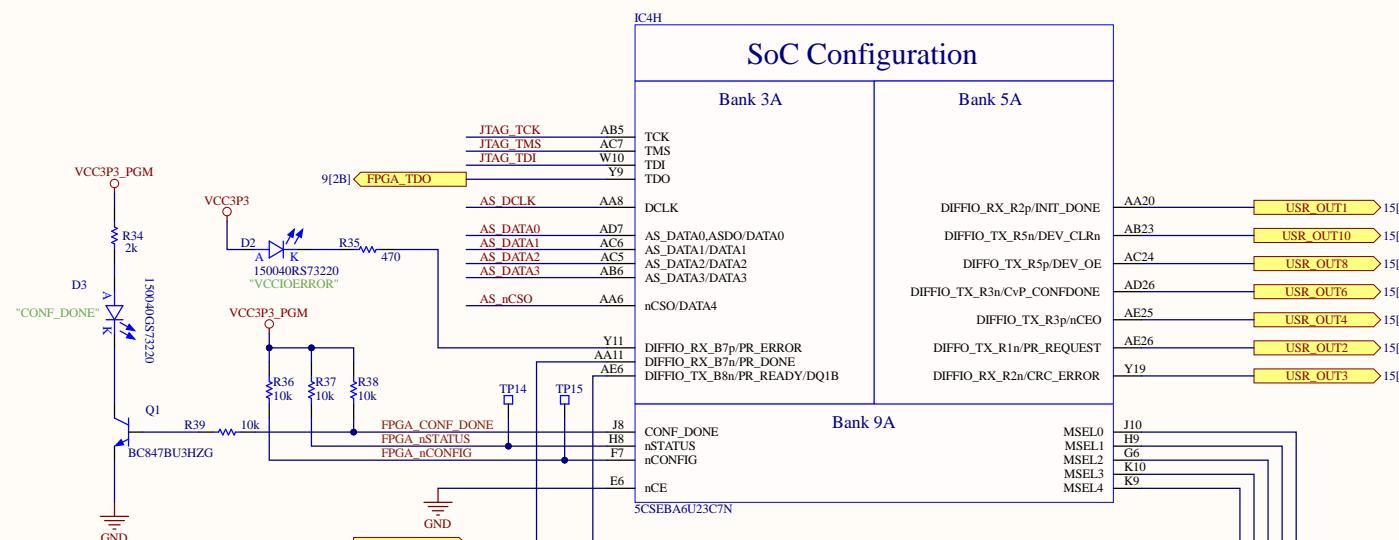
A



Approval H.Sato	File Name SoC_BANK7.SchDoc	Title Yonaguni
Inspection H.Sato	Date 2022/7/19	Size A3
Drawing Hiroyuki Tsuchiya	DWG No 1113-01-YG-302-001	Sheet 9 / 25
	Rev 2.02	



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Inspection H.Sato	Date 2022/7/19	Size A3	Rev 2.02
Drawing Hiroyuki Tsuchiya	Marimo Electronics Co., Ltd.	DWG No 1113-01-YG-302-001	



Approval	File Name	Title	Sheet
H.Sato	SoC_Conf.SchDoc	Yonaguni	
Inspection	Date 2022/7/19	Size A3	11 / 25
Drawing			
Hirokazu Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001	Rev 2.02

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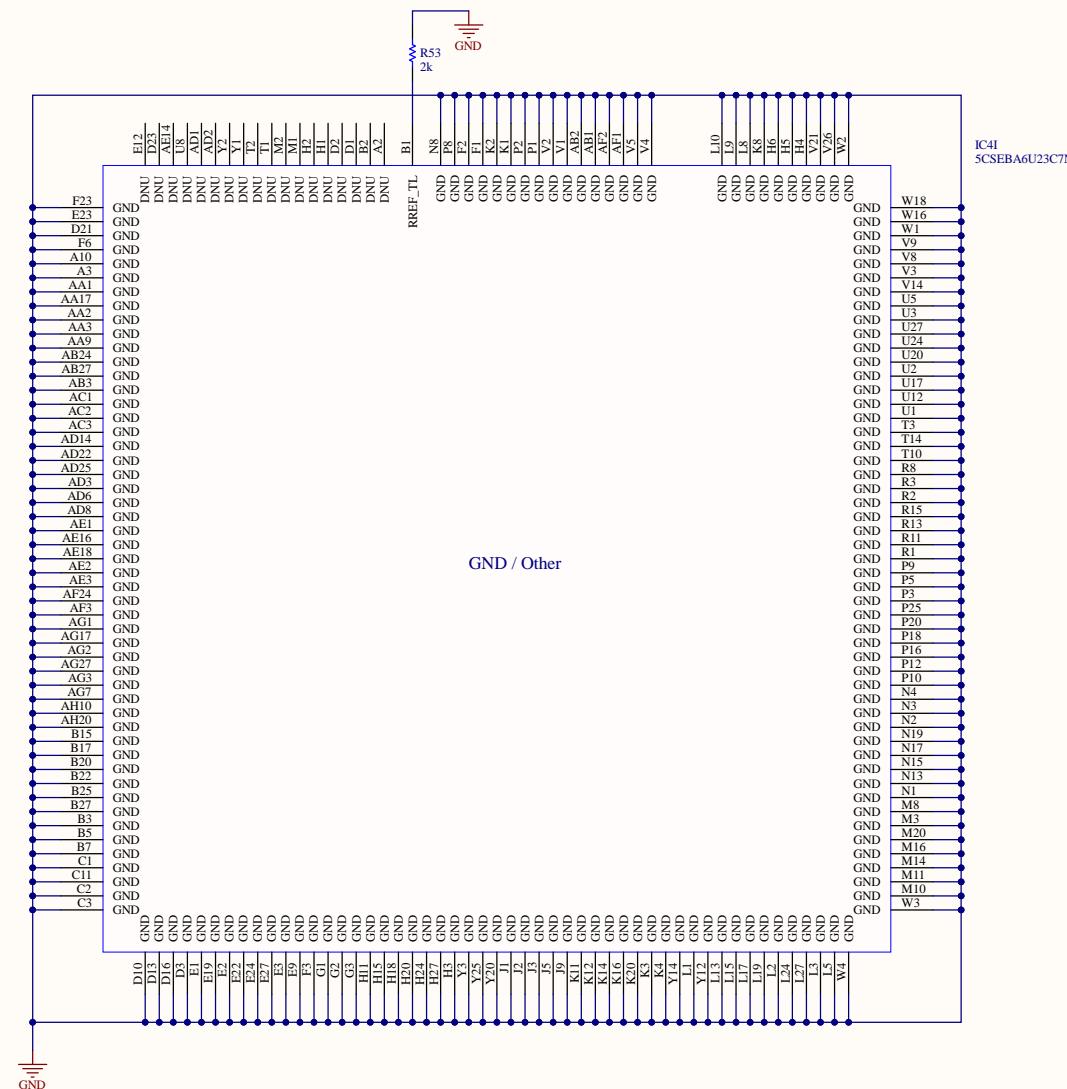
B

C

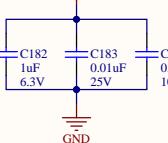
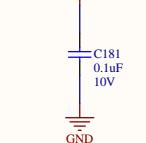
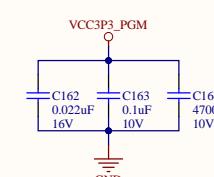
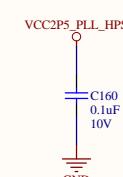
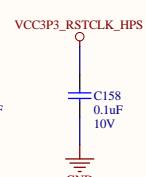
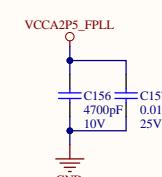
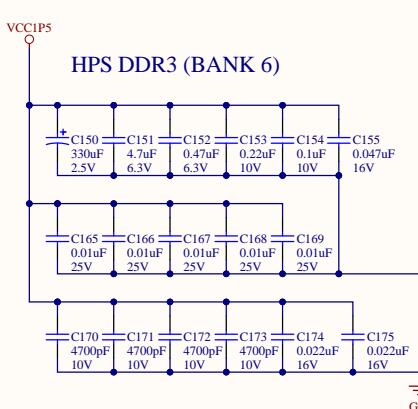
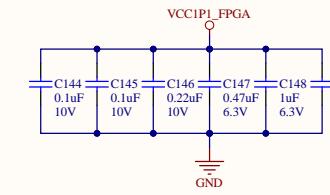
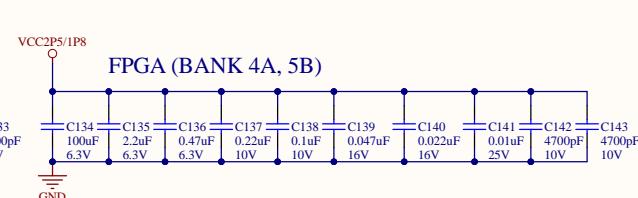
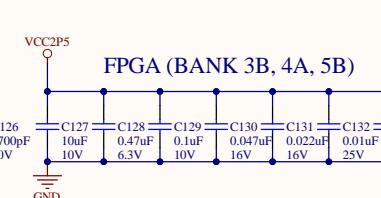
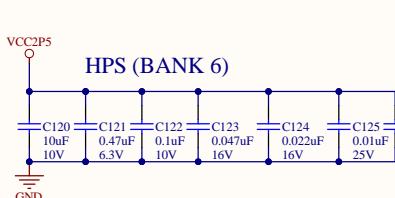
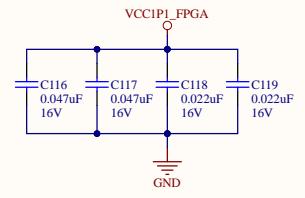
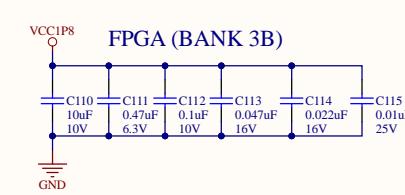
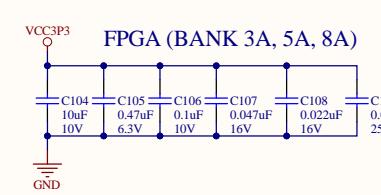
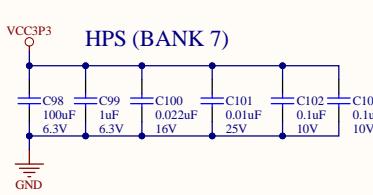
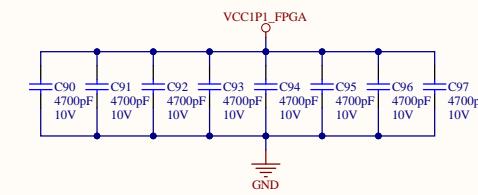
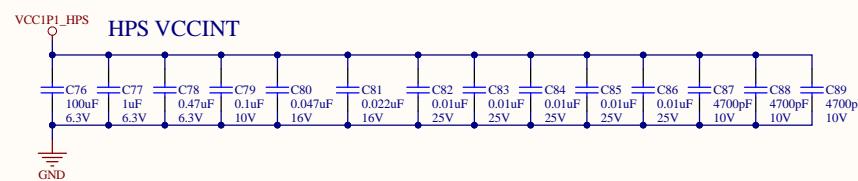
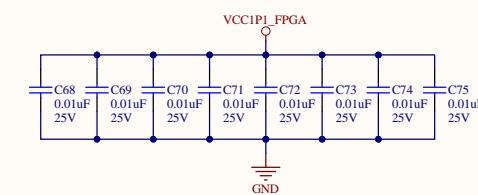
C

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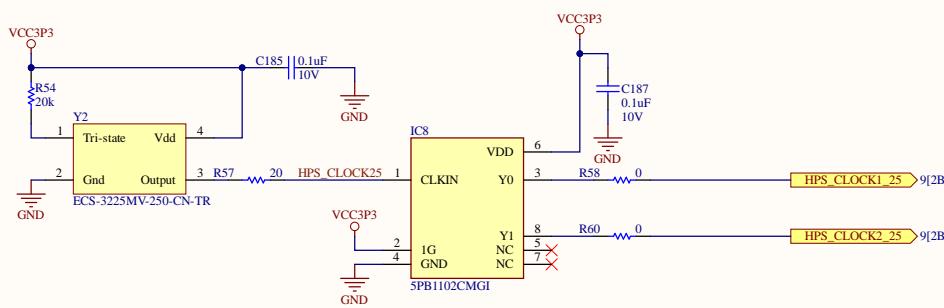
Approval H.Sato	File Name SoC_GND.SchDoc	Title Yonaguni
Inspection H.Sato	Date 2022/7/19	Size A3
Drawing Hiroyuki Tsuchiya	DWG No 1113-01-YG-302-001	Sheet 12 / 25 Rev 2.02

FPGA VCCINT

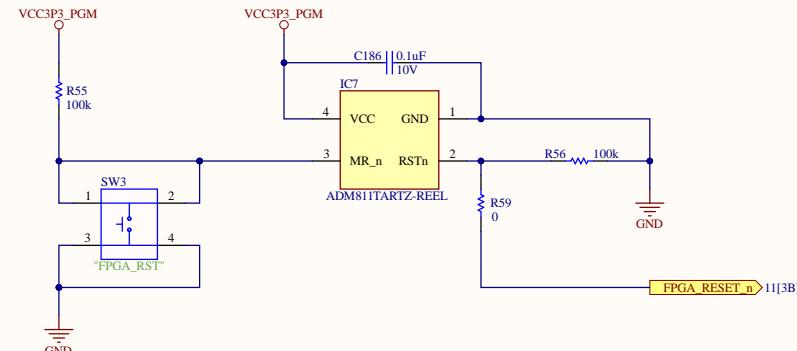
Approval	File Name	Title	Sheet
H.Sato	SoC_PassCon.SchDoc	Yonaguni	
Inspection	Date 2022/7/19	Size A3	
H.Sato			
Drawing			
Hirofumi Tsuchiya		DWG No 1113-01-YG-302-001	Rev 2.02

Clock / Reset

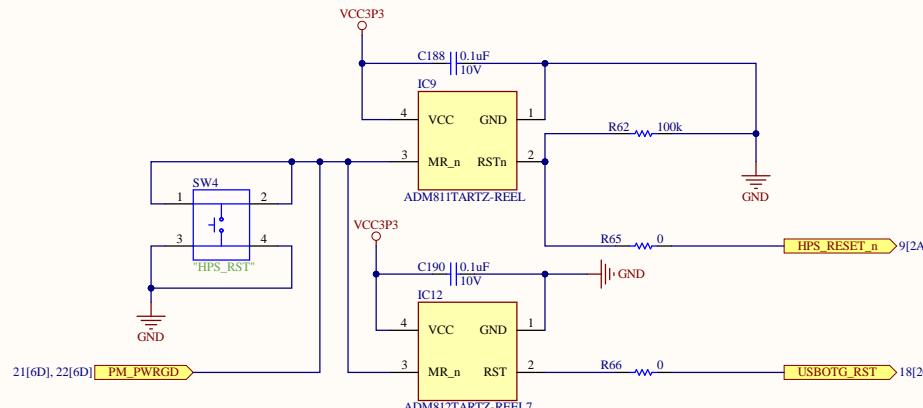
HPS Clock



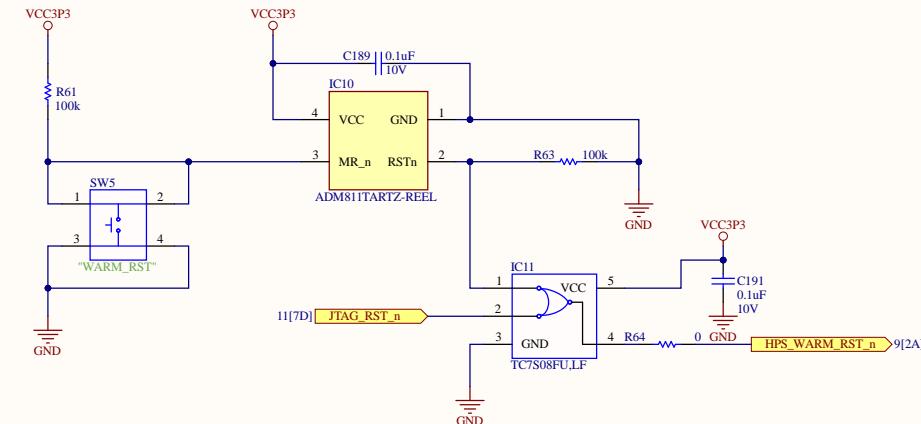
FPGA Reset



HPS Reset



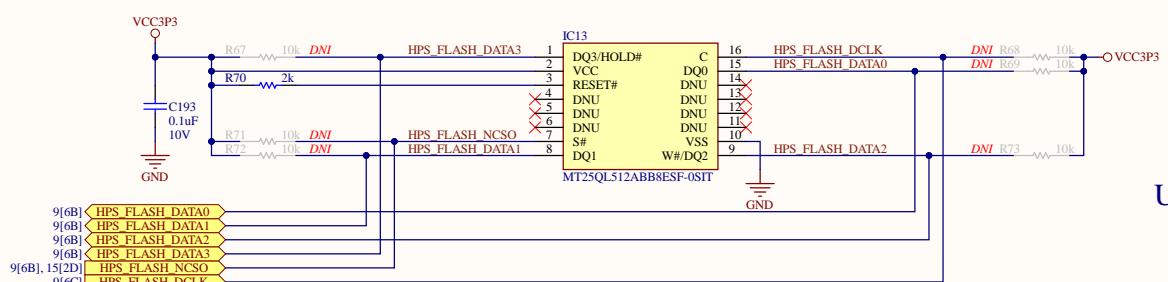
HPS Warm Reset



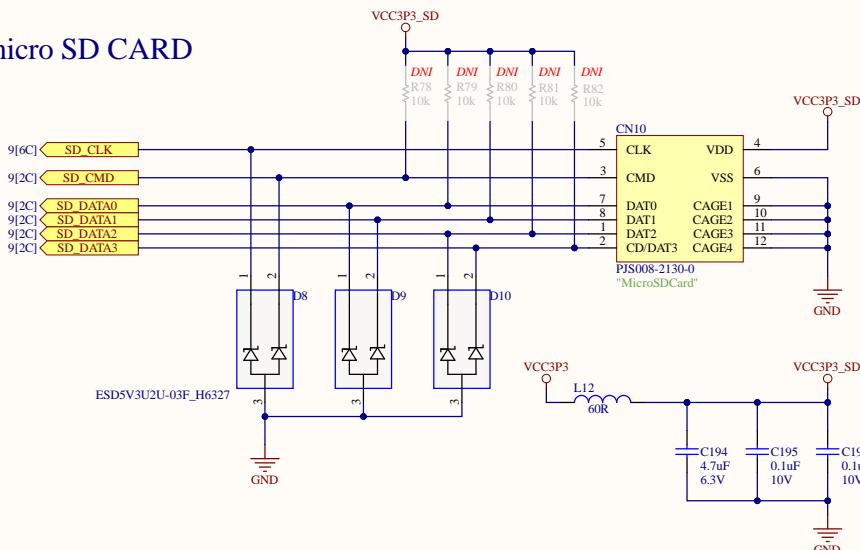
Approval	File Name	Title
H.Sato	Clk_Rst.SchDoc	Yonaguni
Inspection	Date 2022/7/19	Size A3
Drawing		Sheet 14 / 25
Hiroki Tsuchiya	Rev 2.02	DWG No 1113-01-YG-302-001



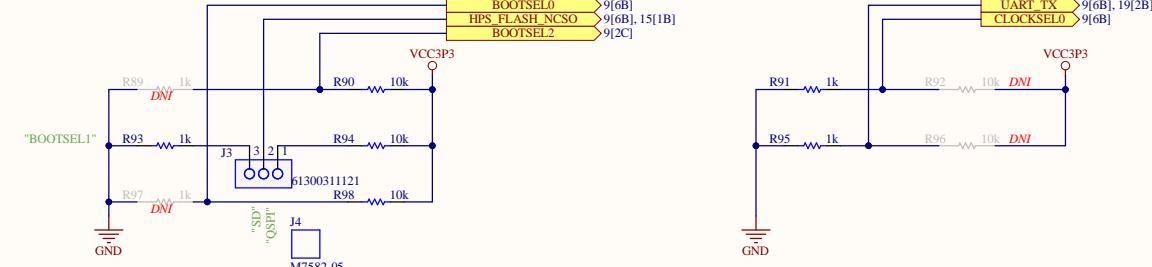
Quad SPI Flash



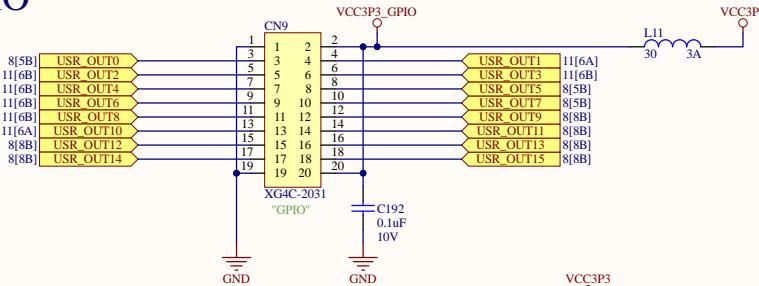
micro SD CARD



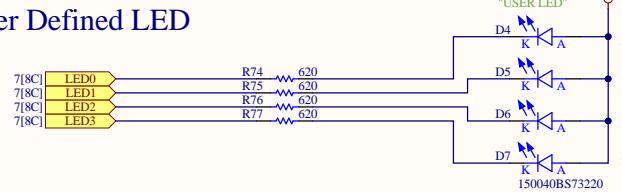
Boot Select



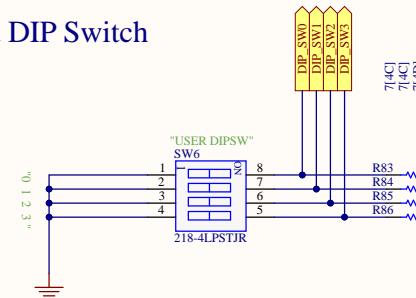
GPIO



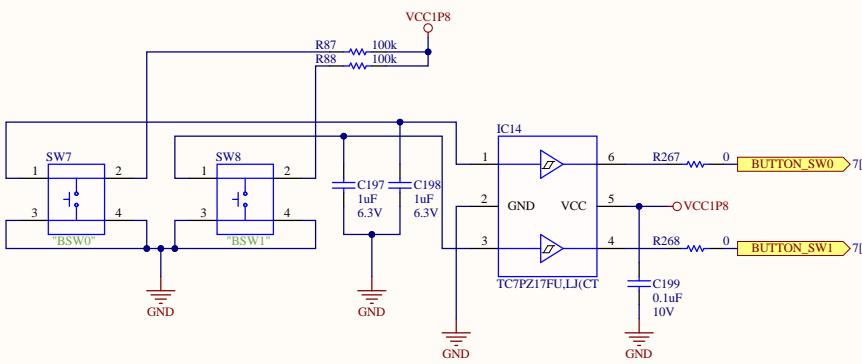
User Defined LED



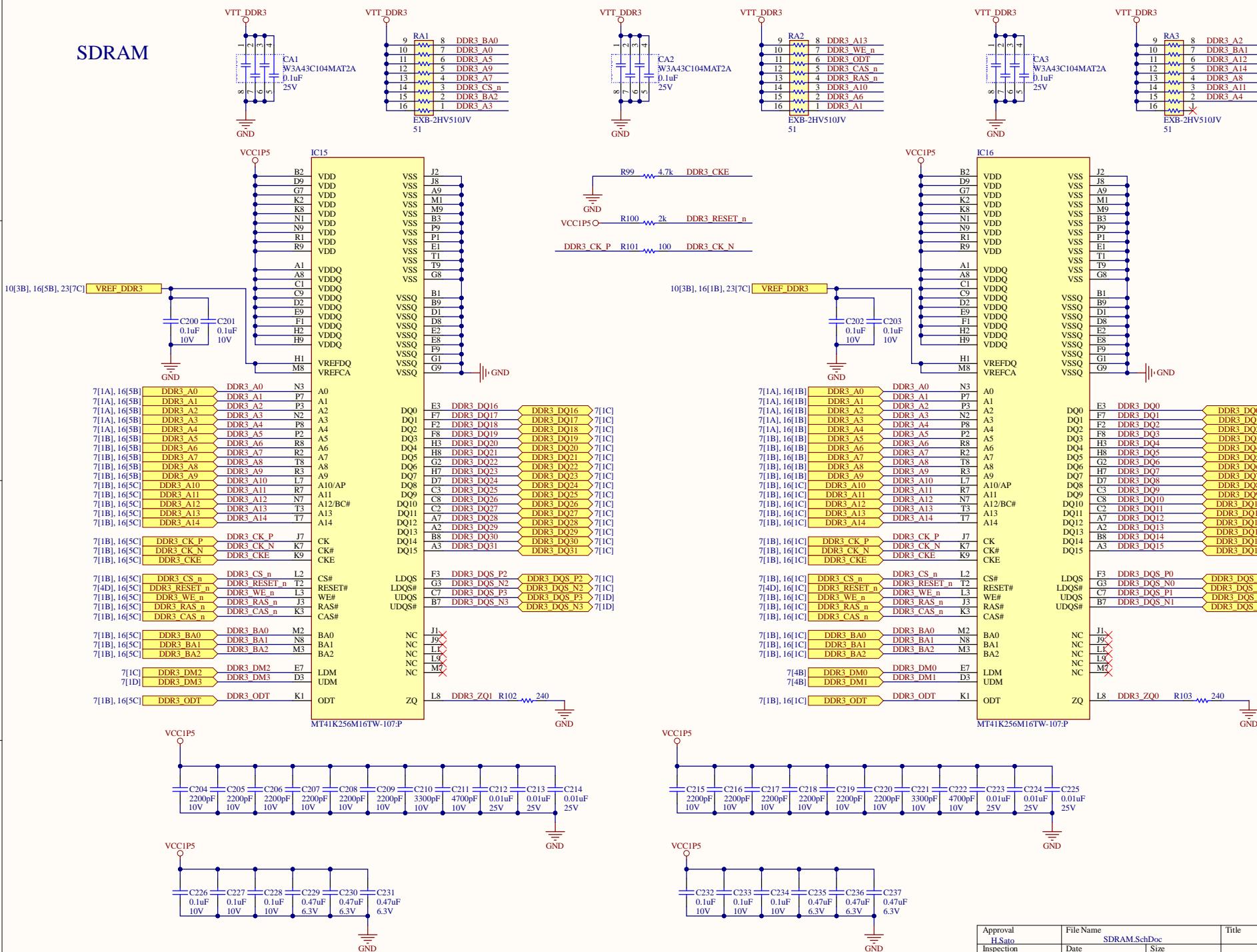
User Defined DIP Switch



User Defined Button Switch

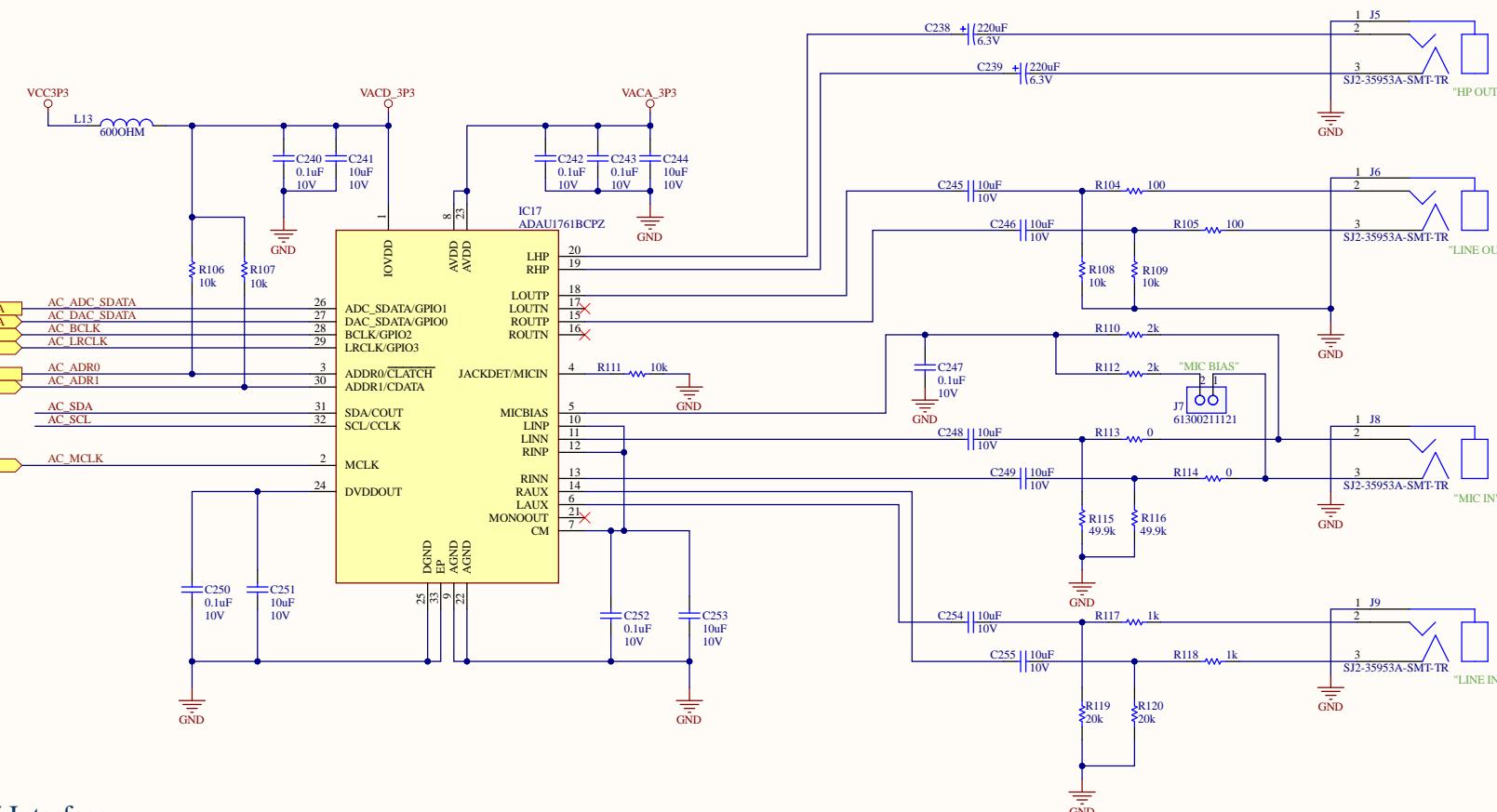


Approval H.Sato	File Name Boot_GPIO.SchDoc	Title Yonaguni	
Inspection H.Sato	Date 2022/7/19	Size A3	Sheet 15 / 25
Drawing Hiroyuki Tsuchiya	Marimo Electronics Co., Ltd.	DWG No 1113-01-YG-302-001	Rev 2.02

SDRAM

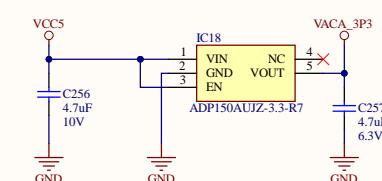
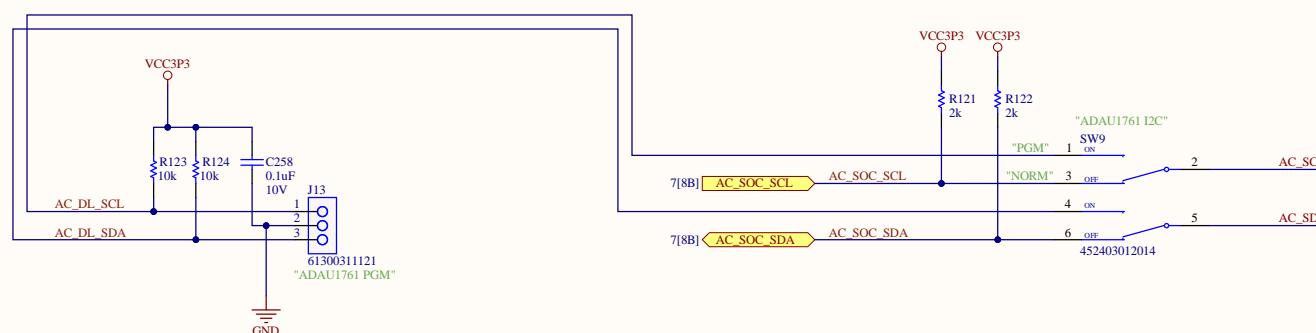
Approval H.Sato	File Name SDRAM.SchDoc	Title Yonaguni
Inspection H.Sato	Date 2022/7/19	Size A3
Drawing Hiroyuki Tsuchiya	DWG No 1113-01-YG-302-001	Rev 2.02
		Sheet 16 / 25

Audio Codec



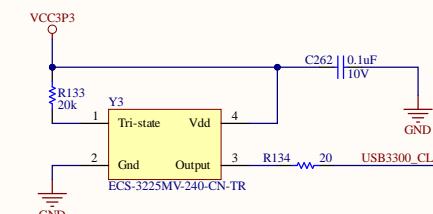
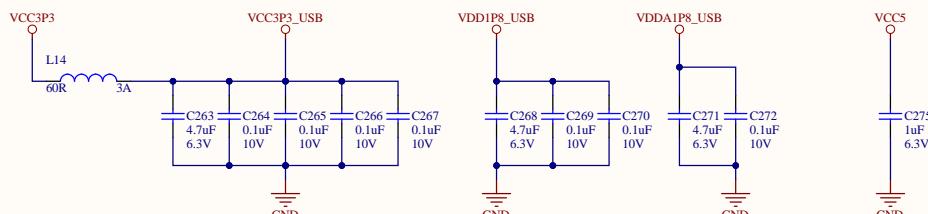
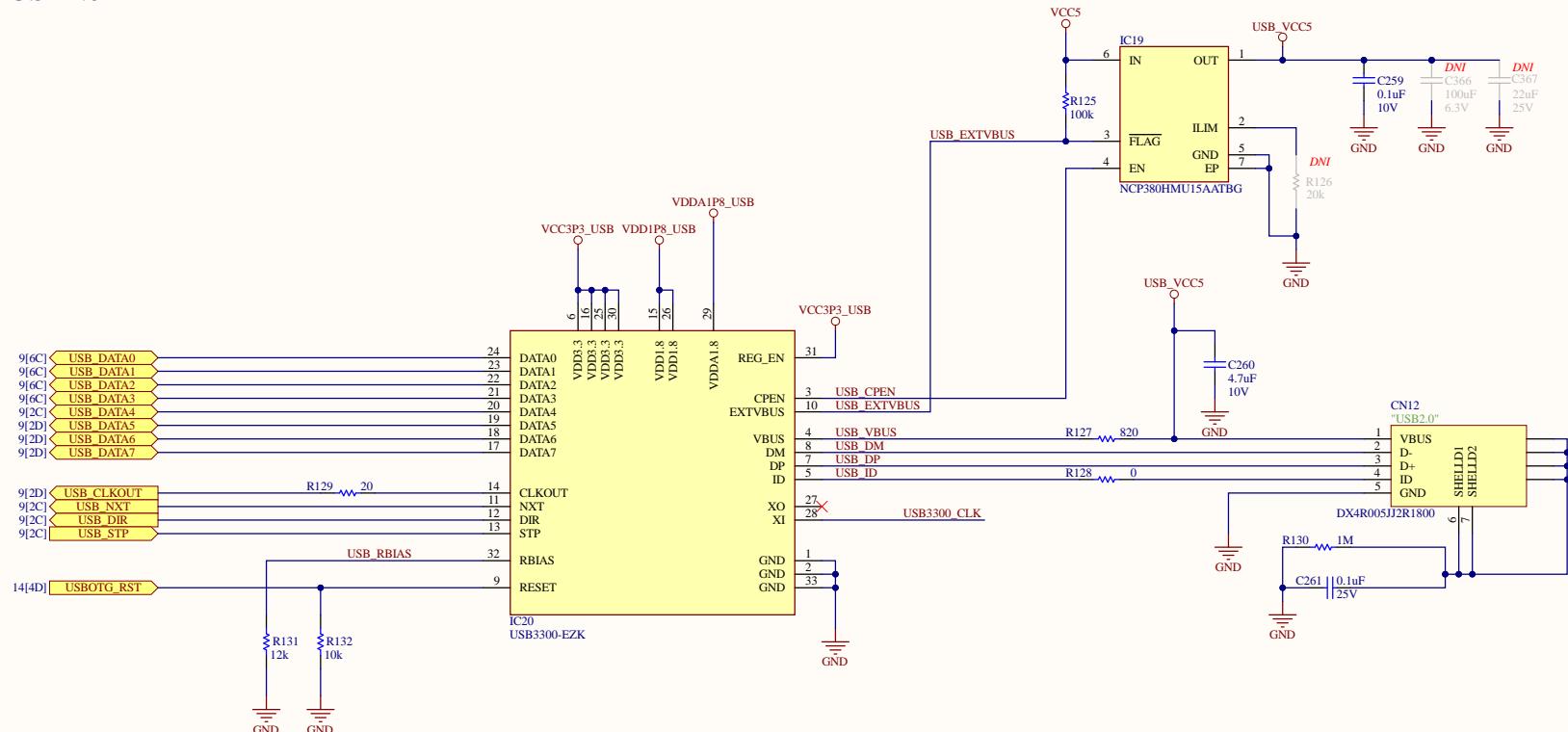
ADUSB2EBZ Interface

Audio CODEC 3.3V Power Supply



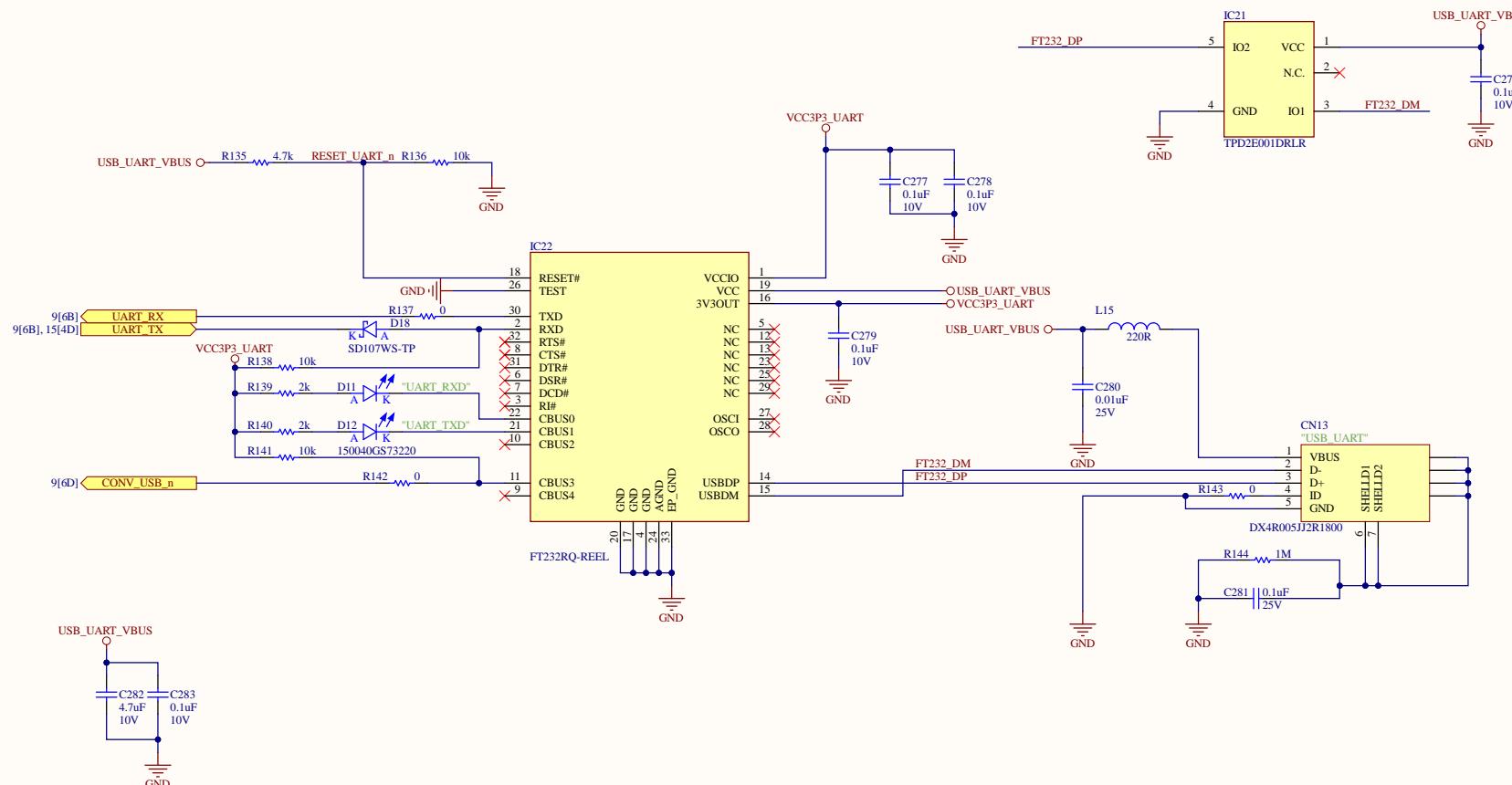
Approval Last Date	File Name	Title	Yonaguni	
Inspection	Date	Size		Sheet 17 / 25
H.Sato	2022/7/19	A3		
Drawing			DWG No	Rev
Hiroaki Tsuehira	Marimo Electronics Co. Ltd.		1113-01-YG-302-001	2.02

USB2.0



Approval <u>H.Sato</u>	File Name <u>USB2.0.SchDoc</u>	Title <u>Yonaguni</u>	
Inspection <u>H.Sato</u>	Date <u>2022/7/19</u>	Size <u>A3</u>	Sheet <u>18 / 25</u>
Drawing <u>Hiroyuki Tsuchiya</u>	Marimo Electronics Co.,Ltd.	DWG No <u>1113-01-YG-302-001</u>	Rev <u>2.02</u>

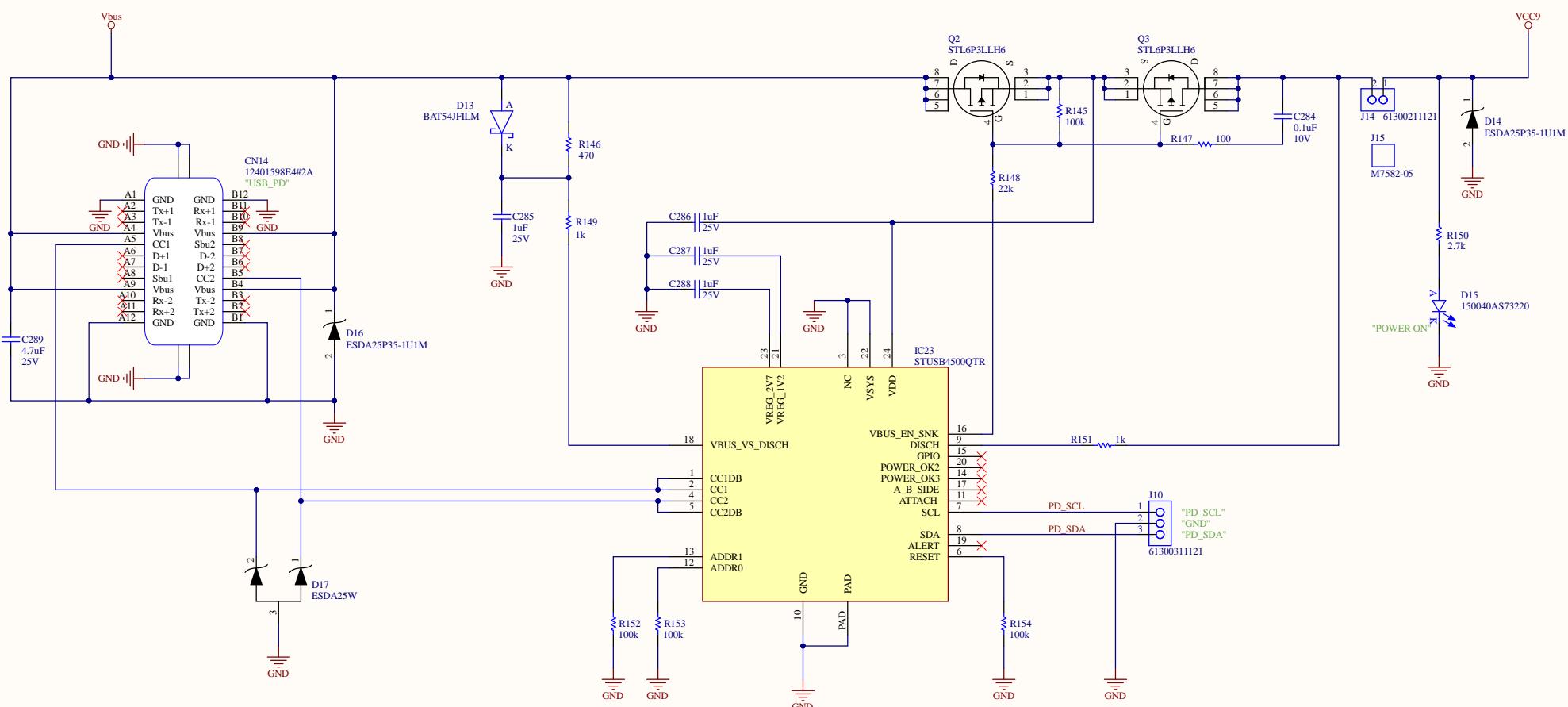
USB UART



Approval H.Sato	File Name USB_UART.SchDoc	Title Yonaguni
Inspection H.Sato	Date 2022/7/19	Size A3
Drawing Hiroyuki Tsuchiya	DWG No 1113-01-YG-302-001	Sheet 19 / 25 Rev 2.02

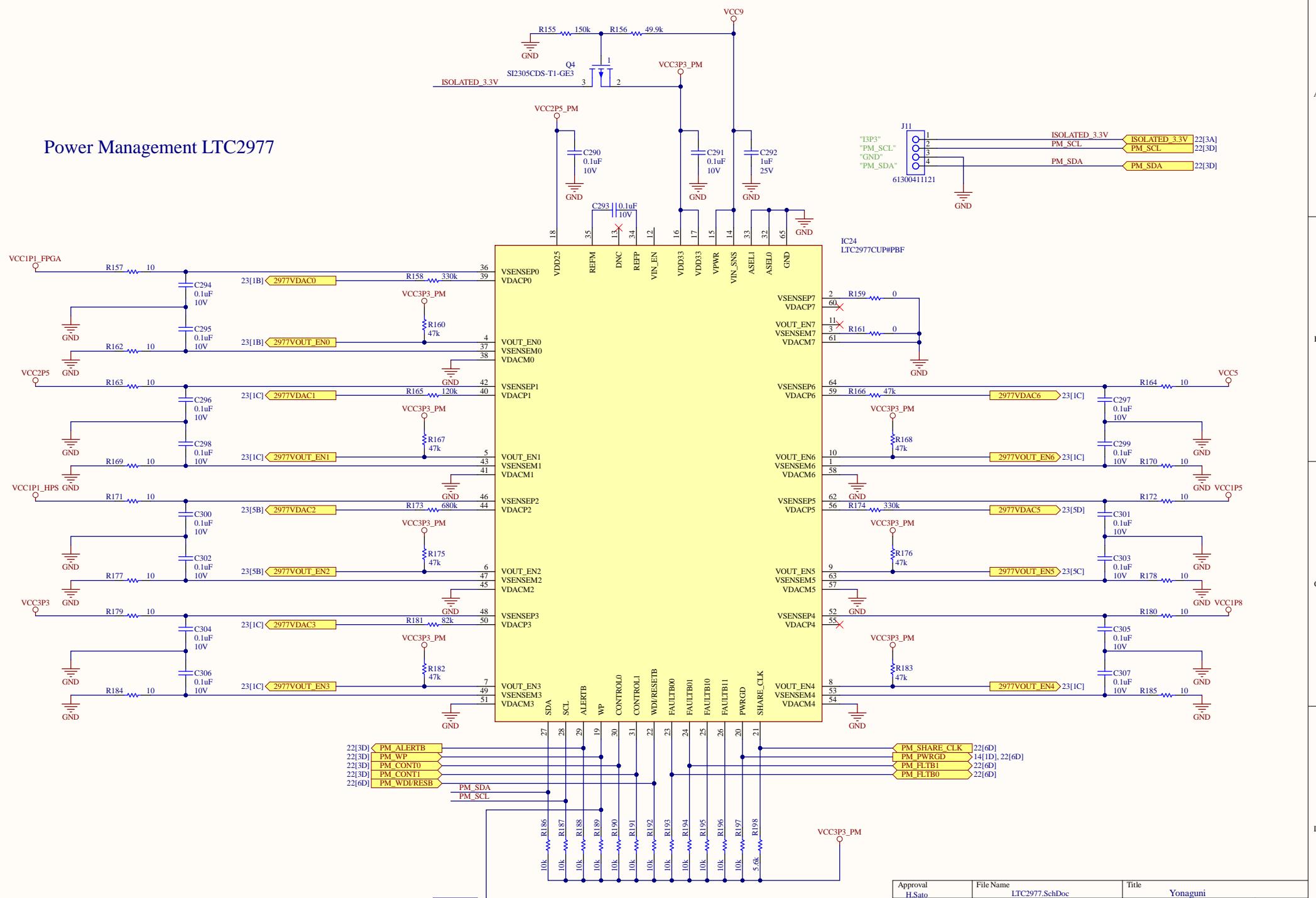
A

USB Power Delivery

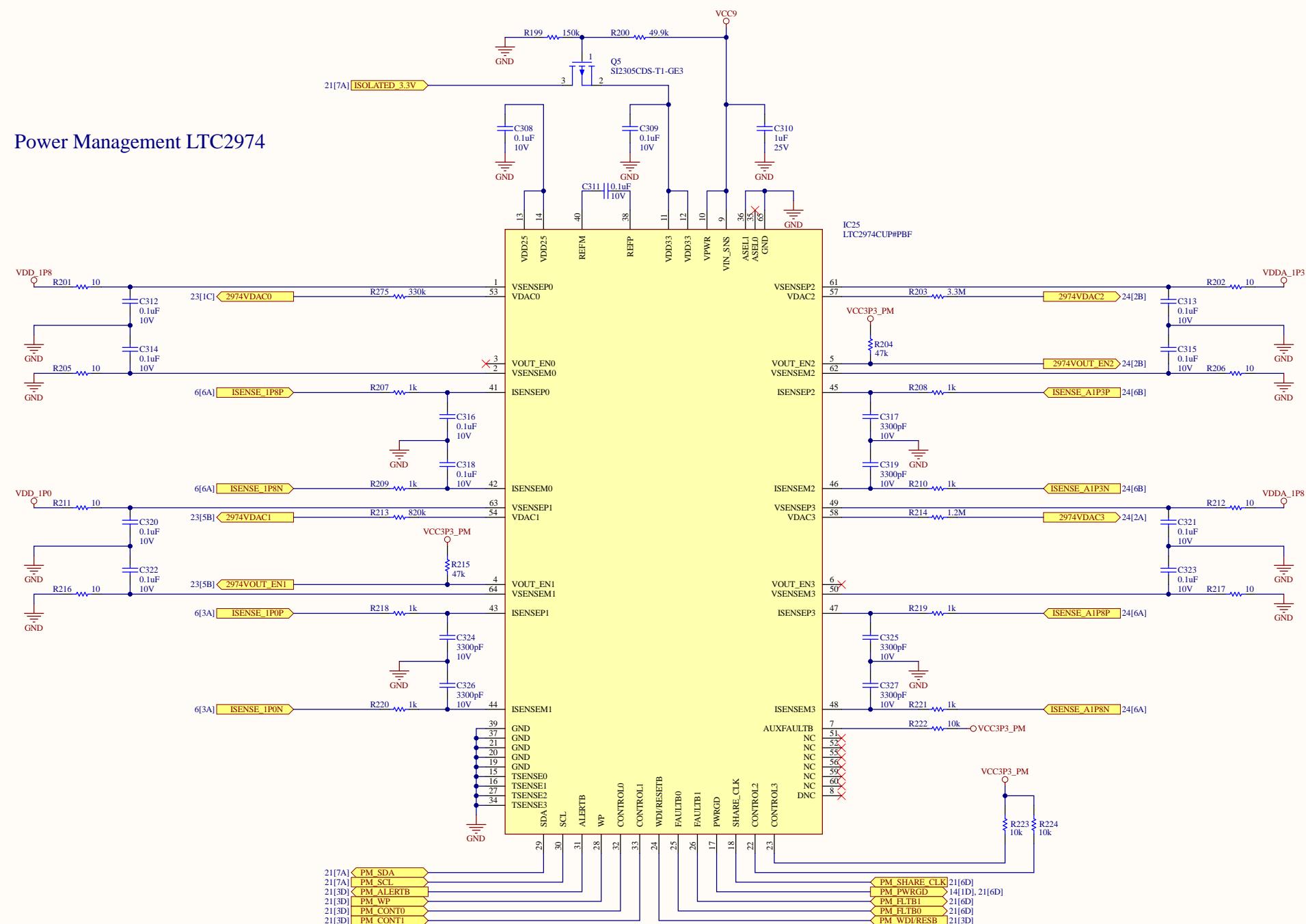


Approval	File Name	Title
H.Sato	USBPD.SchDoc	Yonaguni
Inspection	Date 2022/7/19	Size A3
Drawing	Sheet 20 / 25	Rev 2.02
Hirofumi Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001

Power Management LTC2977

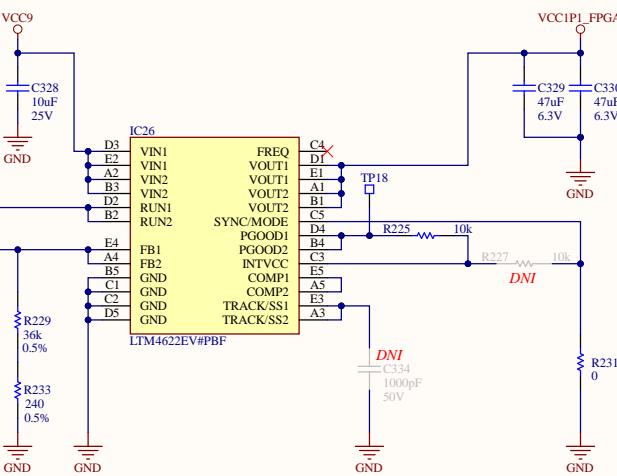


Power Management LTC2974

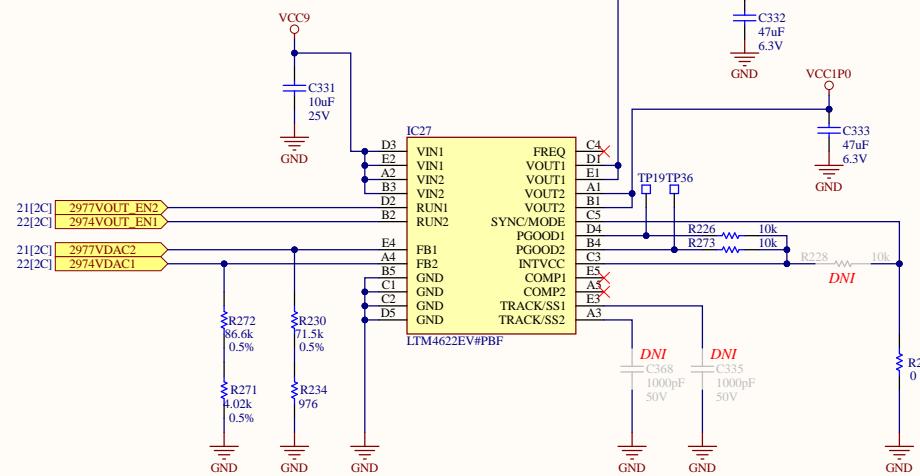


Approval	File Name	Title
H.Sato	LTC2974.SchDoc	Yonaguni
Inspection	Date 2022/7/19	Size A3
Drawing		Sheet 22 / 25
Hirokuya Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001
		Rev 2.02

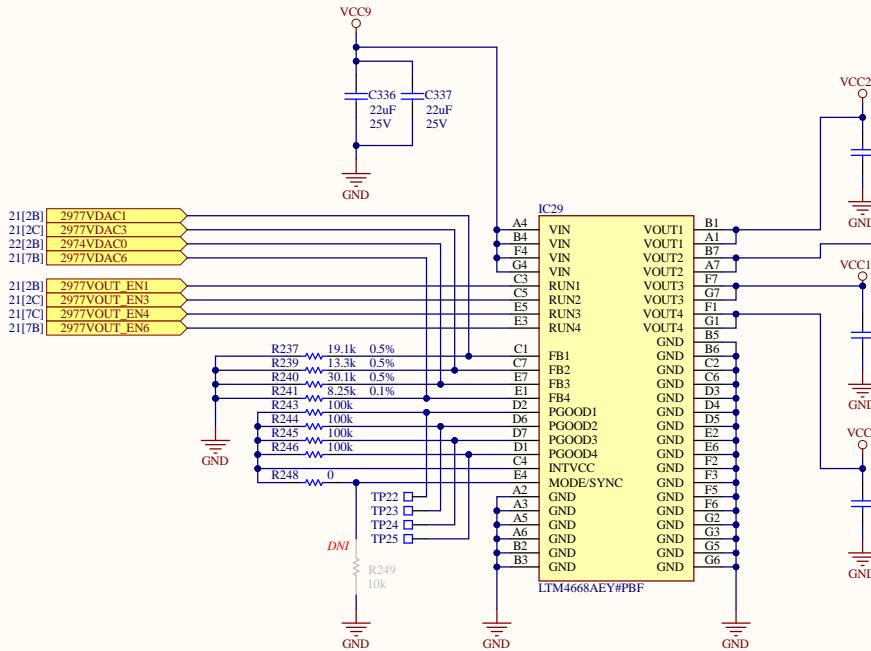
FPGA 1.1V Power Supply



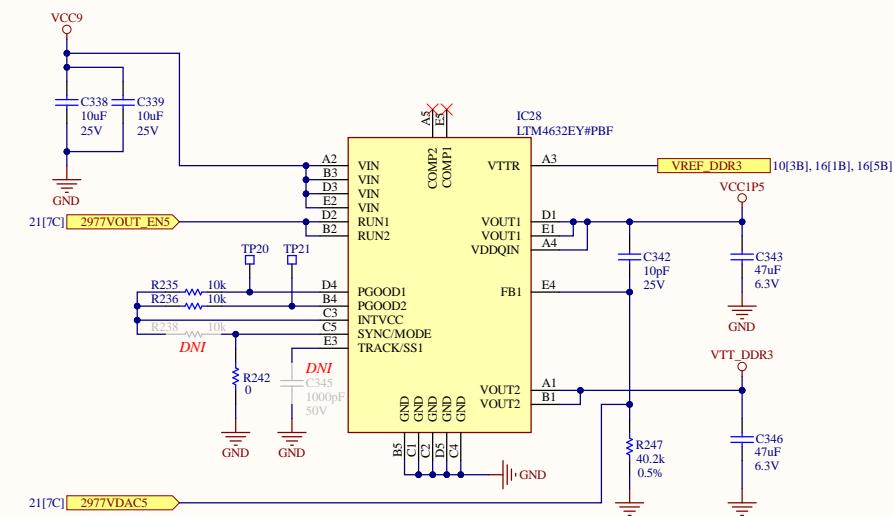
HPS 1.1V Power Supply



SoC/Audio CODEC/Navassa 5V, 3.3V, 2.5V, 1.8V Power Supply

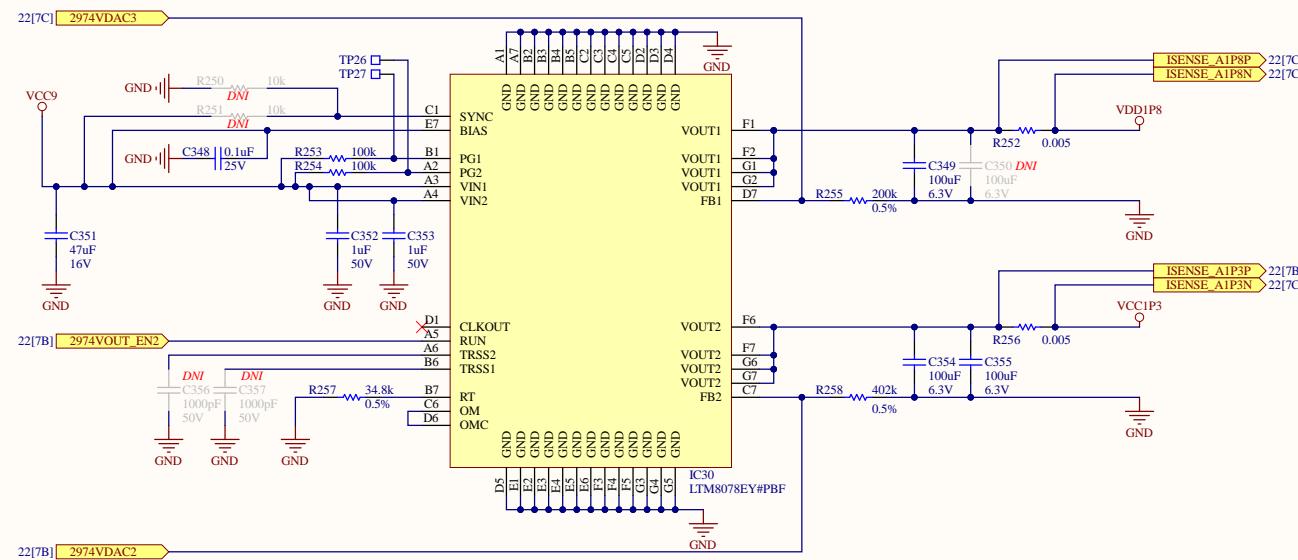


DDR3 1.5V Power Supply



Approval H.Sato	File Name Power1.SchDoc	Title Yonaguni
Inspection H.Sato	Date 2022/7/19	Size A3
Drawing Hiroyuki Tsuchiya	MARINO ELECTRONICS CO., LTD.	DWG No 1113-01-YG-302-001
Rev 2.02		

Navassa 1.8V, 1.3V Power Supply



Approval	File Name	Title
H.Sato	Power2.SchDoc	Yonaguni
Inspection	Date 2022/7/19	Size A3
Drawing	Rev 2.02	Sheet 24 / 25
Hirofumi Tsuchiya	Marimo Electronics Co.,Ltd.	DWG No 1113-01-YG-302-001

Missing Parts Number		Revision	History of revisions				
A	C54,C159,C161,C273,C274,C358,C359, C360,C361,C362,C363,C364,C365, R17,R18,R19,R20,R21,R259,R260, R261,R262,R263,R264,R265,R266, E11,E14,E15,TP8,TP9,TP10,TP11,TP28, CN11,L16,IC31		2.00	<ul style="list-style-type: none"> Release 2022/7/19 			A
	2.01	<ul style="list-style-type: none"> 2023/5/12 Sheet 18: R126 has been DNI Sheet 24: R257 changed from 40.2kΩ to 34.8kΩ Sheet 18, 19: Removed the "GND" for the 4 mounting pins of the CN12, CN13 				B	
	2.02	<ul style="list-style-type: none"> 2023/6/15 Sheet 20: Changed D15 Parts Discription "LED RED" to "LED AMBER" 				C	
B	C	D					D
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