



LogUp

LogUp is a protocol to prove the equality of two multisets.

$$\{1, 2, 3, 3\} \equiv \{3, 2, 1, 3\}$$

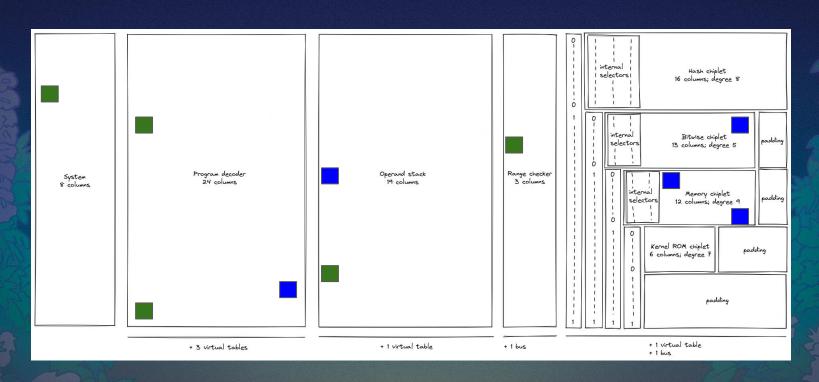
 $\{1, 2, 3, 3\} \neq \{1, 2, 3\}$

LogUp

Multiset checks are very useful in virtual machines:

- Enables practically unlimited stack depth
- Enables performing large numbers of range checks efficiently
- Communication between different parts of the trace

LogUp



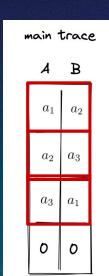
$$\sum_{i=1}^{n-1} \left(\sum_{j=1}^{k} \frac{m_{a_{ij}}}{(\alpha - a_{ij})} - \frac{m_{b_{ij}}}{(\alpha - b_{ij})} \right) = 0$$

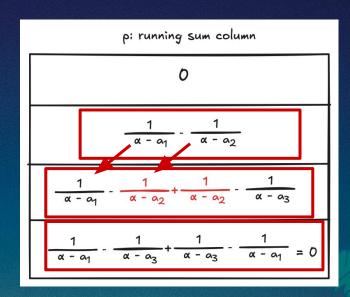
LogUp: the equation

- n is the trace length
- k is the number of elements per trace row
- m_{aij} and m_{bij} are the *multiplicities* {a_{ij}} is the first multiset
 {b_{ij}} is the second multiset



LogUp proved with STARKs





$$\sum_{i=1}^{n-1} \left(\sum_{j=1}^{k} \frac{m_{a_{ij}}}{(\alpha - a_{ij})} - \frac{m_{b_{ij}}}{(\alpha - b_{ij})} \right) = 0$$

LogUp proved with STARKs

p: running sum column

0

$$\frac{1}{\alpha - a_1} - \frac{1}{\alpha - a_2}$$

$$\frac{1}{\alpha - a_1} - \frac{1}{\alpha - a_2} + \frac{1}{\alpha - a_2} - \frac{1}{\alpha - a_3}$$

$$\frac{1}{\alpha - a_1} - \frac{1}{\alpha - a_3} + \frac{1}{\alpha - a_3} - \frac{1}{\alpha - a_1} =$$

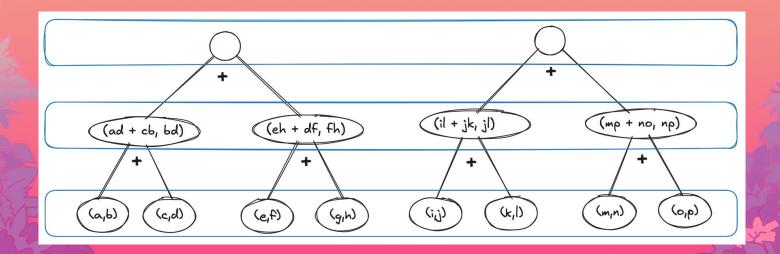
$$p[i+1] = p[i] + \sum_{j=1}^{k} \frac{m_{a_{ij}}}{(\alpha - a_{ij})} - \frac{m_{b_{ij}}}{(\alpha - b_{ij})}$$

Degree 3

$$p[i+1](\alpha - a_{i0})(\alpha - b_{i0}) = p[i](\alpha - a_{i0})(\alpha - b_{i0}) + m_{a_{i0}}(\alpha - b_{i0}) - m_{b_{i0}}(\alpha - a_{i0})$$



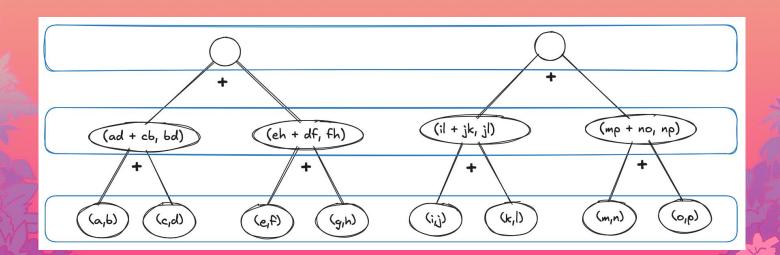
GKR is a protocol to prove the correct evaluation of a layered circuit

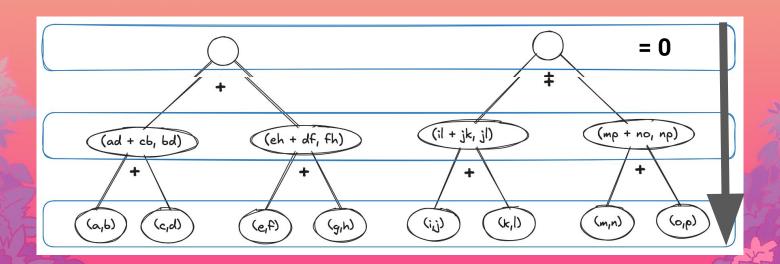


$$\frac{f \cdot h(a \cdot d + c \cdot b) + b \cdot d(e \cdot h + g \cdot f)}{b \cdot d \cdot f \cdot h}$$

$$\frac{a \cdot d + c \cdot b}{b \cdot d} + \frac{e \cdot h + g \cdot f}{f \cdot h}$$

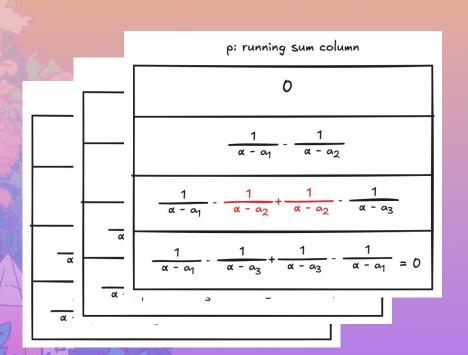
$$\frac{a}{b} + \frac{c}{d} + \frac{e}{f} + \frac{g}{h}$$

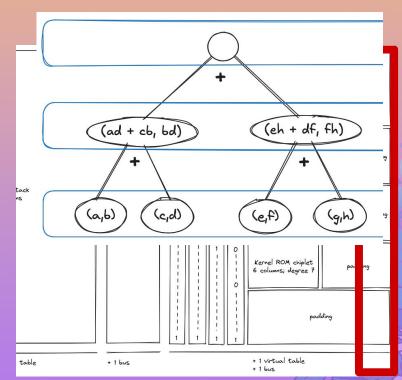




internal Hash chiplet 1 selectors 1 16 columns; degree 8 internal Bitwise chiplet selectors 13 columns; degree 5 Program decoder Operand stack Range checker System 24 columns 19 columns 3 columns 8 columns internal Memory chiplet 12 columns; degree 9 padding selectors Kernel ROM chiplet 6 columns; degree 7 padding + 1 virtual table + 3 virtual tables + 1 virtual table + 1 bus

STARKs vs GKR







	Small	Large
Hash function speed		
Degree of STARK constraints		
Size of the bus		

