JIEJIE MICROELECTRONICS CO., Ltd

BTA08/BTB08 Series 8A TRIACs

DESCRIPTION:

High current density due to double mesa technology; SIPOS and Glass Passivation.

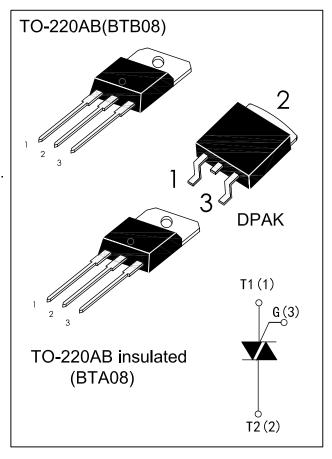
BTA08/BTB08 series triacs is suitable for general purpose AC switching. They can be used as an ON/OFF Function in applications such as static relays, heating regulation, induction motor stating circuits... or for phase contol operation light dimmers, motorspeed controllers.

BTA08/BTB08- \times \times SW, - \times \times CW, - \times \times BW are 3 Quadrants triacs, They are specially recommended for use on inductive loads.

BTA08 are isolated internally, they provides a 2500V RMS isolation voltage from all three terminals to external heetsink.

MAIN FEATURES

Symbol	Value	Unit
IT(RMS)	8	Α
VDRM/VRRM	600and800	V
Vтм	≤1.55	V



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit	
Storage junction temperature range Operrating junction temperature rang	Tstg Tj	-40 to +150 -40 to +125	°C	
Repetitive Peak Off-state Voltage Repetitive Peak Reverse Voltage	Tj=25°C	Vdrm Vrrm	600and800 600and800	V
Non repetitive Surge Peak Off-state Non repetitive Peak Reverse Voltage	Vdsm Vrsm	700and900 700and900	V	
RMS on-state current (full sine wave)				
Non repetitive surge peak on-state current (full cycle,Tj=25°C)	. •			
I ² t Value for fusing tp=10ms	I ² t	36	A ² s	
Critical rate of rise of on-state current IG=2×IGT, tr≤100 ns, f=120Hz, Tj=12	dl /dt	50	A/us	
Peak gate current tp=20us,	IGМ	4	Α	
Average gate power dissipation	Tj=125°C	PG(AV)	1	W



ELECTRICAL CHARACTERISTICS(Tj=25℃ unless otherwise specified)

3 Quadrants

Symbol	Test Condition	Quadrant		BTA08/BTB08		Unit	
				SW	CW	BW	
lgī	V= 10V B: 200	1 - 11 - 111	MAX.	10	35	50	mA
VGT	VD=12V RL=30Ω	- -	MAX.		1.3		٧
VGD	VD=VDRM RL=3.3KΩ Tj=125℃	- -	MIN		0.2		V
IL	IG=1.2IGT	1 - 111	MAX.	25	50	70	,nn A
		II		30	60	80	mA
Iн	IT=100mA		MAX.	15	35	50	mA
dV/dt	VD=67%VDRM gate open Tj=125℃		MIN.	40	500	1000	V/µs
(dl/dt)c	(dV/dt) c=0.1V/µs Tj=125℃		MIN.	5.4			A/ms
	(dV/dt) c=10V/µs Tj=125℃			2.8			
	Without snubber Tj=125°	С			4.5	7.0	

4 Quadrants

Symbol	Test Condition	Quadrant		BTA08/	Unit	
				С	В	
IGT		1 - -	MAX.	25	50	mA
	V5=10V B:=200	IV		50	100	
VGT	VD=12V RL=30Ω	ALL	MAX.	1.	.3	\ \
VGD	VD=VDRM RL=3.3KΩ Tj=125℃	ALL	MIN.	0	.2	V
IL	IG=1.2IGT	I -III-IV	MAX.	40	50	mA
		II		80	100	
lн	τ=100mA		MAX.	25	50	mA
dV/dt	VD=67%VDRM gate ope Tj=125℃	MIN.	200	400	V/µs	
(dl/dt)c	(dV/dt) c=0.1V/µs Tj=12	MIN.			A/ms	
	(dV/dt) c=10V/µs Tj=12					
	Without snubber Tj=125°	C				



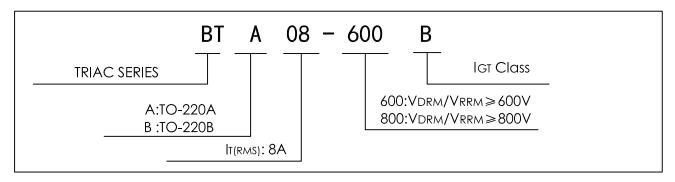
STATIC CHARACTERISTICS

Symbol	Test Conditions	Value (MAX)	Unit	
VTM	ITM=11A, tp=380uS	Tj=25℃	1.55	٧
IDRM	VD=VDRM	Tj=25℃	5	uA
Irrm	VR=VRRM	Tj=125℃	1	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit	
Rth(j-c)	Junction to case (AC)	DPAK/TO-220AB	1.6	℃/W
		TO-220AB Insulated	2.5	

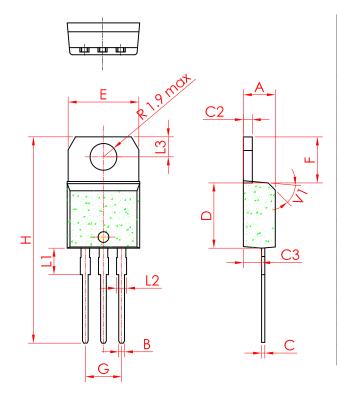
ORDERING INFORMATION





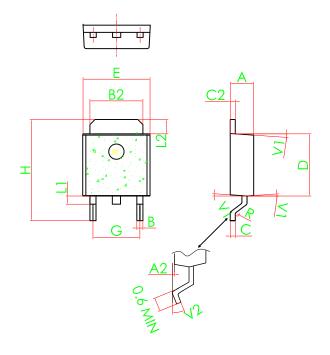
PACKAGE MECHANICAL DATA

TO-220AB



	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Мах.
Α	4.4		4.6	0.173		1.181
В	0.61		0.88	0.024		0.034
С	0.49		0.70	0.019		0.027
C2	1.23		1.32	0.048		0.051
C3	2.4		2.72	0.094		0.107
D	8.6		9.7	0.338		0.382
Е	10		10.4	0.393		0.409
F	6.2		6.6	0.244		0.259
G	4.8		5.4	0.189		0.213
Н	28.0		29.8	11.0		11.7
L1		3.75			0.147	
L2	1.14		1.7	0.044		0.066
L3	2.65		2.95	0.104		0.116
V1		40°			40°	

DPAK



	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Мах.	Min.	Тур.	Мах.
Α	2.2		2.4	0.086		0.095
A2	0.03		0.23	0.001		0.009
В	0.55		0.65	0.021		0.026
B2	5.2		5.4	0.204		0.212
С	0.45		0.62	0.017		0.024
C2	0.48		0.62	0.019		0.024
D	6		6.2	0.236		0.244
Е	6.4		6.6	0.251		0.259
G	4.40		4.60	0.173		0.181
Н	9.35		10.1	0.368		0.397
L1		0.8			0.031	
L2	1.37		1.5	0.054		0.059
V1		4°			4°	
V2	0°		8°	0°		8°

FIG.1: Maximum power dissipation versus RMS on-state current(full cycle)

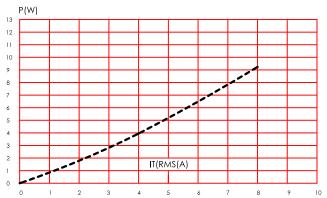


FIG.3: On-state characteristics (maximum values)

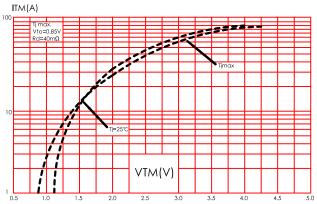


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponding value of lt

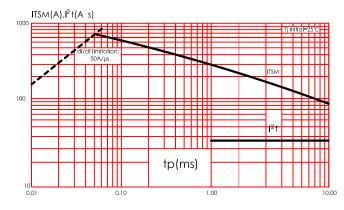


FIG.2: RMS on-state current versus case temperature(full cycle)

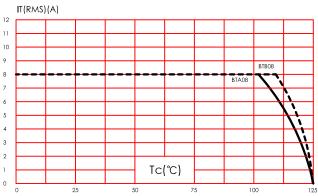


FIG.4: Surge peak on-state current versus number of cycles.

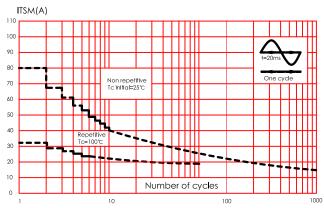


FIG.6: Relative variation of gate trigger current, holding current and latching current versus junction temperature (typical values).

