Chronologic VCS simulator copyright 1991-2018 Contains Synopsys proprietary information. Compiler version O-2018.09-SP2-3_Full64; Runtime version O-2018.09-SP2-3_Full64; Sep 2 3 19:10 2020 VCD+ Writer 0-2018.09-SP2-3_Full64 Copyright (c) 1991-2018 by Synopsys Inc. 0Count Q = 0000 Clear = 134Count Q = 0000 Clear = 0 40Count Q = 0001 Clear = 060Count Q = 0010 Clear = 080Count Q = 0011 Clear = 0100Count Q = 0100 Clear = 0120Count Q = 0101 Clear = 0140Count Q = 0110 Clear = 0160Count Q = 0111 Clear = 0180Count Q = 1000 Clear = 0\$finish called from file "counter_fixture.v", line 36. \$finish at simulation time 200 VCS Simulation Report Time: 200 CPU Time: 0.330 seconds; Data structure size: 0.0Mb Wed Sep 23 19:10:15 2020

1