projeto_ex1_caal

```
Spec = accept.Loop_s0;
Loop_s0 = 'send0.Loop_s0 + 'send0.'receive0.deliver.Loop_ack0;
Loop_ack0 = 'snd_ack0.Loop_ack0 + 'snd_ack0.'rcv_ack0.accept.Loop_s1;
Loop_s1 = 'send1.Loop_s1 + 'send1.'receive1.deliver.Loop_ack1;
Loop_ack1 = 'snd_ack1.Loop_ack1 + 'snd_ack1.'rcv_ack1.accept.Loop_s0;

Spec2 = accept.deliver.Spec2;

Sender0 = accept.'send0.Loop0;
Loop0 = rcv_ack0.Sender1 + 'send0.Loop0 + rcv_ack1.Loop0;

Sender1 = accept.'send1.Loop1;
Loop1 = rcv_ack1.Sender0 + 'send1.Loop1 + rcv_ack0.Loop1;


Receiver0 = receive0.deliver.'snd_ack0.AckLoop0;
AckLoop0 = 'snd_ack0.AckLoop0 + Receiver1 + receive0.AckLoop0;

Receiver1 = receive1.deliver.'snd_ack1.AckLoop1;
AckLoop1 = 'snd_ack1.AckLoop1 + Receiver0 + receive1.AckLoop1;


Trans = Send0 + Send1;
Send0 = send0.('receive0.Trans + Trans + 'receive0.'receive0.Trans);
Send1 = send1.('receive1.Trans + Trans + 'receive1.'receive1.Trans);


Ack = Ack0 + Ack1;
Ack0 = snd_ack0.('rcv_ack0.Ack + Ack + 'rcv_ack0.'rcv_ack0.Ack);
Ack1 = snd_ack1.('rcv_ack1.Ack + Ack + 'rcv_ack1.'rcv_ack1.Ack);

P = (Sender0 | Trans | Receiver0 | Ack ) \ {send0, send1, rcv_ack0,
rcv_ack1, snd_ack0, snd_ack1, receive0, receive1};
```

Unsatisfied	25 ms	P ~ Spec
Unsatisfied	26 ms	P ≈ Spec
Unsatisfied	25 ms	P ~ Spec2
Satisfied	50 ms	P ≈ Spec2